

Ultra-Low R_{ON} with Slew Rate Controlled Load Switch

General Description

The ET35621 is a small, ultra-low R_{ON} load switch with controlled turn on. The device contains a low R_{ON} N-Channel MOSFET that can operate over an input voltage range of 1.0 V to 5.5 V and switch currents of up to 3A. An integrated charge pump biases the NMOS switch in order to achieve a low switch ON-Resistance.

The switch is controlled by an on/off input (ON), which is capable of interfacing directly with low-voltage GPIO control signals. An internal reverse voltage comparator disables the power-switch when the output voltage (V_{OUT}) is driven higher than the input voltage (V_{IN}) by V_{RCP} , to quickly stop the flow of current towards the input side of the switch. Reverse current protection is always active, even when the power-switch is disable.

The ET35621 contains a 40 Ω on-chip load resistor for quick output discharge when the device is turn off.

The ET35621 is available in a small, space-saving WLCSP4 package and is characterized for operation over the free air temperature range of -40°C to 85°C.

Features

- Input Voltage Range: 1.0V to 5.5V
- Ultra-Low On-Resistance
 - $R_{ON} = 35m\Omega$ @ $V_{IN} = 5.5V$
 - $R_{ON} = 36m\Omega$ @ $V_{IN} = 3.3V$
 - $R_{ON} = 37m\Omega$ @ $V_{IN} = 1.8V$
 - $R_{ON} = 49m\Omega$ @ $V_{IN} = 1.0V$
- 3A Maximum Continuous Switch Current
- Low Threshold 1.2V GPIO Control Input
- Controlled Slew Rate to Avoid Inrush Current
- ESD Protection Complies with JESD22 Standard
 - HBM: $\pm 4000V$ Pass (JEDEC JS-001)
 - CDM: $\pm 2000V$ Pass (JEDEC JS-002)
- Part No. and Package

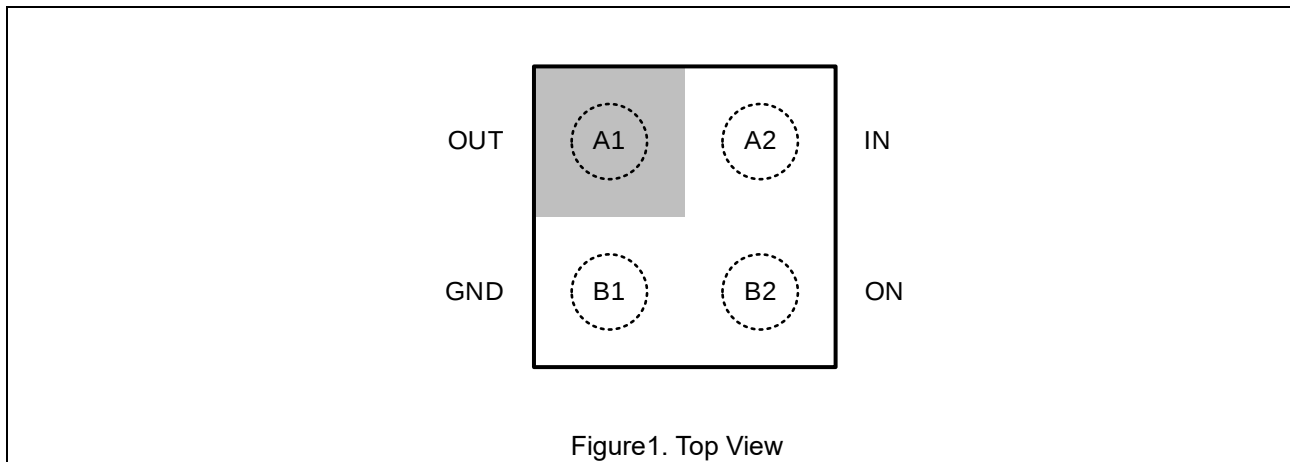
Part No.	Package	Packing Option	MSL
ET35621	WLCSP4 (0.5mm pitch)	Tape and Reel, 3K/Reel	1

Applications

- Smartphone
- Notebook Computer and Ultra-book
- Solid State Drive (SSD)
- DTV/IP Set Top Box
- POS Terminal and Media Gateway

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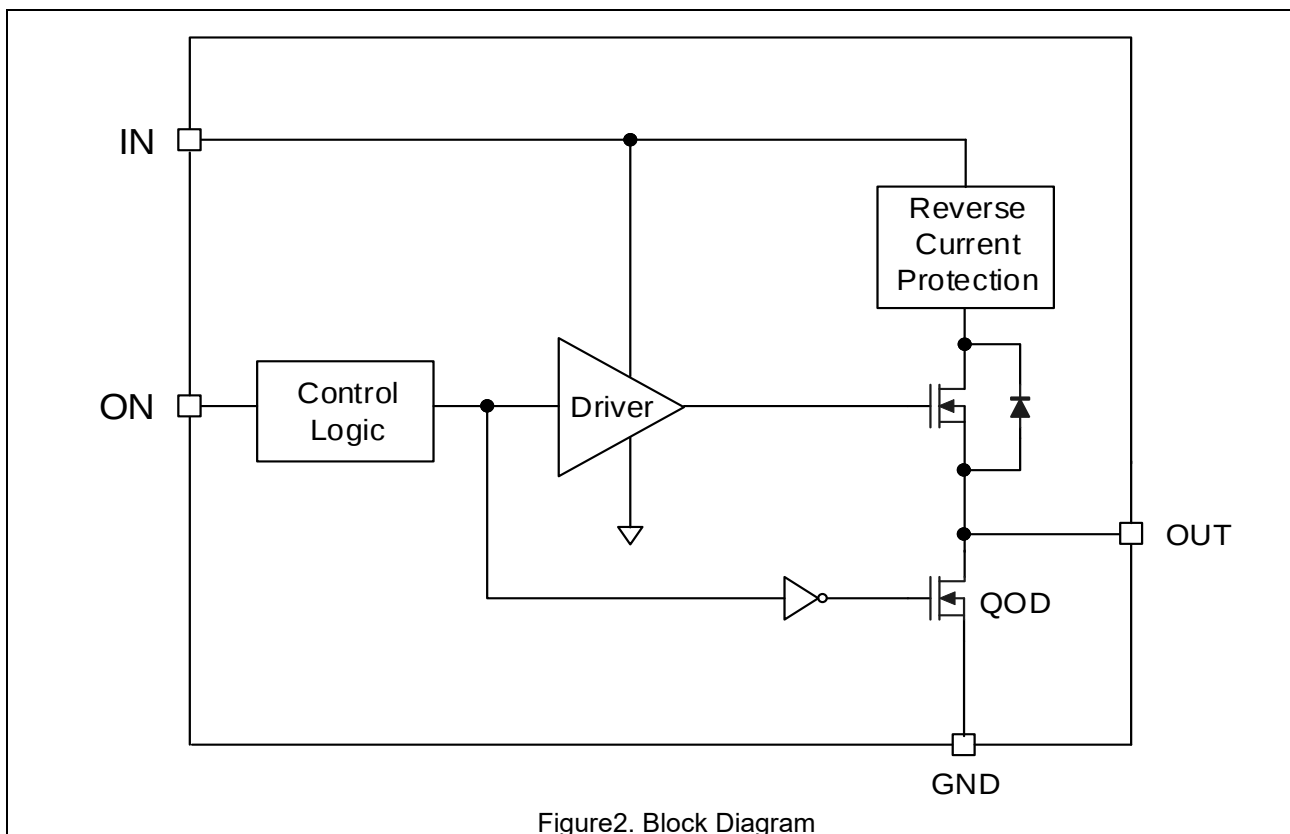
Pin Configuration



Pin Function

Pin	Name	Description
A1	OUT	Switch Output
A2	IN	Supply Input: Input to the Power Switch
B1	GND	Ground
B2	ON	ON/OFF Control, Active HIGH Compatible

Block Diagram



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Functional Description

ON/OFF Control

The ON pin controls the switch. The ON pin is compatible with standard GPIO logic threshold. It can be used with any micro-controller with 1.2V, 1.8V, 2.5V, 3.3V or 5.5V GPIO.

Table 1. Function Table

ON Pin State	V_{IN} to V_{OUT}
L	Off
H	On

Reverse Current Protection

The device monitors V_{IN} and V_{OUT} voltage levels. When the reverse current voltage threshold (VRCP) is exceeded, the switch is disabled, so as to prevent any reverse current flow to V_{IN} . This feature is particularly useful when the outputs of ET35621 need to be driven by another voltage source.

Application Information

Input Capacitor

It is recommended to place a capacitor (C_{IN}) between V_{IN} and GND pins of ET35621. This capacitor helps to limit the voltage drop on the input voltage supply when the switch turns ON into a discharged load capacitor. A 1 μ F ceramic capacitor that is placed close to the IC pins is usually sufficient. Higher values of C_{IN} can be used to further reduce the voltage drop in high current applications.

Output Capacitor

It is recommended to place a capacitor (C_{OUT}) between OUT and GND pins of ET35621. This capacitor acts as a low pass filter along with the switch ON-resistance to remove any voltage glitches coming from the input voltage source. It is generally recommended to have C_{IN} greater than C_{OUT} so that once the switch is turned ON, C_{OUT} can charge up to V_{IN} without V_{IN} dropping significantly. A 0.1 μ F ceramic capacitor that is placed close to the IC pins is usually sufficient.

Standby Power Reduction

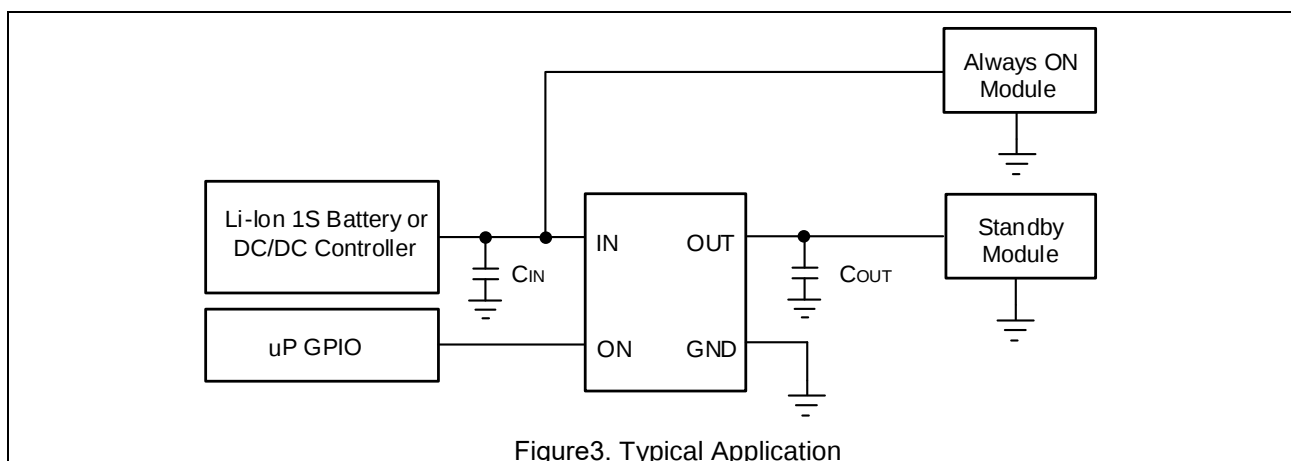


Figure3. Typical Application

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Any end equipment that is being powered from the battery has a need to reduce current consumption in order to keep the battery charged for a longer time. ET35621 helps to accomplish this by turning off the supply to the modules that are in standby state and hence significantly reduces the leakage current overhead of the standby modules, shows in [Figure3](#).

Power Supply Sequencing Without a GPIO Input

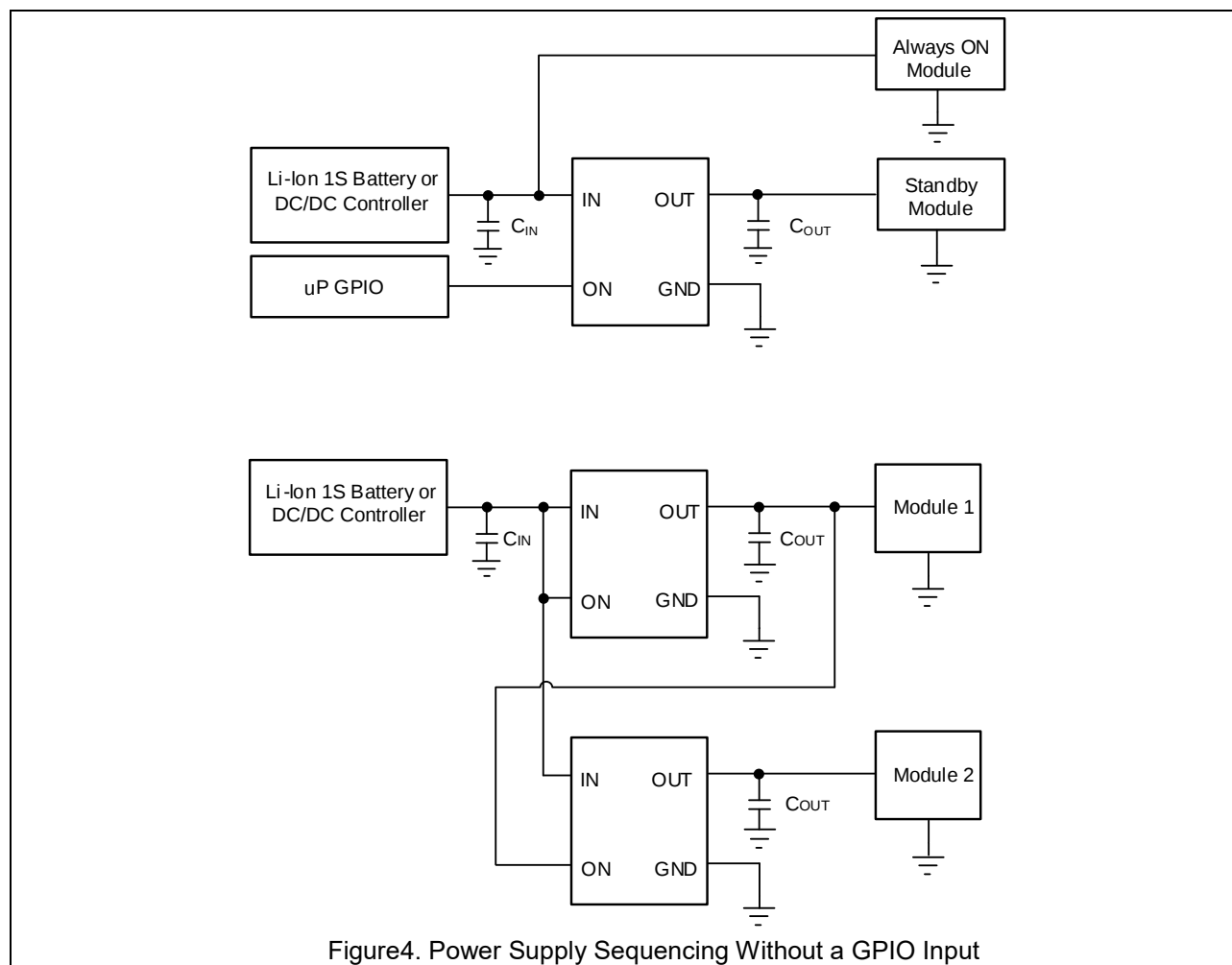


Figure4. Power Supply Sequencing Without a GPIO Input

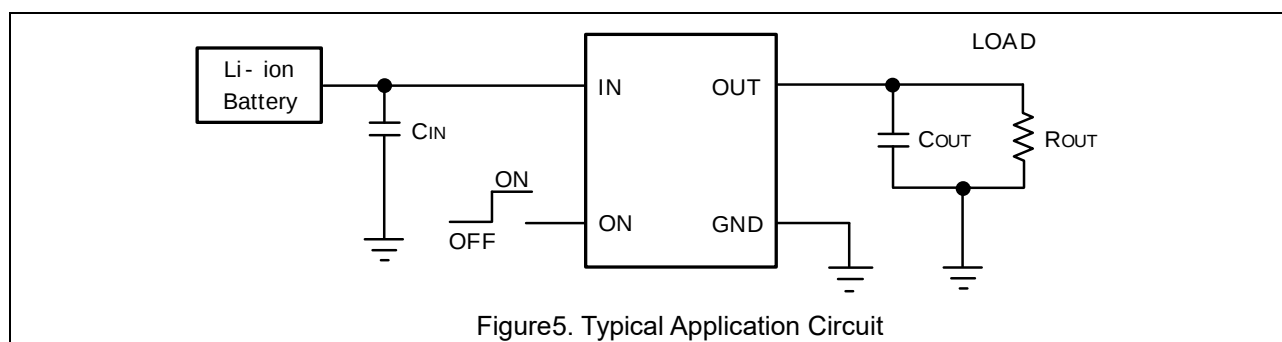
In many end devices, there is a need to power up various modules in a predetermined manner. ET35621 can solve the problem of power sequencing without adding any complexity to the overall system. [Figure4](#) shows the configuration required for powering up two modules in a fixed sequence. The output of the first load switch is tied to the enable of the second load switch, so when Module 1 is powered the second load switch is enabled and Module 2 is powered.

Typical Application

ET35621 is an ultra-low ON-resistance, 3A integrated load switch that is capable of interfacing directly with 1S battery in portable consumer devices such as smartphones, tablets etc. Its wide input voltage range (1.0V to 5.5V) makes it suitable to be used for lower voltage rails as well inside different end equipment to accomplish power sequencing, inrush current control and reducing leakage current in subsystems that are in standby mode.

[Figure5](#) shows the typical application circuit of ET35621.

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Detailed Design Procedure

Power Supply Recommendations

The device is designed to operate with a V_{IN} range of 1.0V to 5.5 V. This supply must be well regulated and placed as close to the device terminal as possible with the recommended 1 μ F bypass capacitor.

If the supply is located more than a few inches from the device terminals, additional bulk capacitance may be required in addition to the ceramic bypass capacitors. If additional bulk capacitance is required, an electrolytic, tantalum, or ceramic capacitor of 10 μ F may be sufficient.

Absolute Maximum Ratings

Over operating free-air temperature range (unless otherwise noted)

Symbol	Parameter	Rating	Unit
V_{IN}	Input Voltage Range	-0.3 to 6.0	V
V_{OUT}	Output Voltage Range	-0.3 to 6.0	V
V_{ON}	ON Pin Voltage Range	-0.3 to 6.0	V
I_{MAX}	Maximum Continuous Switch Current	3	A
T_{J_MAX}	Maximum Junction Temperature	150	°C
T_{STG}	Storage Temperature Range	-65 to 150	°C
V_{ESD}	Human-Body Model (HBM) (JEDEC JS-001)	± 4000	V
	Charged-Device Model (CDM) (JEDEC JS-002)	± 2000	V

Recommended Operating Conditions

Symbol	Parameter	Min	Max	Unit
V_{IN}	Input Voltage Range	1.0	5.5	V
V_{OUT}	Output Voltage Range	0	5.5	V
V_{ON}	ON Pin Voltage Range		V_{IN}	V
T_A	Operating Free Air Temperature Range	-40	85	°C
C_{IN}	Input Capacitor	1 ⁽¹⁾		μ F

Note1: Refer to the application section

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Electrical Characteristics

(Unless otherwise noted, $V_{IN}=1.0$ to $5.5V$, $T_A = -40$ to $+85^{\circ}C$; typical values are at $T_A = 25^{\circ}C$)

Symbol	Parameter	Conditions	$T_A = -40^{\circ}C$ to $85^{\circ}C$			Unit
			Min	Typ	Max	
V_{IN}	Input Voltage Range		1.0 ⁽²⁾		5.5	V
I_{IN}	Quiescent Current	$I_{OUT} = 0mA$, $V_{ON} = V_{IN} = 5.5V$		0.47	5.0	μA
		$I_{OUT} = 0mA$, $V_{ON} = V_{IN} = 3.3V$		0.33	5.0	
		$I_{OUT} = 0mA$, $V_{ON} = V_{IN} = 1.8V$		0.27	5.0	
		$I_{OUT} = 0mA$, $V_{ON} = V_{IN} = 1.0V$		0.17	5.0	
I_{IN_OFF}	Off Supply Current	$R_L = 1M\Omega$, $V_{IN} = 5.5V$, $V_{ON} = 0V$		0.14	3.0	μA
		$R_L = 1M\Omega$, $V_{IN} = 3.3V$, $V_{ON} = 0V$		0.08	3.0	
		$R_L = 1M\Omega$, $V_{IN} = 1.8V$, $V_{ON} = 0V$		0.05	3.0	
		$R_L = 1M\Omega$, $V_{IN} = 1.0V$, $V_{ON} = 0V$		0.02	3.0	
$I_{IN(Leakage)}$	Leakage Current	$V_{OUT}=0V$, $V_{IN} = 5.5V$, $V_{ON} = 0V$		0.14	3.0	μA
		$V_{OUT}=0V$, $V_{IN} = 3.3V$, $V_{ON} = 0V$		0.08	3.0	
		$V_{OUT}=0V$, $V_{IN} = 1.8V$, $V_{ON} = 0V$		0.05	3.0	
		$V_{OUT}=0V$, $V_{IN} = 1.0V$, $V_{ON} = 0V$		0.02	3.0	
R_{ON}	On-Resistance	$V_{IN} = 5.5V$, $I_{OUT} = -200mA$		35	62	$m\Omega$
		$V_{IN} = 3.3V$, $I_{OUT} = -200mA$		36	65	
		$V_{IN} = 1.8V$, $I_{OUT} = -200mA$		37	65	
		$V_{IN} = 1.0V$, $I_{OUT} = -200mA$		49	80	
R_{PD}	Output Pull Down Resistance	$V_{IN} = 3.3V$, $V_{ON} = 0V$, $V_{OUT} = 3.3V$		40		Ω
I_{ON}	ON Pin Leakage Current	$V_{IN} = V_{ON} = 1.0V$ to $5.5V$		2	150	nA
V_{RCP}	Reverse Current Voltage Threshold	$V_{IN} = 1.0V$ to $5.5V$		65	100	mV
$I_{RCP(leak)}$	Reverse Current Protection Leakage After Reverse Current Event	$V_{OUT} - V_{IN} > V_{RCP}$		0.04	3	μA
V_{IH}	High-Level Input Voltage, ON	$V_{IN} = 1.0V$ to $5.5V$	0.9			V
V_{IL}	Low-Level Input Voltage, ON	$V_{IN} = 1.0V$ to $5.5V$			0.4	V

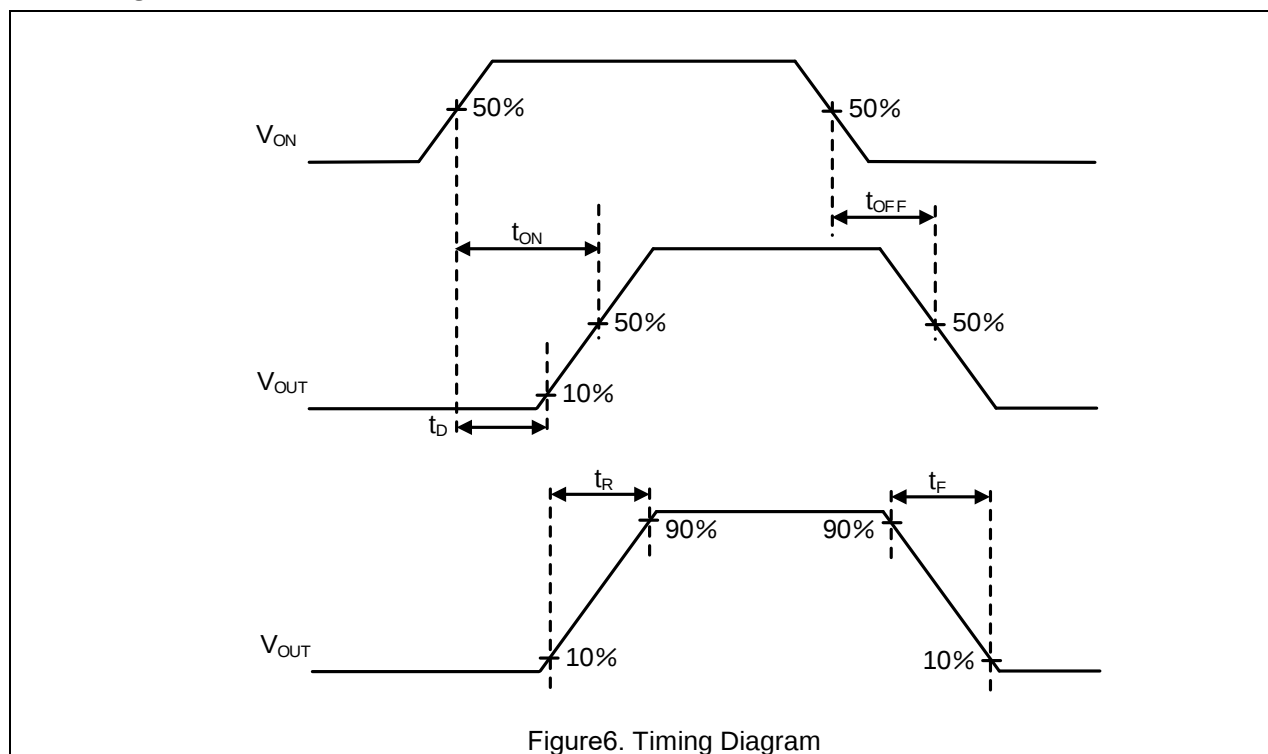
Note2: When $V_{IN} < 1.5V$, we suppose to set $V_{ON} \geq 1.5V$, which can reduce R_{ON} .

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Switching Characteristics

Symbol	Parameter	Conditions	T _A = 25°C			Unit
			Min	Typ	Max	
V _{IN} = 5.0V, Unless Otherwise Noted.						
t _{ON}	Turn-On Time	R _L = 10Ω, C _L = 0.1uF	800	1400	1900	us
t _{OFF}	Turn-Off Time	R _L = 10Ω, C _L = 0.1uF	3	10	20	
t _R	V _{OUT} Rise Time	R _L = 10Ω, C _L = 0.1uF	500	800	1150	
t _F	V _{OUT} Fall Time	R _L = 10Ω, C _L = 0.1uF	0.5	2.1	10	
V _{IN} = 3.3V, Unless Otherwise Noted.						
t _{ON}	Turn-On Time	R _L = 10Ω, C _L = 0.1uF	650	1200	1700	us
t _{OFF}	Turn-Off Time	R _L = 10Ω, C _L = 0.1uF	3	10	20	
t _R	V _{OUT} Rise Time	R _L = 10Ω, C _L = 0.1uF	400	700	1000	
t _F	V _{OUT} Fall Time	R _L = 10Ω, C _L = 0.1uF	0.5	2.1	10	
V _{IN} = 1.5V, Unless Otherwise Noted.						
t _{ON}	Turn-On Time	R _L = 10Ω, C _L = 0.1uF	500	1000	1500	us
t _{OFF}	Turn-Off Time	R _L = 10Ω, C _L = 0.1uF	3	8	20	
t _R	V _{OUT} Rise Time	R _L = 10Ω, C _L = 0.1uF	400	700	1300	
t _F	V _{OUT} Fall Time	R _L = 10Ω, C _L = 0.1uF	0.5	2.1	10	

Switching Waveform



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PCB Layout Guide

WLCSP4

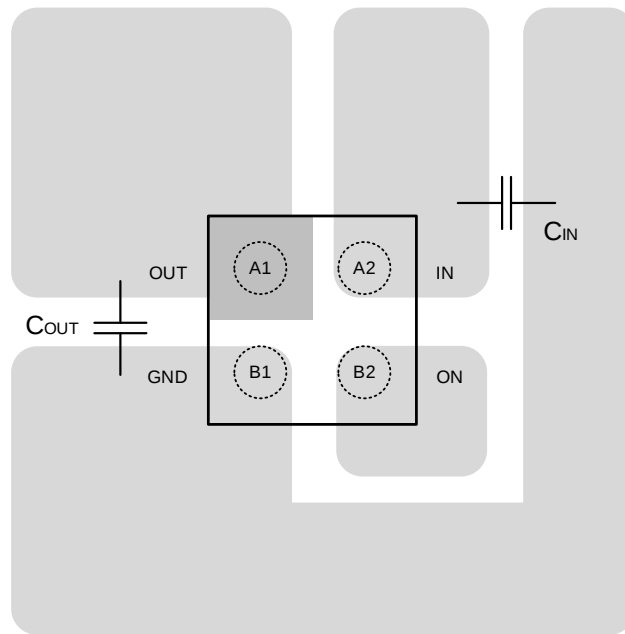
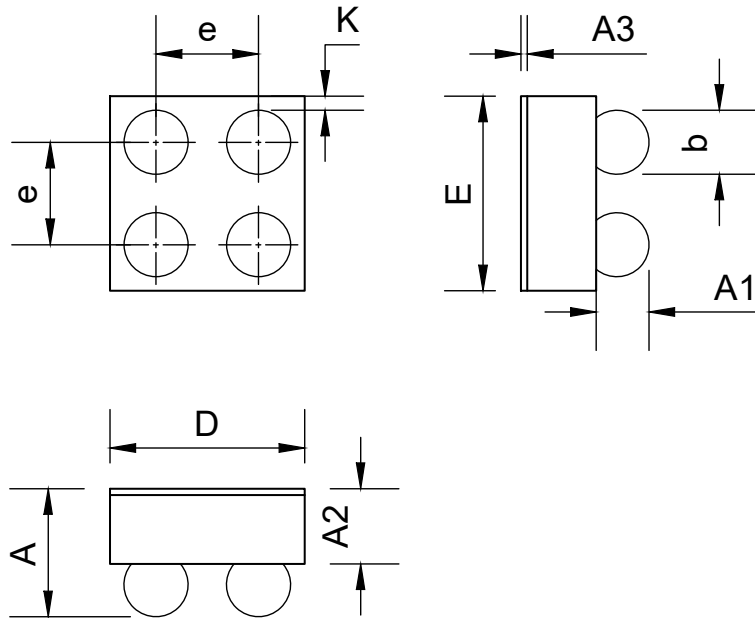


Figure7. PCB Layout Guide

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Package Dimension

WLCSP4



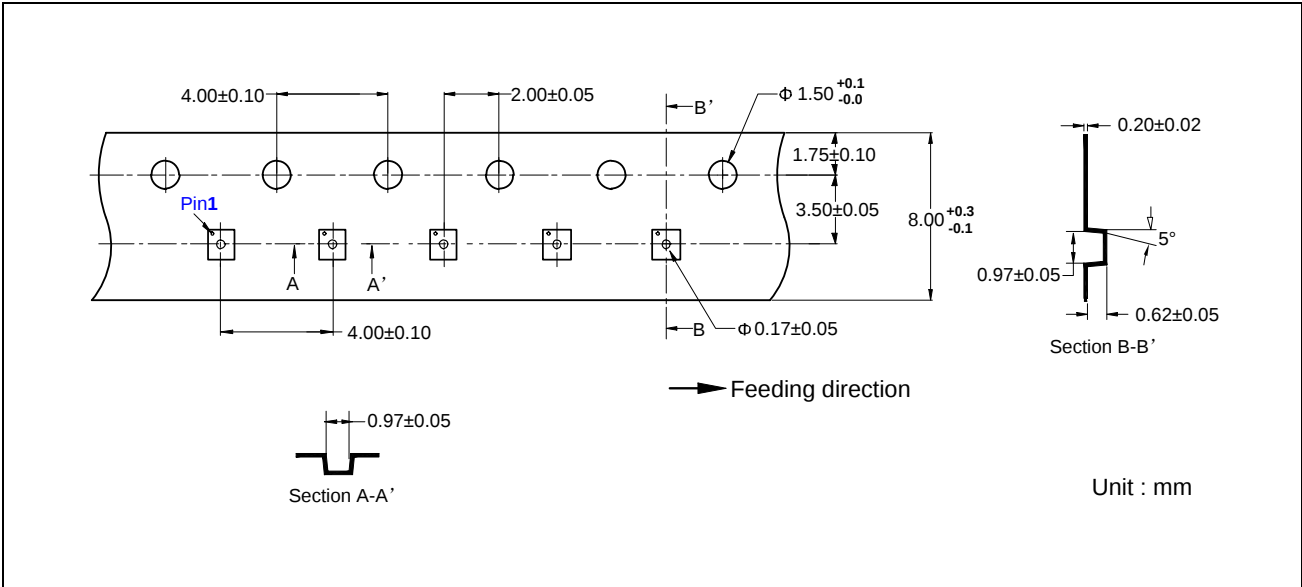
COMMON DIMENSIONS

(Unit: mm)

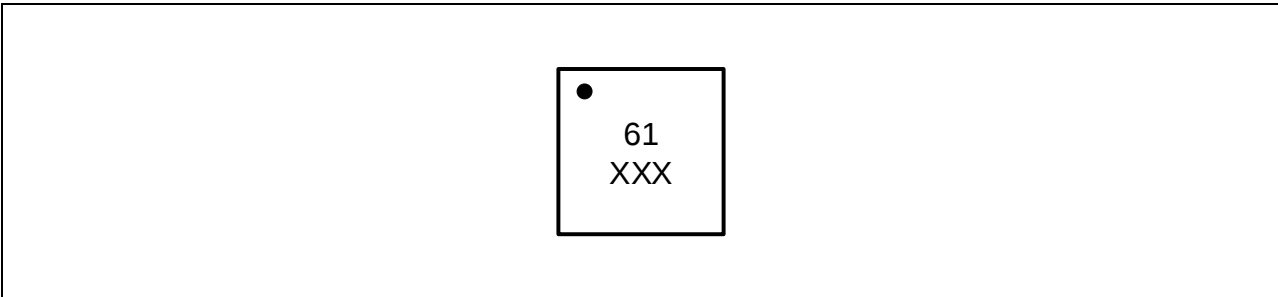
SYMBOL	MIN	NOM	MAX
A	0.447	0.500	0.553
A1	0.175	0.200	0.225
A2	0.250	0.275	0.300
A3	0.022	0.025	0.028
b	0.240	0.265	0.290
D	0.830	0.860	0.890
E	0.830	0.860	0.890
e	0.500BSC		
K	0.0475REF		

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Tape



Marking



Revision History and Checking Table

Version	Date	Revision Item	Modifier	Function & Spec Checking	Package & Tape Checking
1.0	2025-11-24	Original Version	Yangxiaoxu	Luhao	Liujiaying
1.1	2026-06-24	Add PCB Layout Guide	Wanganran	Yangxiaoxu	Liujiaying