

Non-inverting 1-to-2 Demultiplexer with 3-state Deselected

General Description

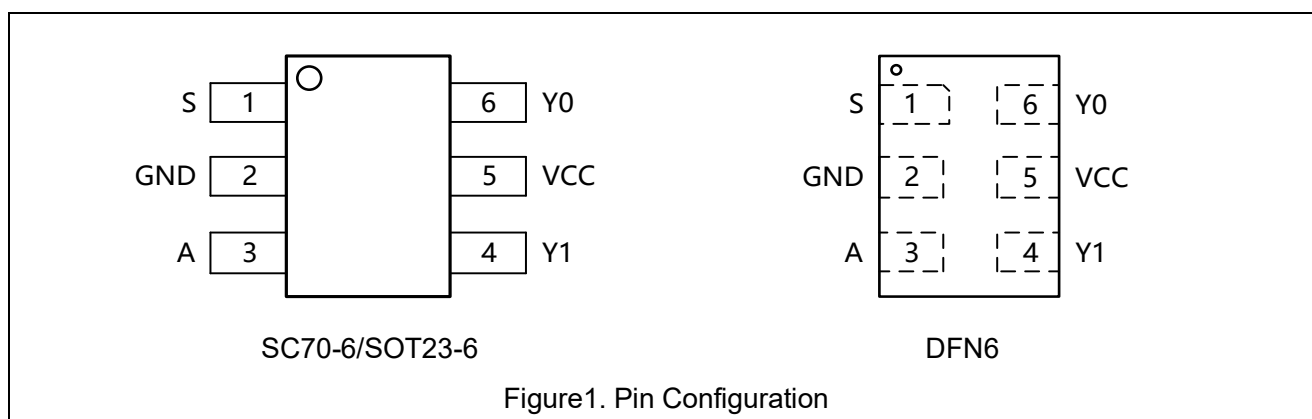
The ET74AHC1G18 is a high-performance non-inverting 1-to-2 demultiplexer. With the Select input [S] at Low, data at A is passed to Y0 and Y1 is set to high impedance. With the Select input [S] at High, data at A is passed to Y1 and Y0 is set to high impedance. The device operates over the voltage range from 2V to 5.5V.

Features

- Designed for 2V to 5.5V V_{CC} Operation
- Over-voltage Tolerant Inputs Accept Voltages to 5.5V
- $\pm 4\text{mA}$ Balanced Output Sink and Source Capability
- Near Zero Static Supply Current Substantially Reduces System Power Requirements
- These Devices are Pb-Free and RoHS Compliant
- ESD Protection Complies with JESD22 Standard
 - HBM: $\pm 4000\text{V}$ Pass (JEDEC JS-001)
 - CDM: $\pm 1000\text{V}$ Pass (JEDEC JS-002)
- Latch-up Performance Exceeds $\pm 100\text{mA}$ per JEDEC JESD78F
- Part No. and Package Information

Part No.	Package	Packing Option	MSL
ET74AHC1G18	SC70-6 (1.3mm × 2.1mm)	Tape and Reel, 3K/Reel	1
ET74AHC1G18T	SOT23-6 (1.6mm × 2.9mm)	Tape and Reel, 3K/Reel	3
ET74AHC1G18Y	DFN6 (1.0mm × 1.5mm)	Tape and Reel, 3K/Reel	1

Pin Configuration



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Pin Function

Pin No.	Pin Name	Pin Function
1	S	Demultiplexer Select
2	GND	Ground
3	A	Data Input
4	Y1	Output 2
5	VCC	Power
6	Y0	Output 1

Block Diagram

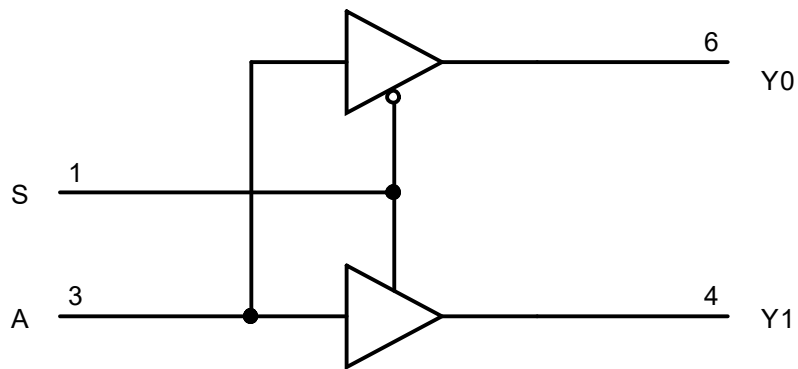


Figure2. Logic Symbol

Functional Table

Input		Output	
S	A	Y0	Y1
L	L	L	Z
L	H	H	Z
H	L	Z	L
H	H	Z	H

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Absolute Maximum Ratings

Symbol	Parameter		Value	Unit
V _{CC}	DC Supply Voltage		-0.5 to 7.0	V
V _I	DC Input Voltage ⁽¹⁾		-0.5 ≤ V _I ≤ 7.0	V
V _O	DC Output Voltage Output in Higher or Low State		-0.5 to V _{CC} + 0.5	V
I _{IK}	DC Input Diode Current, V _I < GND		-50	mA
I _{OK}	DC Output Diode Current, V _O < GND, V _O > V _{CC}		±50	mA
I _O	DC Output Sink Current		±50	mA
I _{CC}	DC Supply Current per Supply Pin		100	mA
I _{GND}	DC Ground Current per Supply Pin		-100	mA
T _{STG}	Storage Temperature Range		-65 to 150	°C
T _L	Lead Temperature, Soldering 10 Seconds		260	°C
T _J	Max Junction Temperature		150	°C
V _{ESD}	ESD Classification	Human Body Model ⁽²⁾	±4000	V
		Charged Device Model ⁽³⁾	±1000	
I _{LU}	Max Latch up Current Above V _{CC} and GND at 125°C ⁽⁴⁾		±100	mA

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

Note1: I_O absolute maximum rating must be observed.

Note2: HBM tested per JEDEC JS-001;

Note3: CDM tested per JEDEC JS-002;

Note4: Latch up Current Maximum Rating tested per JEDEC JESD78F.

Thermal Characteristics

Symbol	Package	Ratings	Value	Unit
R _{θJA}	SC70-6	Thermal Characteristics, Thermal Resistance, Junction-to-Air	280	°C/W
	SOT23-6		180	
	DFN6		440	
P _D	SC70-6	Power Dissipation in Still Air at 85°C	230	mW
	SOT23-6		360	
	DFN6		150	

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Recommended Operating Conditions

Symbol	Parameter		Min	Max	Unit
V _{CC}	DC Supply Voltage Operating		2	5.5	V
V _I	DC Input Voltage		0	V _{CC}	V
V _O	DC Output Voltage (High or Low State)		0	V _{CC}	V
T _A	Operating Temperature Range		-40	125	°C
t _r , t _f	Input Rise and Fall Time	V _{CC} = 2V to 5.5V	0	20	ns/V

Electrical Characteristics

DC Electrical Characteristics

Symbol	Parameter	Condition	$V_{CC}(V)$	$T_A = 25^\circ C$			$-40^\circ C \leq T_A \leq 125^\circ C$		Unit
				Min	Typ	Max	Min	Max	
V_{IH}	High-Level Input Voltage		2.0	1.5			1.5		V
			3.0	2.1			2.1		
			5.5	3.85			3.85		
V_{IL}	Low-Level Input Voltage		2.0			0.5		0.5	V
			3.0			0.9		0.9	
			5.5			1.65		1.65	
V_{OH}	High-Level Output Voltage	$I_{OH} = -50\mu A$	2~5.5	$V_{CC} - 0.1$	V_{CC}		$V_{CC} - 0.1$		V
		$I_{OH} = -4mA$	3	2.58			2.4		
		$I_{OH} = -8mA$	4.5	3.54			3.7		
V_{OL}	Low-Level Output Voltage	$I_{OL} = 50\mu A$	2~5.5			0.1		0.1	V
		$I_{OL} = 4mA$	3			0.36		0.55	
		$I_{OL} = 8mA$	4.5			0.36		0.55	
I_{IN}	Input Leakage Current	$V_I = 5.5V$ or GND	0~5.5			0.1		2.0	μA
I_{OFF}	Power Off Leakage Current	$V_I = 5.5V$ or $V_O = 5.5V$	0			1		10	μA
I_{CC}	Quiescent Supply Current	$V_I = 5.5V$ or GND	5.5			1		40	μA

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

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AC Electrical Characteristics

$t_r = t_f = 3\text{ns}$, $R_L = 1\text{k}\Omega$;

Symbol	Parameter	Condition	$V_{CC}(\text{V})$	$T_A = 25^\circ\text{C}$			$-40^\circ\text{C} \leq T_A \leq 125^\circ\text{C}$		Unit
				Min	Typ	Max	Min	Max	
t_{PLH} t_{PHL}	Propagation Delay (Figure3 and 4)	$C_L = 15\text{pF}$	3~3.6		7.5	11	1.0	13.5	ns
		$C_L = 50\text{pF}$	3~3.6		9.5	16	1.0	19	
		$C_L = 15\text{pF}$	4.5~5.5		6.4	8.4	1.0	9.9	
		$C_L = 50\text{pF}$	4.5~5.5		7.9	10.4	1.0	12.4	
t_{PZL} t_{PZH}	Output Enable Time (Figure5 and 6)	$C_L = 15\text{pF}$	3~3.6		8.1	11.4	1.0	14.9	ns
		$C_L = 50\text{pF}$	3~3.6		10	15	1.0	18	
		$C_L = 15\text{pF}$	4.5~5.5		5.8	7.9	1.0	9.4	
		$C_L = 50\text{pF}$	4.5~5.5		7.2	9.9	1.0	11.9	
t_{PLZ} t_{PHZ}	Output Disable Time (Figure5 and 6)	$C_L = 15\text{pF}$	3~3.6		10.2	13.9	1.0	16.9	ns
		$C_L = 50\text{pF}$	3~3.6		12.5	17.5	1.0	20.8	
		$C_L = 15\text{pF}$	4.5~5.5		7.4	10.1	1.0	11.8	
		$C_L = 50\text{pF}$	4.5~5.5		9	12.1	1.0	14.3	

Capacitance Characteristics

Symbol	Parameter	Condition	Typ	Unit
C_{IN}	Input Capacitance	$V_{CC} = 5.5\text{V}$, $V_I = 0\text{V}$ or V_{CC}	2.5	pF
C_{PD}	Power Dissipation Capacitance ⁽⁵⁾	10MHz, $V_{CC} = 3.3\text{V}$, $V_I = 0\text{V}$ or V_{CC}	15	pF
		10MHz, $V_{CC} = 5.5\text{V}$, $V_I = 0\text{V}$ or V_{CC}	15	

Note5: C_{PD} is used to determine the dynamic power dissipation (P_D in μW).

$$P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \sum (C_L \times V_{CC}^2 \times f_o) \text{ where:}$$

f_i = input frequency in MHz;

f_o = output frequency in MHz;

C_L = output load capacitance in pF;

V_{CC} = supply voltage in V;

N = number of inputs switching;

$\sum (C_L \times V_{CC}^2 \times f_o)$ = sum of outputs.

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AC Test Circuit

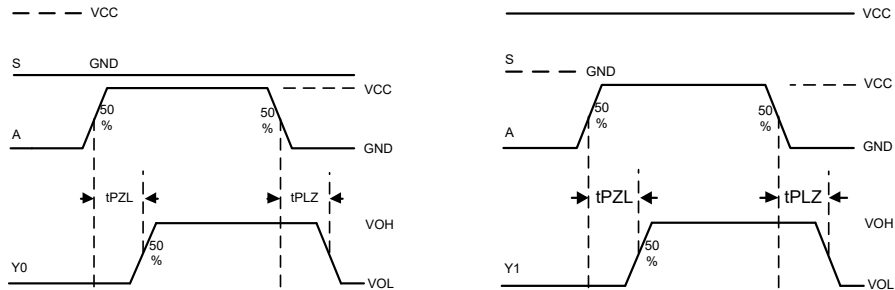


Figure3. Switching Waveform

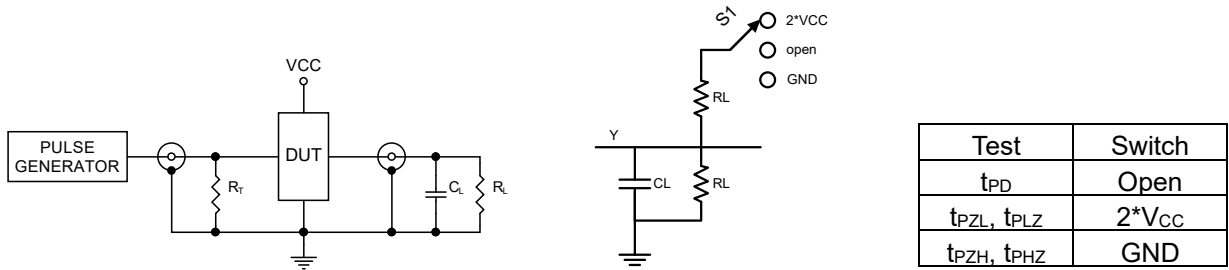


Figure4. Test Circuit $R_T = 50\Omega(\text{typ})$

Figure5. Output Enable/Disable Time Test Circuit

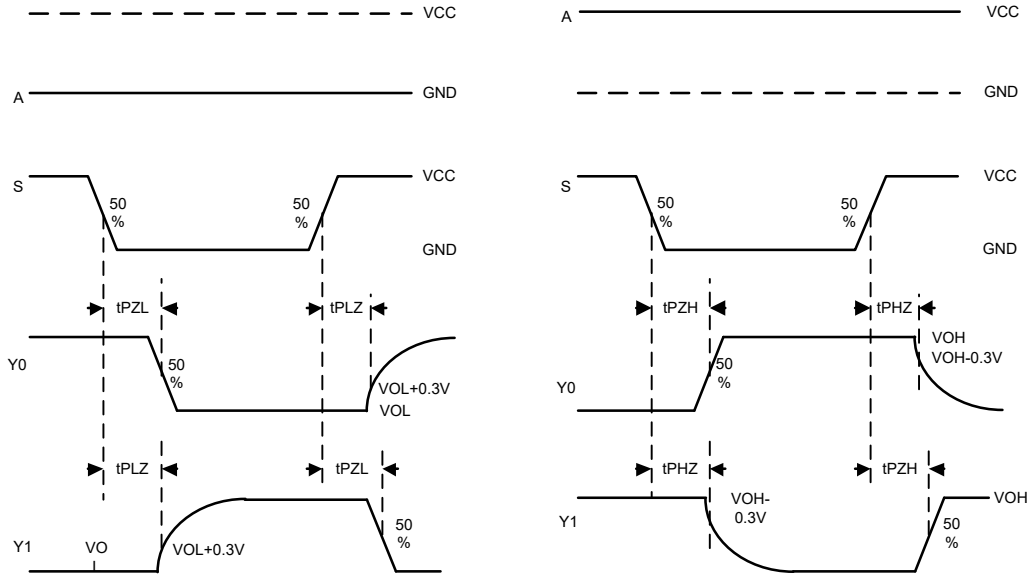
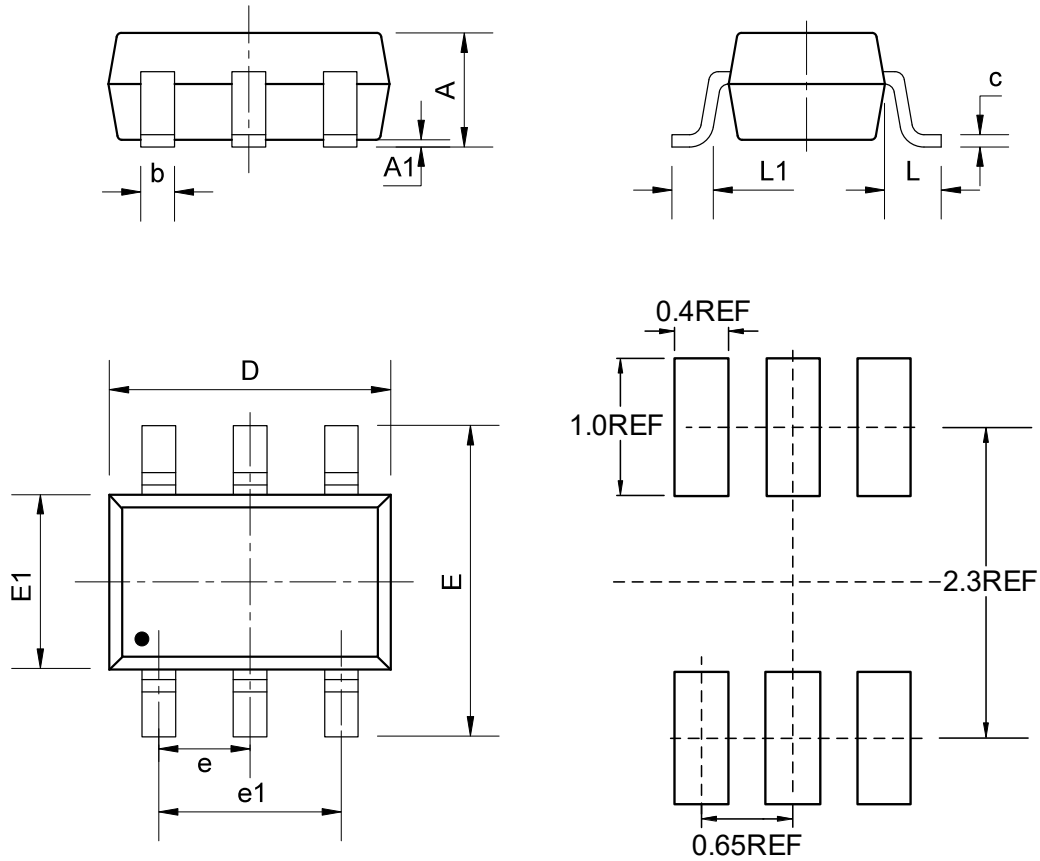


Figure6. Output Enable/Disable Waveform

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Package Dimension

SC70-6 (1.3mm × 2.1mm)



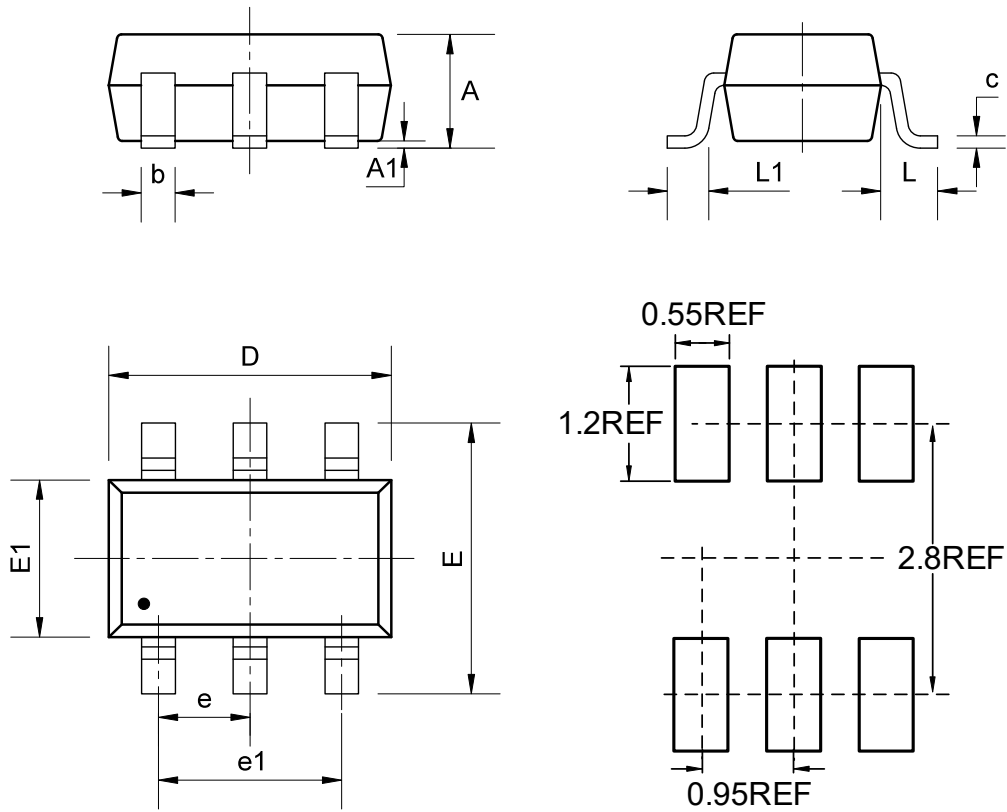
COMMON DIMENSIONS

(Unit: mm)

SYMBOL	MIN	NOM	MAX
A	-	-	1.10
A1	0.00	-	0.15
b	0.15	-	0.35
c	0.08	-	0.20
D	2.00	2.10	2.30
e	0.65BSC		
e1	1.30BSC		
E	2.15	2.30	2.50
E1	1.15	1.30	1.45
L	0.50REF		
L1	0.33REF		

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SOT23-6 (1.6mm × 2.9mm)



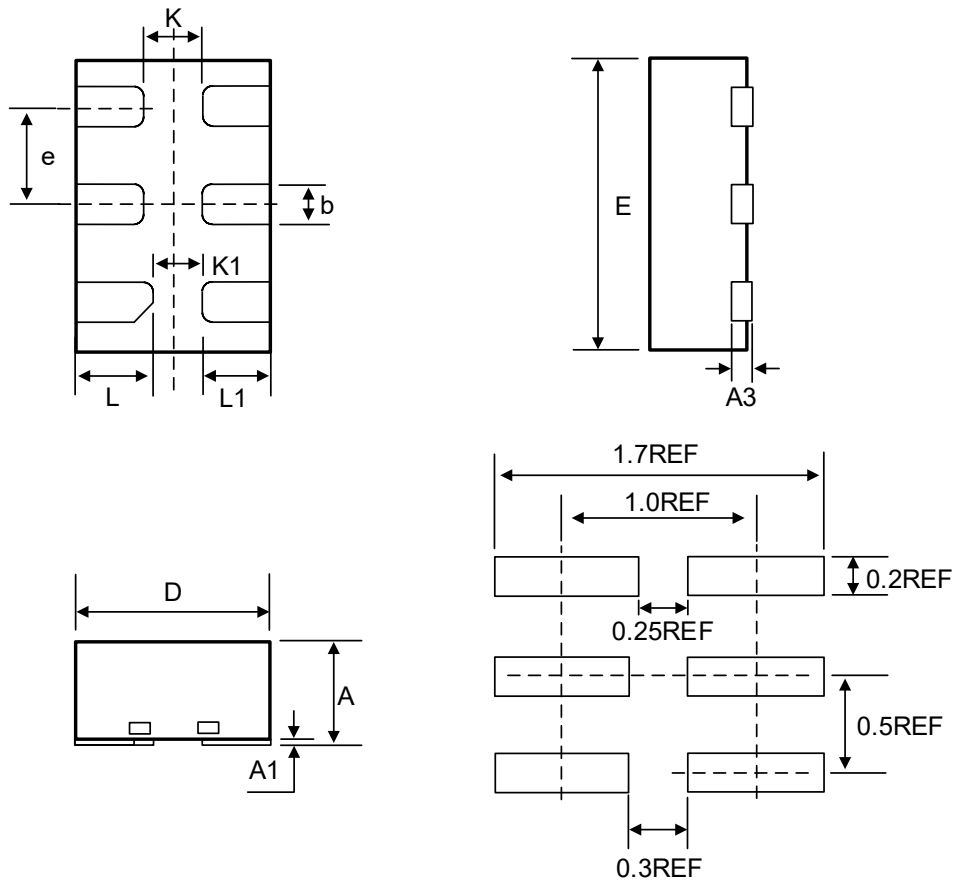
COMMON DIMENSIONS

(Unit: mm)

SYMBOL	MIN	NOM	MAX
A	-	-	1.45
A1	0.00	-	0.15
b	0.28	0.35	0.50
c	0.08	0.15	0.22
D	2.75	2.9	3.05
e	0.90	0.95	1.00
e1	1.80	1.90	2.00
E	2.60	2.80	3.00
E1	1.45	1.6	1.75
L	0.60REF		
L1	0.30	0.45	0.60

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DFN6 (1.0mm × 1.5mm)



COMMON DIMENSIONS

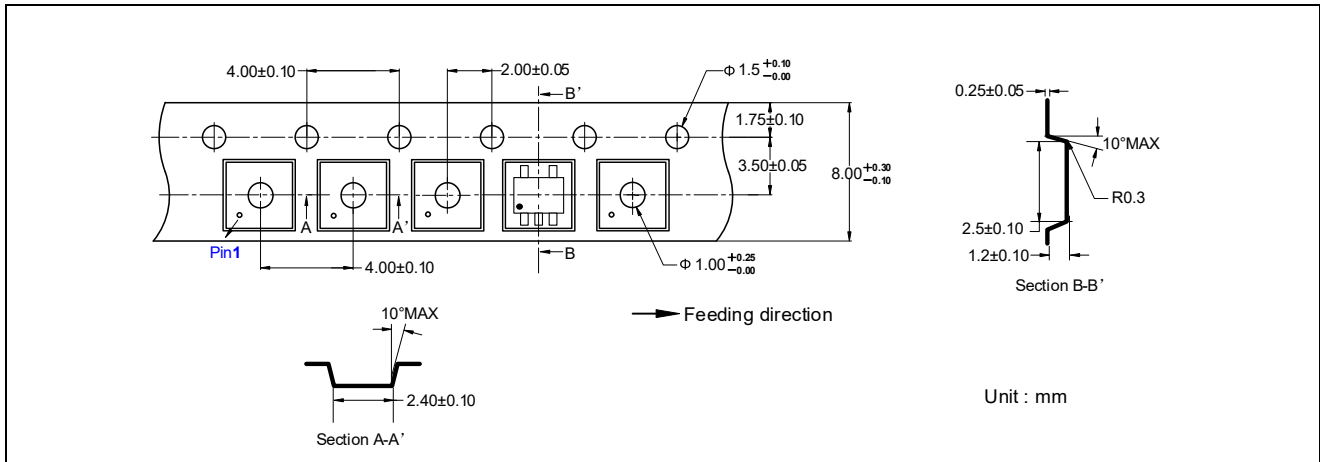
(Unit: mm)

SYMBOL	MIN	NOM	MAX
A	0.50	--	0.60
A1	0.00	0.02	0.05
A3	0.10REF		
b	0.15	0.20	0.25
D	0.90	1.00	1.10
E	1.40	1.50	1.60
e	0.50BSC		
K	0.30REF		
K1	0.25REF		
L	0.35	0.40	0.45
L1	0.30	0.35	0.40

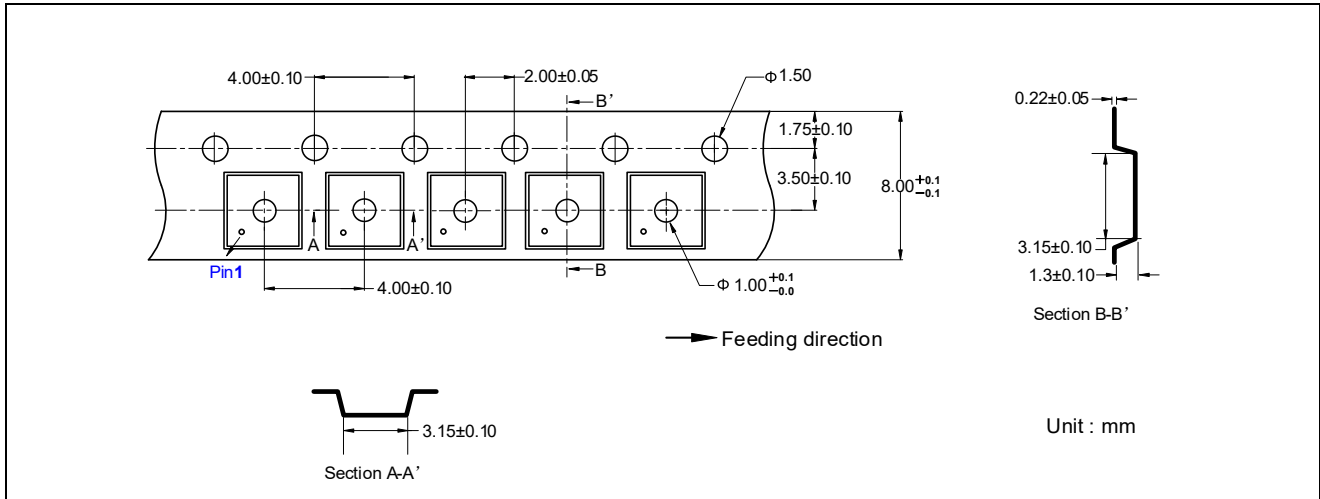
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Tape Information

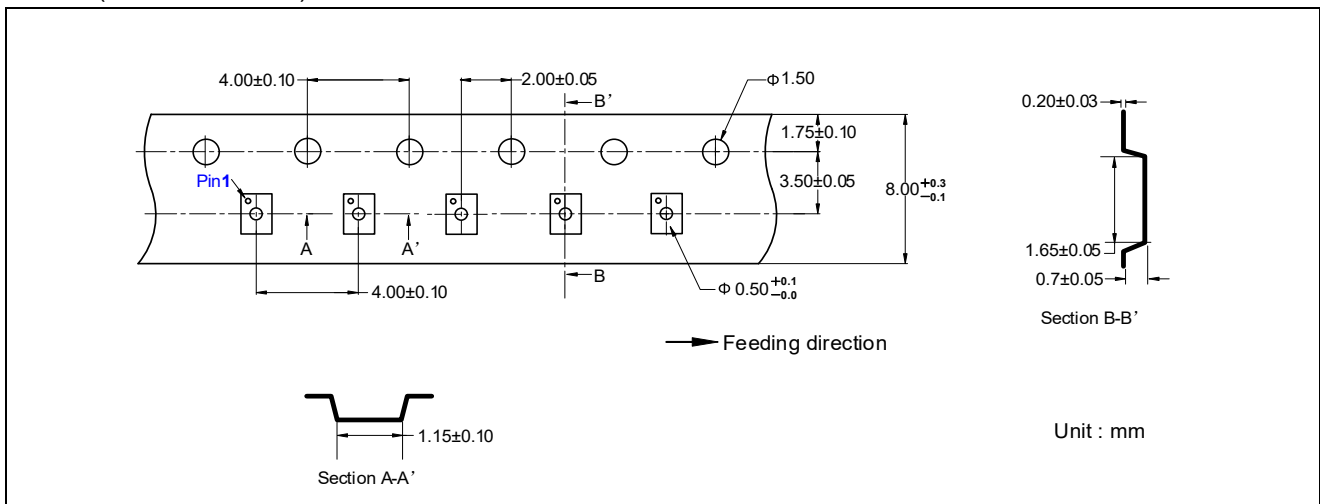
SC70-6 (1.3mm × 2.1mm)



SOT23-6 (1.6mm × 2.9mm)



DFN6 (1.0mm × 1.5mm)



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Marking Information

1G18X ● ET74AHC1G18 1G18 = Part Number X = Tracking Number	C118 XXXXX ● ET74AHC1G18T C118 = Part Number XXXXX = Tracking Number
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Revision History and Checking Table

Version	Date	Revision Item	Modifier	Function & Spec Checking	Package & Tape Checking
1.0	2026-04-08	Official Version	Xu tao	Yang xiaoxu	Liu jiaying