

Dual Supply Voltage

Level Translator/Transceiver; 3-State

General Description

The ET74LVCH1T45 is a dual-bit, dual-supply transceiver that enables bidirectional level translation. It features two data input-output ports (A and B), a direction control input (DIR) and dual-supply pins (VCC(A) and VCC(B)). Both VCC(A) and VCC(B) can be supplied at any voltage between 1.65V and 5.5V making the device suitable for translating between any of the low voltage nodes (1.2V, 1.5V, 1.8V, 2.5V, 3.3V and 5.0V). Pin A and DIR are referenced to VCC(A) and pin B are referenced to VCC(B). A HIGH on DIR allows transmission from A to B and a LOW on DIR allows transmission from B to A.

The device is fully specified for partial power-down applications using I_{OFF}. The I_{OFF} circuitry disables the output, preventing any damaging back-flow current through the device when it is powered down. In Suspend mode when either VCC(A) or VCC(B) are at GND level, both A and B are in the high-impedance OFF-state.

The ET74LVCH1T45 has active bus hold circuitry which is provided to hold unused or floating data inputs at a valid logic level. This feature eliminates the need for external pull-up or pull-down resistors.

Features

- Wide Supply Voltage Range:
 - V_{CC(A)}: 1.2V to 5.5V
 - V_{CC(B)}: 1.2V to 5.5V
- High Noise Immunity
- Maximum Data Rates:
 - 420Mbit/s (3.3V to 5.0V Translation)
 - 210Mbit/s (Translate to 3.3V)
 - 140Mbit/s (Translate to 2.5V)
 - 75Mbit/s (Translate to 1.8V)
 - 60Mbit/s (Translate to 1.5V)
- Bus Hold on Data Inputs
- Suspend Mode
- Over-voltage Tolerant Inputs Accept Voltages to 5.5V
- Low Noise Overshoot and Undershoot < 10% of V_{CC}
- I_{OFF} Circuitry Provides Partial Power-down Mode Operation
- ESD Protection Complies with JESD22 Standard
 - HBM: ±4000V Pass (JEDEC JS-001)
 - CDM: ±1500V Pass (JEDEC JS-002)
- Latch-up Performance Exceeds ±100mA per JEDEC JESD78F

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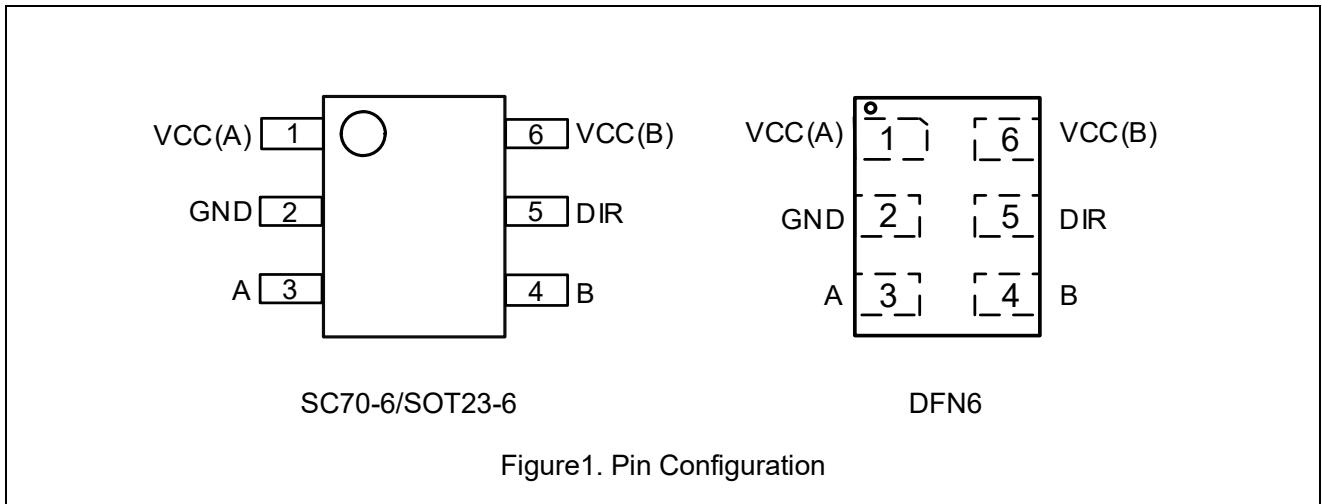
Part No. and Package Information

Part No.	Package	Packing Option	MSL
ET74LVCH1T45	SC70-6 (1.3mm × 2.1mm)	Tape and Reel, 3K/ Reel	3
ET74LVCH1T45T	SOT23-6 (1.6mm × 2.9mm)	Tape and Reel, 3K Reel	3
ET74LVCH1T45Y	DFN6 (1.0mm × 1.45mm)	Tape and Reel, 3K Reel	1

Applications

- Personal Electronic
- Industrial Equipment
- Enterprise Infrastructure
- Telecom Equipment

Pin Configuration



Pin Function

Pin		I/O	Description
Pin Name	Pin Number		
VCC(A)	1	-	Supply Voltage Pin of A Side (Referenced to pin A and DIR)
GND	2	-	Ground (0V)
A	3	I/O	Data Input or Output
B	4	I/O	Data Input or Output
DIR	5	I	Direction Control
VCC(B)	6	-	Supply Voltage Pin of B Side (Referenced to pin B)

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Functional Diagram

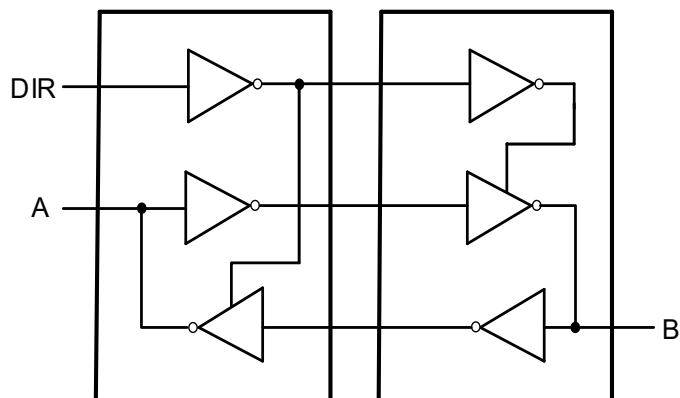


Figure2. Logic Symbol

Functional Table

Supply Voltage	Input	Input/Output ⁽¹⁾	
$V_{CC(A)}, V_{CC(B)}$	DIR ⁽²⁾	nA	nB
1.2V to 5.5V	L	A = B	Input
1.2V to 5.5V	H	Input	B = A
GND ⁽³⁾	X	Hi-Z	Hi-Z

Note1: The input circuit of the data I/O is always active.

Note2: The DIR input circuit is referenced to VCC(A).

Note3: If at least one of VCC(A) or VCC(B) is at GND level, the device goes into suspend mode.

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Absolute Maximum Ratings

Symbol	Parameter	Conditions	Value	Unit
$V_{CC(A)}$	Supply Voltage A Side (VCC(A) Pin)		-0.5~6.5	V
$V_{CC(B)}$	Supply Voltage B Side (VCC(B) Pin)		-0.5~6.5	V
I_{IK}	Input Clamping Current	$V_I < 0V$	-50	mA
V_I	Input Voltage ⁽⁴⁾		-0.5~6.5	V
I_{OK}	Output Clamping Current	$V_O < 0V$	-50	mA
V_O	Output Voltage	Active Mode ⁽⁴⁾⁽⁵⁾⁽⁶⁾	-0.5~ $V_{CCO} + 0.5$	V
		Suspend or 3-state Mode ⁽⁴⁾	-0.5~6.5	
I_O	Output Current	$V_O = 0V$ to V_{CCO}	±50	mA
I_{CC}	Supply Current	Per VCC(A) or VCC(B) Pin	100	mA
I_{GND}	Ground Current	Per GND Pin	-100	mA
$T_{J(MAX)}$	Maximum Junction Temperature		150	°C
T_{STG}	Storage Temperature		-65~150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

Note4: The minimum input voltage ratings and output voltage ratings may be exceeded if the input and output current ratings are observed.

Note5: $V_{CCO} + 0.5V$ should not exceed 6.5V.

Note6: V_{CCO} is the supply voltage associated with the output port.

Recommended Operating Conditions

Symbol	Parameter	Conditions	Min	Max	Unit
$V_{CC(A)}$	Supply Voltage A Side (VCC(A) Pin)		1.2	5.5	V
$V_{CC(B)}$	Supply Voltage B Side (VCC(B) Pin)		1.2	5.5	
V_I	Input Voltage		0	5.5	V
V_O	Output Voltage	Active State ⁽⁶⁾	0	V_{CCO}	V
		Suspend or 3-State	0	5.5	
T_A	Ambient Temperature		-40	125	°C
$\Delta t/\Delta V$	Input Transition Rise or Fall Rate	V_{CCI} ⁽⁷⁾ = 1.2V to 5.5V		5	ns/V

Note7: V_{CCI} is the supply voltage associated with the input port.

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Electrical Characteristics

Typical static characteristics at $T_A = 25^\circ\text{C}$ (unless otherwise noted)⁽⁶⁾⁽⁷⁾

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_{OH}	High-level Output Voltage	$V_I = V_{IH}$ or V_{IL} $I_O = -3\text{mA}$; $V_{CC(A)} = V_{CC(B)} = 1.2\text{V}$		1.09		V
V_{OL}	Low-level Output Voltage	$V_I = V_{IH}$ or V_{IL} $I_O = 3\text{mA}$; $V_{CC(A)} = V_{CC(B)} = 1.2\text{V}$		0.07		V
I_I	Input Leakage Current	DIR Input; $V_I = 0\text{V}$ or 5.5V ; $V_{CC(A)} = V_{CC(B)} = 1.2\text{V}$ to 5.5V			± 1	μA
$I_{BHL}^{(8)}$	Bus Hold LOW Current	A or B Port; $V_I = 0.42\text{V}$; $V_{CC(A)} = V_{CC(B)} = 1.2\text{V}$		26		μA
$I_{BHH}^{(8)}$	Bus Hold HIGH Current	A or B Port; $V_I = 0.78\text{V}$; $V_{CC(A)} = V_{CC(B)} = 1.2\text{V}$		-24		μA
$I_{BHLO}^{(8)(9)}$	Bus Hold LOW Overdrive Current	$V_{CC(A)} = V_{CC(B)} = 1.2\text{V}$		27		μA
$I_{BHHO}^{(8)(9)}$	Bus Hold HIGH Overdrive Current	$V_{CC(A)} = V_{CC(B)} = 1.2\text{V}$		-26		μA
I_{OZ}	Off-state Output Current	A or B Port; $V_O = 0\text{V}$ or V_{CCO} ; ⁽⁶⁾⁽¹⁰⁾ $V_{CC(A)} = V_{CC(B)} = 1.2\text{V}$ to 5.5V			± 2.5	μA
I_{OFF}	Power-off Leakage Current	A Port; V_I or $V_O = 0\text{V}$ to 5.5V ; $V_{CC(A)} = 0\text{V}$; $V_{CC(B)} = 1.2\text{V}$ to 5.5V			± 1	μA
		B Port; V_I or $V_O = 0\text{V}$ to 5.5V ; $V_{CC(B)} = 0\text{V}$; $V_{CC(A)} = 1.2\text{V}$ to 5.5V			± 1	μA
C_I	Input Capacitance	DIR Input; $V_I = 0\text{V}$ or 3.3V ; $V_{CC(B)} = V_{CC(A)} = 3.3\text{V}$		2.2		pF
$C_{I/O}$	Input / Output Capacitance	A and B Port; $V_O = V_{CCO}$ or GND; $V_{CC(A)} = V_{CC(B)} = 3.3\text{V}$		6.0		pF

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Electrical Characteristics (Continued)

Static characteristics⁽⁶⁾⁽⁷⁾

Symbol	Parameter	Conditions		T _A = -40°C to 85°C		T _A = -40°C to 125°C		Unit
				Min	Max	Min	Max	
V _{IH}	High-level Input Voltage	Data Input (7)	V _{CCI} = 1.2V	0.8V _{CCI}		0.8V _{CCI}		V
			V _{CCI} = 1.4V~1.95V	0.65V _{CCI}		0.65V _{CCI}		
			V _{CCI} = 2.3V~2.7V	1.7		1.7		
			V _{CCI} = 3.0V~3.6V	2.0		2.0		
			V _{CCI} = 4.5V~5.5V	0.7V _{CCI}		0.7V _{CCI}		
		DIR Input	V _{CCI} = 1.2V	0.8V _{CC(A)}		0.8V _{CC(A)}		V
			V _{CCI} = 1.4V~1.95V	0.65V _{CC(A)}		0.65V _{CC(A)}		
			V _{CCI} = 2.3V~2.7V	1.7		1.7		
			V _{CCI} = 3.0V~3.6V	2.0		2.0		
			V _{CCI} = 4.5V~5.5V	0.7V _{CC(A)}		0.7V _{CC(A)}		
V _{IL}	Low-level Input Voltage	Data Input (7)	V _{CCI} = 1.2V		0.2V _{CCI}		0.2V _{CCI}	V
			V _{CCI} = 1.4V~1.95V		0.35V _{CCI}		0.35V _{CCI}	
			V _{CCI} = 2.3V~2.7V		0.7		0.7	
			V _{CCI} = 3.0V~3.6V		0.8		0.8	
			V _{CCI} = 4.5V~5.5V		0.3V _{CCI}		0.3V _{CCI}	
		DIR Input	V _{CCI} = 1.2V		0.2V _{CC(A)}		0.2V _{CC(A)}	V
			V _{CCI} = 1.4V~1.95V		0.35V _{CC(A)}		0.35V _{CC(A)}	
			V _{CCI} = 2.3V~2.7V		0.7		0.7	
			V _{CCI} = 3.0V~3.6V		0.8		0.8	
			V _{CCI} = 4.5V~5.5V		0.3V _{CC(A)}		0.3V _{CC(A)}	
V _{OH}	High-level Output Voltage	V _I = V _{IH}						
		I _O = -100μA	V _{CC(A)} = V _{CC(B)} = 1.2V~4.5V	V _{CCO} - 0.1		V _{CCO} - 0.1		V
		I _O = -6mA	V _{CC(A)} = V _{CC(B)} = 1.4V	1.0		1.0		
		I _O = -8mA	V _{CC(A)} = V _{CC(B)} = 1.65V	1.2		1.2		
		I _O = -12mA	V _{CC(A)} = V _{CC(B)} = 2.3V	1.9		1.9		
		I _O = -24mA	V _{CC(A)} = V _{CC(B)} = 3.0V	2.4		2.4		
		I _O = -32mA	V _{CC(A)} = V _{CC(B)} = 4.5V	3.8		3.8		

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Electrical Characteristics (Continued)

Symbol	Parameter	Conditions		T _A = -40°C to 85°C		T _A = -40°C to 125°C		Unit
				Min	Max	Min	Max	
V _{OL}	Low-level Output Voltage	V _I = V _{IL}						V
		I _O = 100μA	V _{CC(A)} = V _{CC(B)} = 1.2V~4.5V		0.1		0.1	
		I _O = 6mA	V _{CC(A)} = V _{CC(B)} = 1.4V		0.3		0.3	
		I _O = 8mA	V _{CC(A)} = V _{CC(B)} = 1.65V		0.45		0.45	
		I _O = 12mA	V _{CC(A)} = V _{CC(B)} = 2.3V		0.3		0.3	
		I _O = 24mA	V _{CC(A)} = V _{CC(B)} = 3.0V		0.55		0.55	
		I _O = 32mA	V _{CC(A)} = V _{CC(B)} = 4.5V		0.55		0.55	
I _I	Input Leakage Current	DIR Input; V _I = V _{CC(A)} or GND	V _{CC(A)} = V _{CC(B)} = 1.2V~5.5V		±2		±10	μA
I _{BHLO} ^{(8) (9)}	Bus Hold LOW Overdrive Current	A or B port						
		V _{CC(A)} = V _{CC(B)} = 1.6V		125		125		μA
		V _{CC(A)} = V _{CC(B)} = 1.95V		200		200		μA
		V _{CC(A)} = V _{CC(B)} = 2.7V		300		300		μA
		V _{CC(A)} = V _{CC(B)} = 3.6V		500		500		μA
		V _{CC(A)} = V _{CC(B)} = 5.5V		900		900		μA
I _{BHHO} ^{(8) (9)}	Bus Hold HIGH Overdrive Current	A or B port						
		V _{CC(A)} = V _{CC(B)} = 1.6V		-125		-125		μA
		V _{CC(A)} = V _{CC(B)} = 1.95V		-200		-200		μA
		V _{CC(A)} = V _{CC(B)} = 2.7V		-300		-300		μA
		V _{CC(A)} = V _{CC(B)} = 3.6V		-500		-500		μA
		V _{CC(A)} = V _{CC(B)} = 5.5V		-900		-900		μA
I _{OZ}	Off-state Output Current	A or B Port; V _O = 0V or V _{CCO} ;	V _{CC(A)} = V _{CC(B)} = 1.2V~5.5V ⁽⁶⁾⁽¹⁰⁾		±2		±10	μA
I _{OFF}	Power-off Leakage Current	A Port; V _I or V _O = 0V~5.5V;	V _{CC(A)} = 0V; V _{CC(B)} = 1.2V~5.5V		±5		±35	μA
		B Port; V _I or V _O = 0V~5.5V;	V _{CC(B)} = 0V; V _{CC(A)} = 1.2V~5.5V		±5		±35	

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Electrical Characteristics (Continued)

Symbol	Parameter	Conditions		T _A = -40°C to 85°C		T _A = -40°C to 125°C		Unit
				Min	Max	Min	Max	
I _{BHL} ⁽⁸⁾	Bus Hold LOW Current	A or B port						
		V _I = 0.49V	V _{CC(A)} = V _{CC(B)} = 1.4V	15		10		μA
		V _I = 0.58V	V _{CC(A)} = V _{CC(B)} = 1.65V	25		20		μA
		V _I = 0.70V	V _{CC(A)} = V _{CC(B)} = 2.3V	45		45		μA
		V _I = 0.80V	V _{CC(A)} = V _{CC(B)} = 3.0V	100		80		μA
		V _I = 1.35V	V _{CC(A)} = V _{CC(B)} = 4.5V	100		100		μA
I _{BHH} ⁽⁸⁾	Bus Hold HIGH Current	A or B port						
		V _I = 0.91V	V _{CC(A)} = V _{CC(B)} = 1.4V	-15		-10		μA
		V _I = 1.07V	V _{CC(A)} = V _{CC(B)} = 1.65V	-25		-20		μA
		V _I = 1.60V	V _{CC(A)} = V _{CC(B)} = 2.3V	-45		-45		μA
		V _I = 2.00V	V _{CC(A)} = V _{CC(B)} = 3.0V	-100		-80		μA
		V _I = 3.15V	V _{CC(A)} = V _{CC(B)} = 4.5V	-100		-100		μA

Note8: '+/-' represents the direction of the current.

Note9: I_{BHL}, I_{BHH} means the bus hold current; I_{BHLO}, I_{BHHO} means the minimum overdrive current to flip the level.

Note10: For I/O ports, the parameter I_{OZ} includes the input leakage current.

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Electrical Characteristics (Continued)

Symbol	Parameter	Conditions	T _A = -40°C to 85°C		T _A = -40°C to 125°C		Unit
			Min	Max	Min	Max	
I _{CC}	Supply Current	A Port; V _I = 0V or V _{CCI} ; I _O = 0mA					
		V _{CC(A)} , V _{CC(B)} = 1.2V~5.5V		10		12	μA
		V _{CC(A)} = 5.5V; V _{CC(B)} = 0V		10		12	μA
		V _{CC(A)} = 0V; V _{CC(B)} = 5.5V	-2		-8		μA
		B Port; V _I = 0V or V _{CCI} ; I _O = 0mA					
		V _{CC(A)} , V _{CC(B)} = 1.2V~5.5V		10		12	μA
		V _{CC(B)} = 5.5V; V _{CC(A)} = 0V		10		12	μA
		V _{CC(B)} = 0V; V _{CC(A)} = 5.5V	-2		-8		μA
		A Plus B Port (I _{CC(A)} + I _{CC(B)}); V _I = 0V or V _{CCI} ; I _O = 0mA; V _{CC(A)} , V _{CC(B)} = 1.2V~5.5V		16		16	μA

Switching Characteristics

Typical power dissipation capacitance at V_{CC(A)} = V_{CC(B)} and T_A = 25°C (Unless otherwise noted)⁽¹¹⁾⁽¹²⁾

Symbol	Parameter	Conditions	V _{CC(A)} = V _{CC(B)}				Unit
			1.8V	2.5V	3.3V	5.0V	
C _{PD}	Power Dissipation Capacitance	A Port:(Direction A to B); B Port:(Direction B to A)	2	3	3	4	pF
		A Port:(Direction B to A); B Port:(Direction A to B)	15	16	17	35	pF

Note11: C_{PD} is used to determine the dynamic power dissipation (P_D in μW).

$$P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \Sigma(C_L \times V_{CC}^2 \times f_o) \text{ where:}$$

f_i = input frequency in MHz;

f_o = output frequency in MHz;

C_L = output load capacitance in pF;

V_{CC} = supply voltage in V;

N = number of inputs switching;

Σ(C_L × V_{CC}² × f_o) = sum of outputs.

Note12: f_i = 10MHz; V_I = 0 to V_{CC}; t_r = t_f = 1ns; C_L = 0pF; R_L = ∞Ω.

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Switching Characteristics (Continued)⁽¹³⁾⁽¹⁴⁾

Typical switching characteristics at $V_{CC(A)} = 1.2V$ and $T_A = 25^\circ C$ (unless otherwise noted)

For test circuit, see [Figure5](#); for wave forms, see [Figure3](#) and [Figure4](#)

Symbol	Parameter	Conditions	$V_{CC(B)}$				Unit
			1.2V	1.8V	3.3V	5.0V	
t_{pd}	Propagation Delay	A to B	10.6	8.0	9.5	5.4	ns
		B to A	12.7	12.2	11.8	8.2	ns
t_{dis}	Disable Time	DIR to A	23.6	23.6	23.6	19.9	ns
		DIR to B	16.8	12.7	11.6	8.8	ns
t_{en}	Enable Time	DIR to A	55.7	55.2	60.6	56.7	ns
		DIR to B	56.4	54.6	55.6	58.6	ns

Typical switching characteristics at $V_{CC(B)} = 1.2V$ and $T_A = 25^\circ C$ (unless otherwise noted)

For test circuit, see [Figure5](#); for wave forms, see [Figure3](#) and [Figure4](#)

Symbol	Parameter	Conditions	$V_{CC(A)}$				Unit
			1.2V	1.8V	3.3V	5.0V	
t_{pd}	Propagation Delay	A to B	12.7	12.2	11.8	8.2	ns
		B to A	10.6	8.0	9.5	5.4	ns
t_{dis}	Disable Time	DIR to A	15.6	10.1	7.4	5.6	ns
		DIR to B	16.8	16.6	14.4	12.9	ns
t_{en}	Enable Time	DIR to A	55.7	53.1	51.0	59.9	ns
		DIR to B	56.4	54.6	59.0	53.9	ns

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Switching Characteristics: -40°C~85°C (Continued)⁽¹³⁾⁽¹⁴⁾

Symbol	Parameter	Conditions	V _{CC(B)}						Unit
			1.8V ± 0.15V		3.3V ± 0.3V		5V ± 0.5V		
			Min	Max	Min	Max	Min	Max	
V _{CC(A)} = 1.65V to 1.95V									
t _{pd}	Propagation Delay	An to Bn	0.5	17.7	0.5	7.2	1.4	7.0	ns
		Bn to An	0.5	17.7	0.5	15.5	1.8	15.1	ns
t _{dis}	Disable Time	DIR to An	1.6	17.1	1.6	17.1	2.4	17.1	ns
		DIR to Bn	1.4	21.9	1.5	10.3	2.4	14.2	ns
t _{en}	Enable Time	DIR to An	-	78	-	73	-	70	ns
		DIR to Bn	-	56	-	56	-	105	ns
V _{CC(A)} = 3.0V to 3.6V									
t _{pd}	Propagation Delay	An to Bn	0.5	15.5	0.5	6.6	0.7	6.0	ns
		Bn to An	0.5	7.2	0.5	6.7	0.6	6.1	ns
t _{dis}	Disable Time	DIR to An	1.5	13.4	1.5	13.4	2.0	13.4	ns
		DIR to Bn	0.6	18.7	1.7	17.1	1.7	16.3	ns
t _{en}	Enable Time	DIR to An	-	70	-	65	-	63	ns
		DIR to Bn	-	60	-	60	-	115	ns
V _{CC(A)} = 4.5V to 5.5V									
t _{pd}	Propagation Delay	An to Bn	1.8	15.1	0.7	5.1	0.5	4.8	ns
		Bn to An	1.4	7.0	0.7	5.0	0.5	4.8	ns
t _{dis}	Disable Time	DIR to An	1.4	16.4	1.0	16.4	0.9	16.4	ns
		DIR to Bn	2.4	21.2	2.3	18.0	1.8	15.3	ns
t _{en}	Enable Time	DIR to An	-	80	-	78	-	76	ns
		DIR to Bn	-	130	-	130	-	130	ns

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Switching Characteristics: -40°C~125°C (Continued)⁽¹³⁾⁽¹⁴⁾

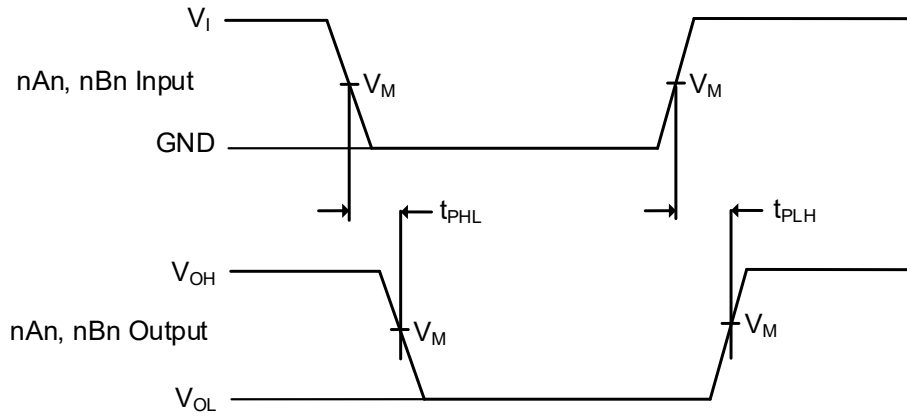
Symbol	Parameter	Conditions	V _{CC(B)}						Unit
			1.8V ± 0.15V		3.3V ± 0.3V		5V ± 0.5V		
			Min	Max	Min	Max	Min	Max	
V _{CC(A)} = 1.65V to 1.95V									
t _{pd}	Propagation Delay	An to Bn	0.5	19.5	0.5	8.0	1.2	7.7	ns
		Bn to An	0.5	19.5	0.5	17.1	1.6	16.7	ns
t _{dis}	Disable Time	DIR to An	1.6	18.9	1.6	18.9	2.1	18.9	ns
		DIR to Bn	1.4	24.1	1.5	11.4	2.1	15.1	ns
t _{en}	Enable Time	DIR to An	-	83	-	78	-	75	ns
		DIR to Bn	-	61	-	61	-	110	ns
V _{CC(A)} = 3.0V to 3.6V									
t _{pd}	Propagation Delay	An to Bn	0.5	17.1	0.5	7.2	0.6	6.5	ns
		Bn to An	0.5	8.0	0.5	7.4	0.5	6.6	ns
t _{dis}	Disable Time	DIR to An	1.5	14.2	1.5	14.2	1.8	14.2	ns
		DIR to Bn	0.6	19.2	1.7	17.6	1.5	16.8	ns
t _{en}	Enable Time	DIR to An	-	75	-	70	-	68	ns
		DIR to Bn	-	65	-	65	-	120	ns
V _{CC(A)} = 4.5V to 5.5V									
t _{pd}	Propagation Delay	An to Bn	1.6	16.7	0.6	6.2	0.4	6.0	ns
		Bn to An	1.2	7.7	0.6	6.1	0.4	6.0	ns
t _{dis}	Disable Time	DIR to An	1.2	19.0	0.9	19.0	0.8	19.0	ns
		DIR to Bn	2.1	26.5	2.0	23.4	1.6	20.9	ns
t _{en}	Enable Time	DIR to An	-	85	-	83	-	81	ns
		DIR to Bn	-	135	-	135	-	135	ns

Note13: t_{pd} is the same as t_{PLH} and t_{PHL}; t_{dis} is the same as t_{PLZ} and t_{PHZ}; t_{en} is the same as t_{PZL} and t_{PZH}.

Note14: When testing t_{en}, the An or Bn port needs to change its state after the Bn or An port has changed. When testing t_{dis}, the An or Bn port does not rely on the output state change of the other port to change.

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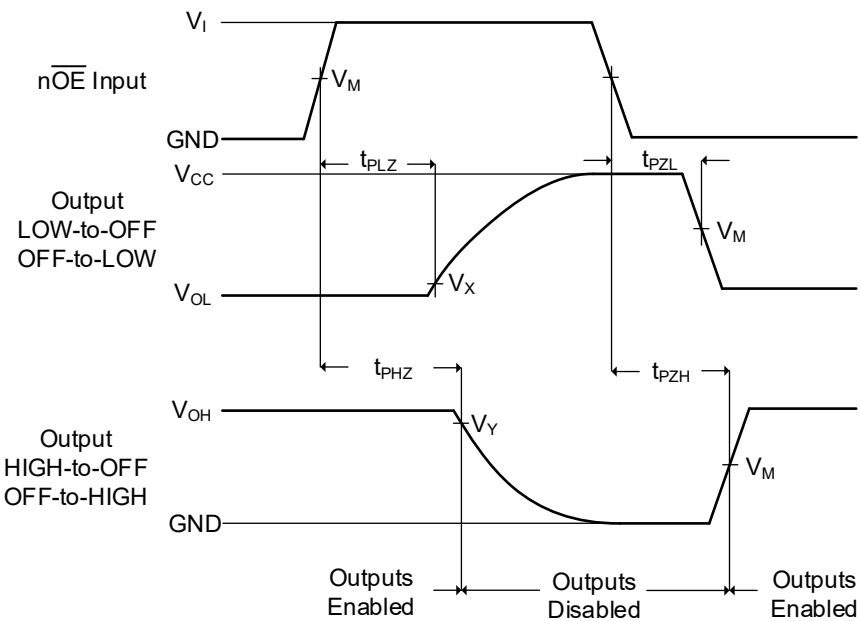
Test Circuit



Measurement points are given in [Table 1](#).

V_{OL} and V_{OH} are typical output voltage levels that occur with the output load.

Figure3. The Data Input (nAn, nBn) to Output (nBn, nAn) Propagation Delay Times



Measurement points are given in [Table 1](#).

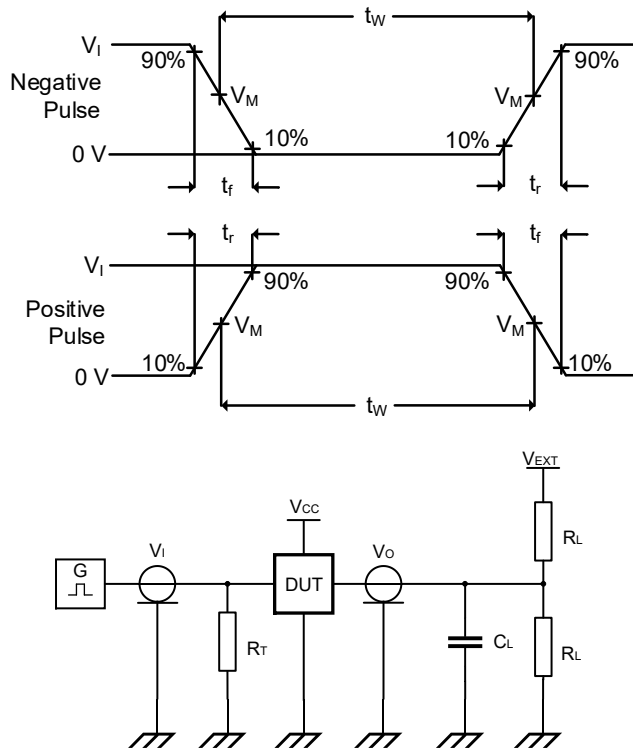
V_{OL} and V_{OH} are typical output voltage levels that occur with the output load.

Figure4. Enable and Disable Times

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Table 1. Measurement Points

Supply Voltage	Input	Output		
$V_{CC(A)}, V_{CC(B)}$	V_M	V_M	V_X	V_Y
1.2V to 1.6V	$0.5 \times V_{CCI}$	$0.5 \times V_{CCO}$	$V_{OL} + 0.1V$	$V_{OH} - 0.1V$
1.65V to 2.7V	$0.5 \times V_{CCI}$	$0.5 \times V_{CCO}$	$V_{OL} + 0.15V$	$V_{OH} - 0.15V$
3.0V to 5.5V	$0.5 \times V_{CCI}$	$0.5 \times V_{CCO}$	$V_{OL} + 0.3V$	$V_{OH} - 0.3V$



Measurement points are given in [Table 2](#).

Definitions test circuit:

R_L = Load resistance;

C_L = Load capacitance including jig and probe capacitance;

R_T = Termination resistance should be equal to output impedance Z_o of the pulse generator;

V_{EXT} = External voltage for measuring switching times.

Figure5. Test Circuit for Measuring Switching Times

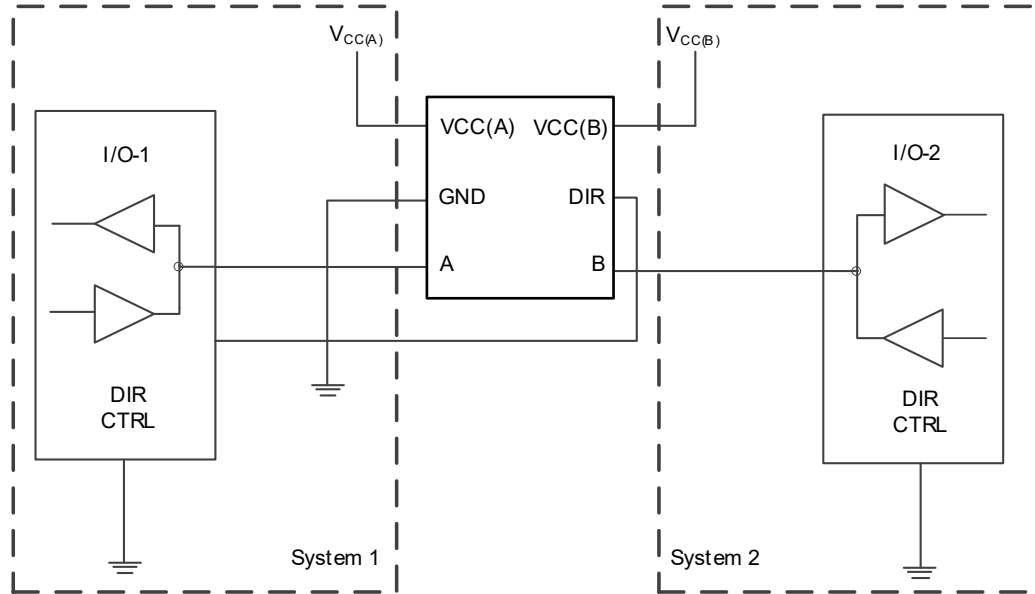
Table 2. Test Data

Supply Voltage	Input		Load		V_{EXT}		
$V_{CC(A)}, V_{CC(B)}$	V_I	$\Delta t/\Delta V^{(15)}$	C_L	R_L	t_{PLH}, t_{PHL}	t_{PZH}, t_{PHZ}	t_{PZL}, t_{PLZ}
1.2V to 5.5V	V_{CCI}	$\leq 1.0ns/V$	15pF	2kΩ	Open	GND	$2 \times V_{CCO}$

Note15: $dV/dt \geq 1.0V/ns$

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Application Circuits



Data transmission from System1 to System2 and then from System 2 to System1:

State	DIR Ctrl	IO-1	IO-2	Description
1	H	Output	Input	System1 data to System2
2	H	Hi-Z	Hi-Z	System2 is getting ready to send data to System1. I/O-1 and I/O-2 are disabled
3	L	Hi-Z	Hi-Z	DIR bit is flipped. I/O-1 and I/O-2 still are disabled
4	L	Input	Output	System2 data to System1

Calculate the enable times using the following formulas:

$$t_{PZH} \text{ (DIR to A)} = t_{PLZ} \text{ (DIR to B)} + t_{PLH} \text{ (B to A)}$$

$$t_{PZL} \text{ (DIR to A)} = t_{PHZ} \text{ (DIR to B)} + t_{PHL} \text{ (B to A)}$$

$$t_{PZH} \text{ (DIR to B)} = t_{PLZ} \text{ (DIR to A)} + t_{PLH} \text{ (A to B)}$$

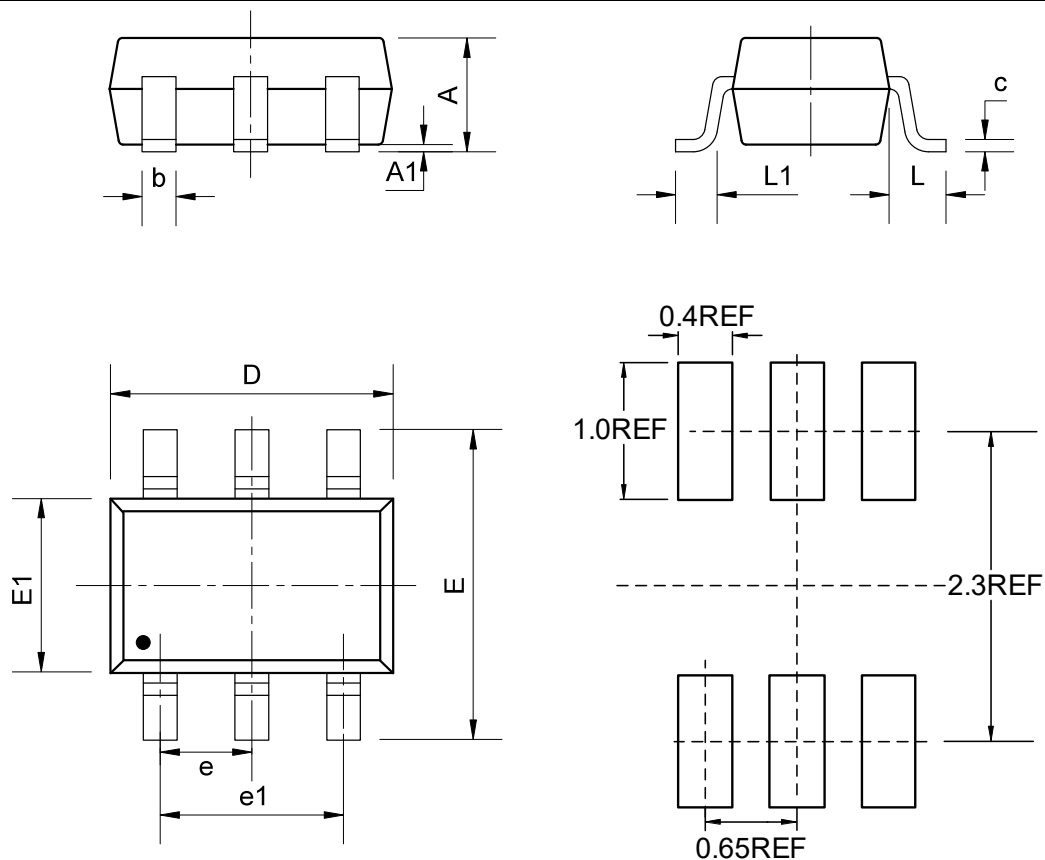
$$t_{PZL} \text{ (DIR to B)} = t_{PHZ} \text{ (DIR to A)} + t_{PHL} \text{ (A to B)}$$

Figure6. Application Circuits

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Package Dimension

SC70-6 (1.3mm × 2.1mm)



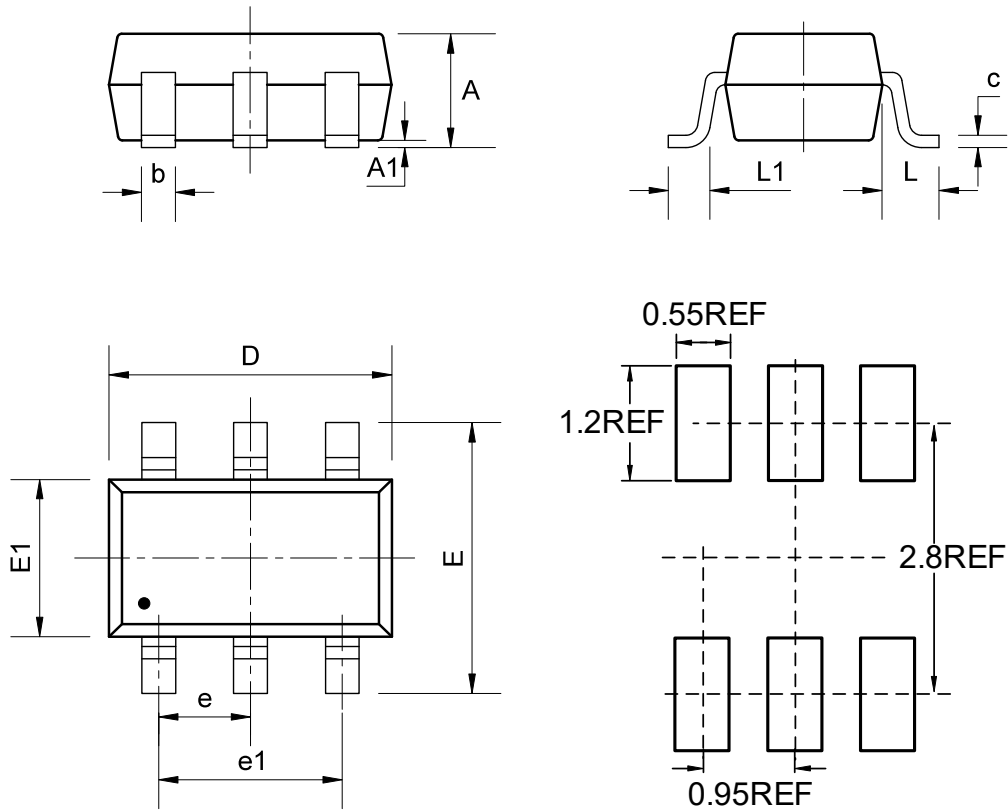
COMMON DIMENSIONS

(Unit: mm)

SYMBOL	MIN	NOM	MAX
A	-	-	1.10
A1	0.00	-	0.15
b	0.15	-	0.35
c	0.08	-	0.20
D	2.00	2.10	2.30
e	0.65BSC		
e1	1.30BSC		
E	2.15	2.30	2.50
E1	1.15	1.30	1.45
L	0.50REF		
L1	0.33REF		

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SOT23-6 (1.6mm × 2.9mm)



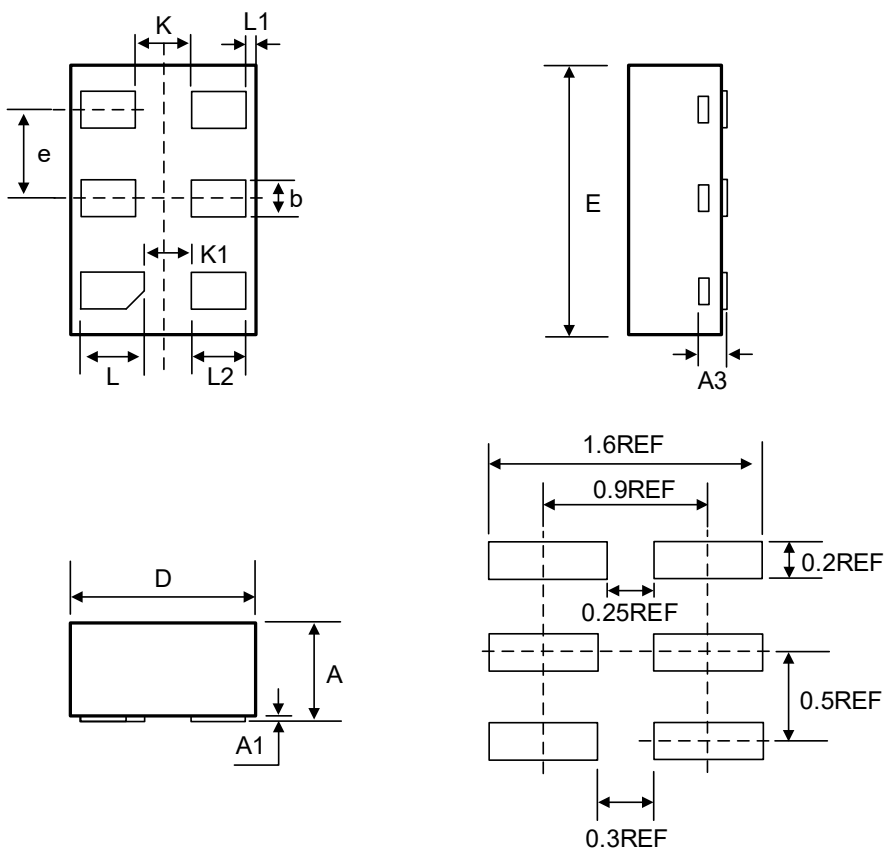
COMMON DIMENSIONS

(Unit: mm)

SYMBOL	MIN	NOM	MAX
A	-	-	1.45
A1	0.00	-	0.15
b	0.28	0.35	0.50
c	0.08	0.15	0.22
D	2.75	2.9	3.05
e	0.90	0.95	1.00
e1	1.80	1.90	2.00
E	2.60	2.80	3.00
E1	1.45	1.6	1.75
L	0.60REF		
L1	0.30	0.45	0.60

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DFN6 (1.0mm × 1.45mm)



COMMON DIMENSIONS

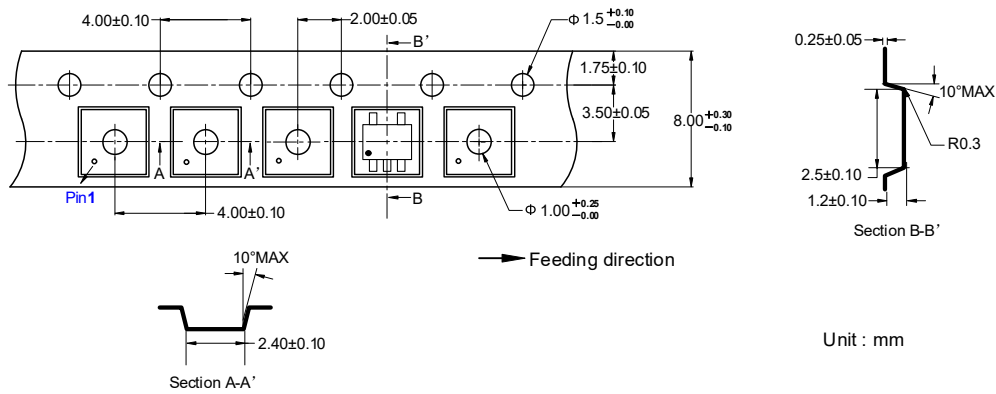
(Unit: mm)

SYMBOL	MIN	NOM	MAX
A	0.450	0.500	0.550
A1	0.000	0.035	0.050
A3	0.152REF		
b	0.150	0.20	0.250
D	0.900	1.000	1.100
E	1.350	1.450	1.550
e	0.500BSC		
K	0.300REF		
K1	0.250REF		
L	0.300	0.350	0.400
L1	0.050REF		
L2	0.250	0.300	0.350

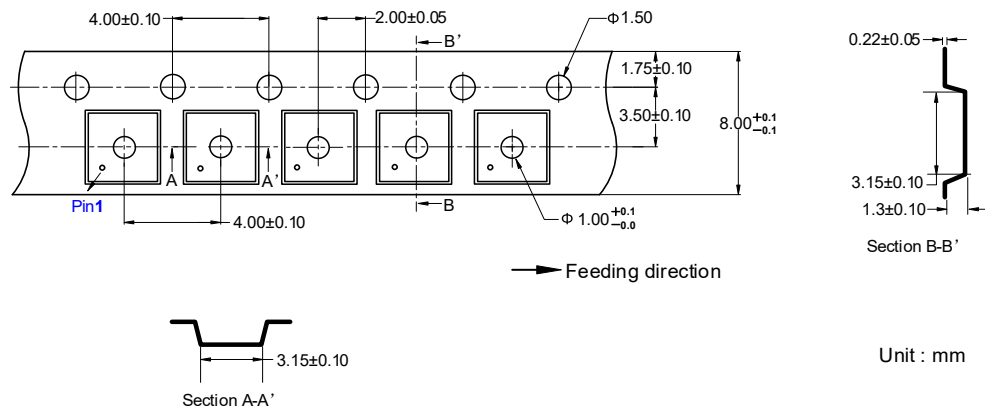
ET74LVCH1T45

Tape Information

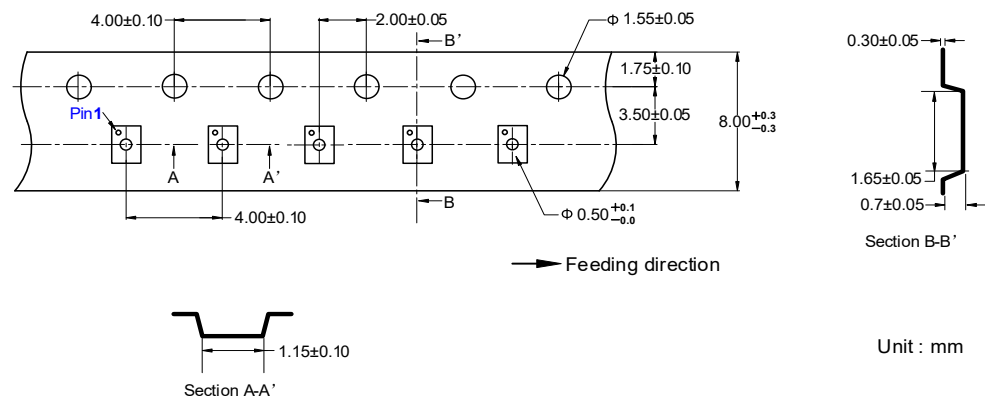
SC70-5 (1.3mm × 2.1mm)



SOT23-5 (1.6mm × 2.9mm)



DFN6 (1.0mm × 1.45mm)



ET74LVCH1T45

Marking Information

45X ET74LVCH1T45 45 = Part Number X = Tracking Number	45X ET74LVCH1T45Y 45 = Part Number X = Tracking Number
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Revision History and Checking Table

Version	Date	Revision Item	Modifier	Function & Spec Checking	Package & Tape Checking
1.0	2025-11-27	Update Format	Yang xiaoxu	Yang xiaoxu	Liu jiaying