

High PSRR Low Noise 300mA LDO

General Description

The ET533XX family are the 300mA LDO with auto discharge function, it uses an advanced CMOS process and a PMOSFET pass device to achieve high power supply rejection ratio (PSRR), low noise, low dropout, low ground current, fast start-up and excellent output accuracy.

The ET533XX family are stable with a 1.0 μ F ceramic output capacitor, uses a precision voltage reference and feedback loop to achieve excellent regulation and transient response.

The ET533XX family offered in a small SOT23-5 / SC70-5 / DFN4 package, which are ideal for small form factor portable equipment.

Features

- Wide Input Voltage Range from 2.0V to 6.0V
- Up to 300mA Load Current
- Standard Fixed Output Voltage Options: 1.2V, 1.35V, 1.5V, 1.7V, 1.8V, 2.5V, 2.8V, 3.0V and 3.3V etc
- Custom voltage options (50mV step options between 1.2V and 5.0V are available upon request)
- Very Low IQ is 42 μ A typical
- Low Dropout is typical 240mV@2.8V at 300mA Load
- Very Low Noise is 45 μ Vrms at 1.2V output
- Ultra-Fast Start-Up Time is 25 μ s typical
- Excellent Load/Line Transient Response
- Part No. and Package

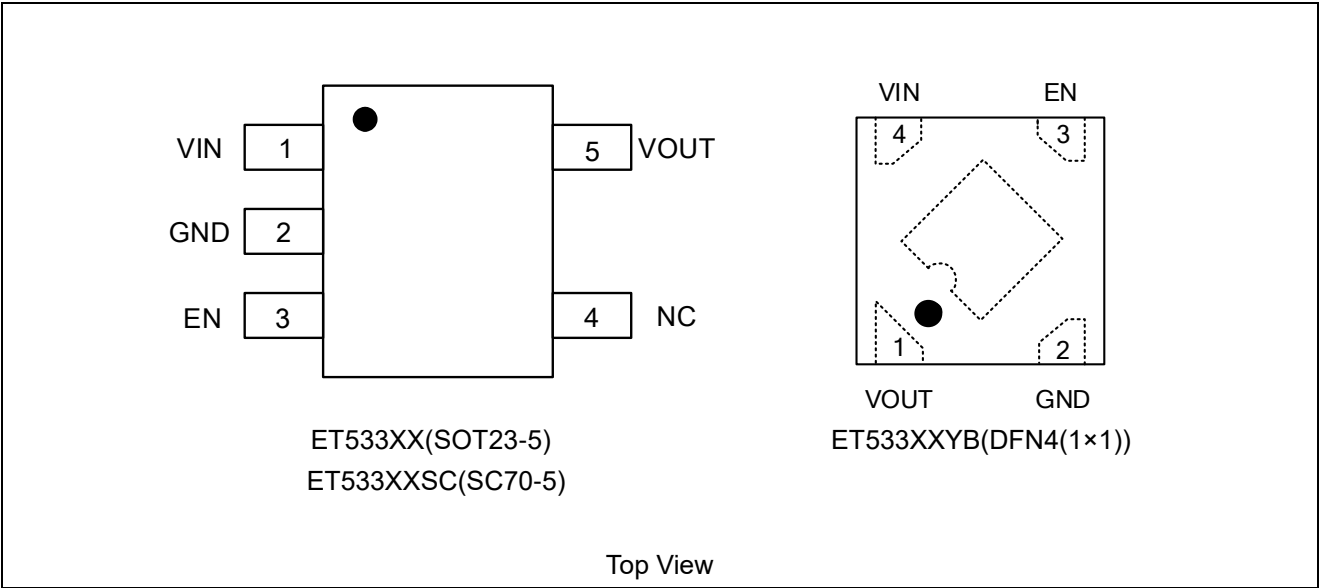
Part No.	Package	MSL
ET533XX	SOT23-5	Level 3
ET533XXSC	SC70-5	Level 3
ET533XXYB	DFN4(1 $\times$ 1)	Level 1

Applications

- Smart Phones and Cellular Phones
- Digital Still Cameras
- Portable Instruments

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Pin Configuration

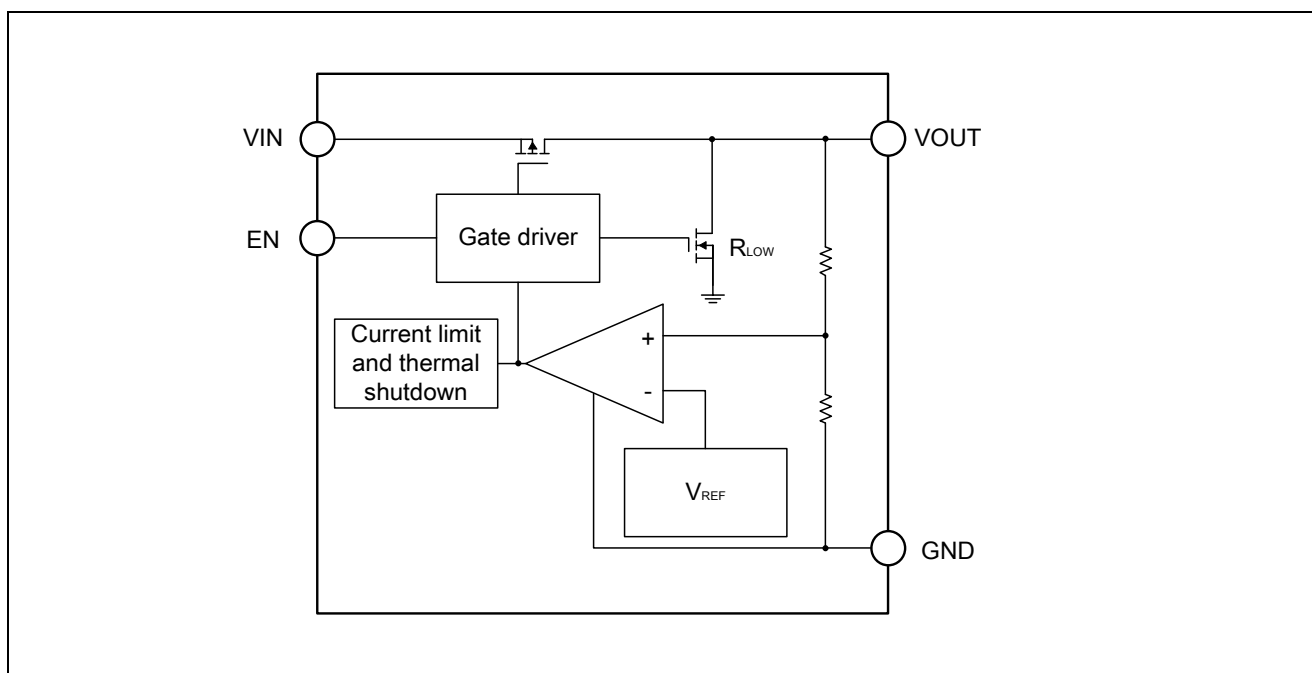


Pin Function

Pin No.		Pin Name	Pin Function
SOT23-5 / SC70-5	DFN4(1×1)		
1	4	VIN	Supply input pin.
2	2	GND	Ground.
3	3	EN	Enable control input, active high. Do not leave EN floating.
4		NC	No connection.
5	1	VOUT	Output pin.
	Thermal pad		Connect to GND or leave floating. Do not connect to any potential other than GND.

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Block Diagram



Functional Description

Input Capacitor

A 1 μ F ceramic capacitor is recommended to connect between **VIN** and **GND** pins to decouple input power supply glitch and noise. The amount of the capacitance may be increased without limit. This input capacitor must be located as close as possible to the device to assure input stability and less noise. For PCB layout, a wide copper trace is required for both **VIN** and **GND**.

Output Capacitor

An output capacitor is required for the stability of the LDO. The recommended output capacitance is from 0.47 μ F to 4.7 μ F, Equivalent Series Resistance (ESR) is from 5m Ω to 100m Ω , and temperature characteristics is X7R or X5R. Higher capacitance values help to improve load/line transient response. The output capacitance may be increased to keep low undershoot/overshoot. Place output capacitor as close as possible to **VOUT** and **GND** pins.

ON/OFF Input Operation

The ET533XX is turned on by setting the **EN** pin high, and is turned off by pulling it low. If this feature is not used, the **EN** pin should be tied to **V_{IN}** pin to keep the regulator output on at all time.

High PSRR and Low Noise

RF circuits such as LNA (low-noise amplifier), up/down-converter, mixer, PLL, VCO, and IF stage, require low noise and high PSRR LDOs. The temperature-compensated crystal oscillator circuit requires very high PSRR at RF power amplifier burst frequency. For instance, minimum 65dB PSRR at 217Hz is recommended for the GSM handsets.

In order to provide good audio quality, the audio power supply for hand-free, game, Hi-Fi, and multimedia applications in cellular phones, require low-noise and high PSRR at audio frequency range (20Hz-20kHz).

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The ET533XX, with PSRR of 75dB at 100Hz, is suitable for most of these applications that require high PSRR and low noise.

Ultra Fast Start-up

After enabled, the ET533XX is able to provide full power in as little as tens of microseconds. This feature will help load circuitry move in and out of standby mode in real time, eventually extend battery life for mobile phones and other portable devices.

Fast Transient Response

Fast transient response LDOs can also extend battery life. To meet this load requirement, the LDO must react very quickly without a large voltage drop or overshoot .

The ET533XX's fast transient response from 0 to 300mA provides stable voltage supply for fast changing load.

Low Quiescent Current

The ET533XX, consuming only around 42 μ A for all input range and output loading, provides great power saving in portable and low power applications.

Current Limit Protection

When output current at the V_{OUT} pin is higher than current limit threshold or the V_{OUT} pin, the current limit protection will be triggered and clamp the output current to approximately 500mA to prevent over-current and to protect the regulator from damage due to overheating.

Thermal Shutdown Protection

Thermal protection disables the output when the junction temperature rises to approximately +155°C, allowing the device to cool down. When the junction temperature reduces to approximately +130°C the output circuitry is enabled again. Depending on power dissipation, thermal resistance, and ambient temperature, the thermal protection circuit may cycle on and off. This cycling limits the heat dissipation of the regulator, protecting it from damage due to overheating.

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Absolute Maximum Ratings

Symbol	Parameters (Items)	Value	Unit
V _{IN}	Input Voltage Range	-0.3 to 6.5	V
V _{OUT}	Output Voltage Range	-0.3 to V _{IN} + 0.3	V
V _{EN}	Enable Voltage Range	-0.3 to 6.5	V
I _{MAX}	Maximum Load Current	500	mA
T _J	Maximum Junction Temperature	-40 to 150	°C
T _{STG}	Storage Temperature	-65 to 150	°C

Thermal Characteristics

Symbol	Package	Parameters	Value	Unit
R _{θJA}	DFN4	Thermal Resistance, Junction-to-Air	250	°C/W
	SOT23-5		250	
	SC70-5		250	
P _{DMAX}	DFN4	Power Dissipation	400	mW
	SOT23-5		400	
	SC70-5		400	

Recommended Operating Conditions

Symbol	Parameters	Min	Max	Unit
V _{IN}	Input Voltage	2.0	6.0	V
I _{OUT}	Output Current	0	300	mA
T _A	Operating Ambient Temperature	-40	85	°C
C _{IN}	Input Ceramic Capacitor Value	0.47	4.7	μF
C _{OUT}	Output Ceramic Capacitor Value	0.47	4.7	μF
ESR	Input and Output Capacitor Equivalent Series Resistance	5	100	mΩ

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Electrical Characteristics

($V_{IN} = V_{EN} = V_{OUT} + 1V$, $T_A = 25^\circ C$ unless otherwise noted)

Symbol	Parameters	Conditions	Min	Typ	Max	Unit
$V_{IN}^{(1)}$	Input Voltage Operation Range		2.0		6.0	V
$V_{DROP}^{(2)}$	Dropout Voltage	$V_{OUT} = 1.2V, I_{OUT} = 300mA$		790	950	mV
		$V_{OUT} = 1.8V, I_{OUT} = 300mA$		350	420	mV
		$V_{OUT} = 2.5V, I_{OUT} = 300mA$		270	330	mV
		$V_{OUT} = 2.8V, I_{OUT} = 300mA$		240	300	mV
		$V_{OUT} = 3.0V, I_{OUT} = 300mA$		230	270	mV
		$V_{OUT} = 3.3V, I_{OUT} = 300mA$		210	250	mV
I_{Q_ON}	DC Supply Quiescent Current	Active mode: $V_{EN} = V_{IN}$		42	70	μA
I_{Q_OFF}	DC Supply Shutdown Current	$V_{EN} = 0V$		0.01	1	μA
V_{OUT}	Regulated Output Voltage	$I_{OUT} = 1mA$, $-40^\circ C \leq T_A \leq 85^\circ C$	-2		2	%
Reg_{LINE}	Output Voltage Line Regulation	$V_{IN} = V_{OUT} + 1V$ to 5.5V, $I_{OUT} = 10mA$		0.03	0.2	%/V
Reg_{LOAD}	Output Voltage Load Regulation	I_{OUT} from 0mA to 300mA		20	40	mV
T_{ON}	Soft-start Time	From enable to power on		25		μs
I_{LIMIT}	Current Limit	$R_{LOAD} = 1\Omega$	300			mA
I_{SHORT}	Output Turn-on Delay Time	$V_{OUT} = 0V$		60		mA
$PSRR^{(3)}$	Power Supply Rejection Ratio	$f = 1kHz, C_{OUT} = 1\mu F$, $I_{OUT} = 20mA$		75		dB
		$f = 10kHz, C_{OUT} = 1\mu F$, $I_{OUT} = 30mA$		65		dB
$e_N^{(3)}$	Output Noise	10Hz to 100kHz, $I_{OUT} = 200mA, V_{OUT} = 2.8V$, $C_{OUT} = 1\mu F$		75		$\mu VRMS$
		10Hz to 100kHz, $I_{OUT} = 200mA, V_{OUT} = 1.2V$, $C_{OUT} = 1\mu F$		45		
V_{IL}	EN Low Threshold	$V_{IN} = 2.0V$ to 6.0V, V_{EN} falling until the output is disabled			0.3	V
V_{IH}	EN High Threshold	$V_{IN} = 2.0V$ to 6.0V, V_{EN} rising until the output is enabled	1.2			V
I_{EN}	EN Pin Input Current	$V_{EN} = 0V$		0	0.1	μA

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Electrical Characteristics (Continued)

($V_{IN} = V_{EN} = V_{OUT} + 1V$, $T_A = 25^\circ C$ unless otherwise noted)

Symbol	Parameters	Conditions	Min	Typ	Max	Unit
R_{PD}	EN pull-down resistance		0.8	1	1.3	$M\Omega$
R_{LOW}	Output resistance of auto discharge at off state	$V_{EN} = 0V$, $V_{IN} = 4V$		30		Ω
$T_{TSD}^{(3)}$	Over-temperature Shutdown Threshold	T_J rising		155		$^\circ C$
$T_{HYS}^{(3)}$	Over-temperature Shutdown Hysteresis	T_J falling from shutdown		20		$^\circ C$

Notes:

1: The maximum input voltage should take into account the maximum power consumption ($P_{D(MAX)}$). The calculation formula is as follows:

$$P_{D(MAX)} = (V_{IN(MAX)} - V_{IN}) \times I_{OUT}$$

The maximum power consumption of the DFN4 is 400mW.

$$V_{IN(MAX)} = 400mW / I_{OUT} + V_{IN}$$

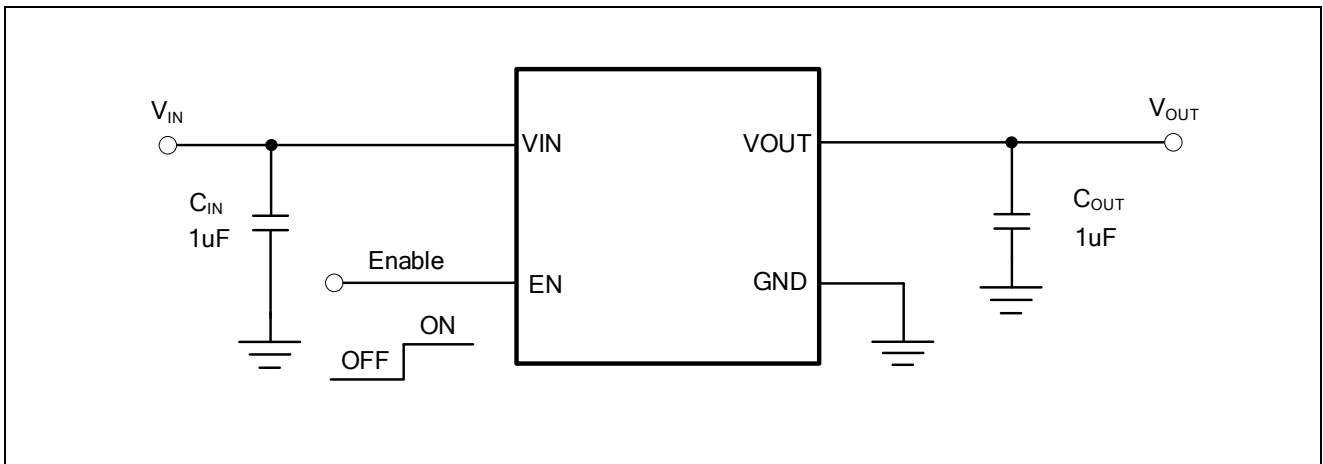
For example, ET53312YB:

If $I_{OUT} = 200mA$, The maximum input voltage is $V_{IN(MAX)} = 400mW / 200mA + 1.2 = 3.2V$

2: V_{DROP} FT test method: test the V_{OUT} voltage at $V_{IN} = V_{OUT} + V_{DROP MAX}$ with output current 300mA.

3: Guaranteed by design and characterization. not a FT item.

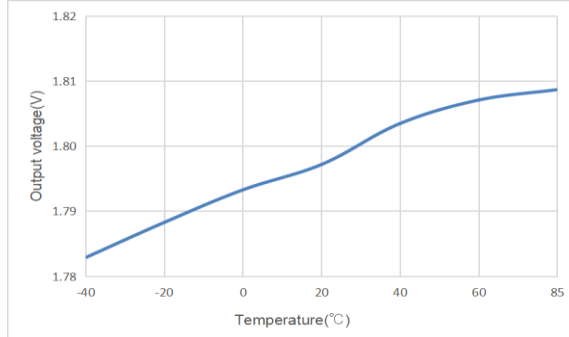
Application Circuits



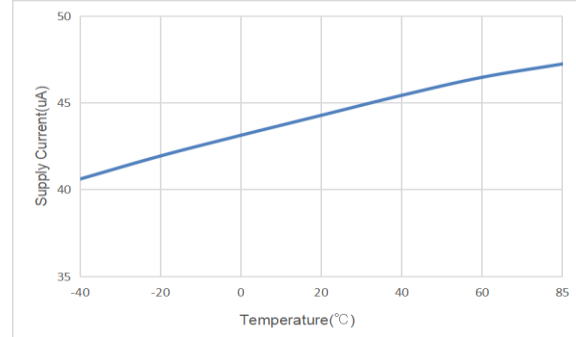
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Typical Characteristics

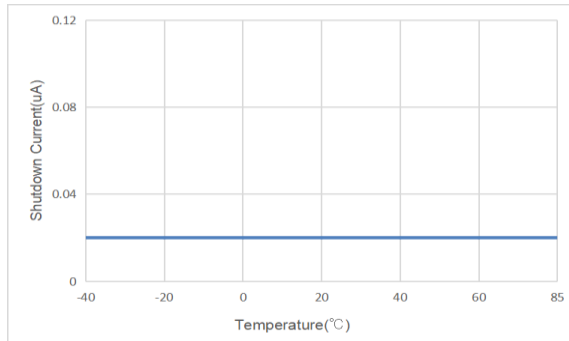
($V_{OUT} = 1.8V$, $V_{IN} = 2.8V$, $I_{OUT} = 1mA$, $C_{IN} = \text{Ceramic } 1.0\mu F$, $C_{OUT} = \text{Ceramic } 1.0\mu F$, $T_A = -40^{\circ}C \sim 85^{\circ}C$)



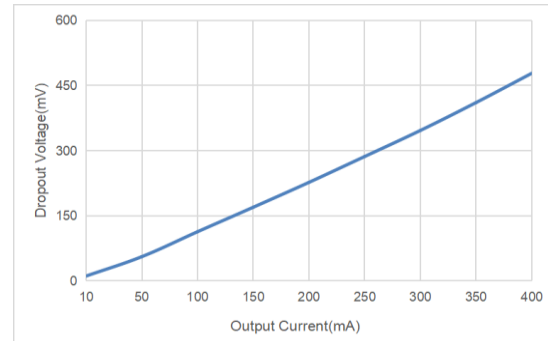
Output Voltage VS Temperature



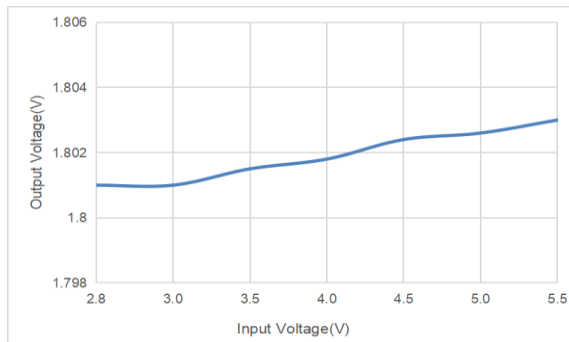
Supply Current VS Temperature



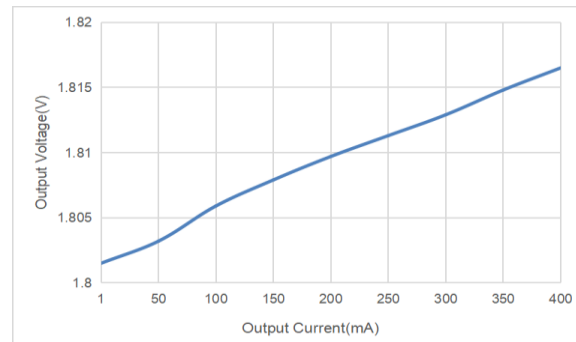
Shutdown Current VS Temperature



Dropout Voltage VS Output Current



Output Voltage VS Input Voltage

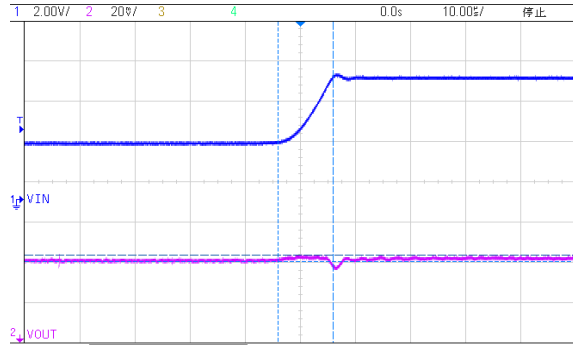


Output Voltage VS Output Current

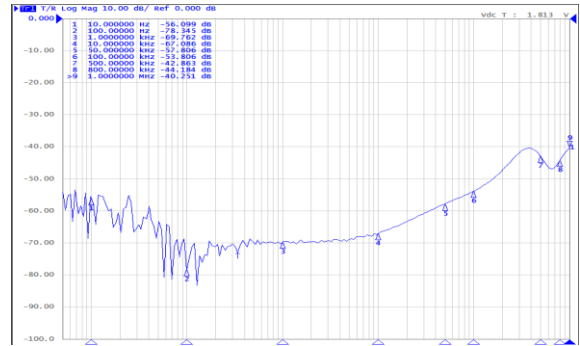
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Typical Characteristics (Continued)

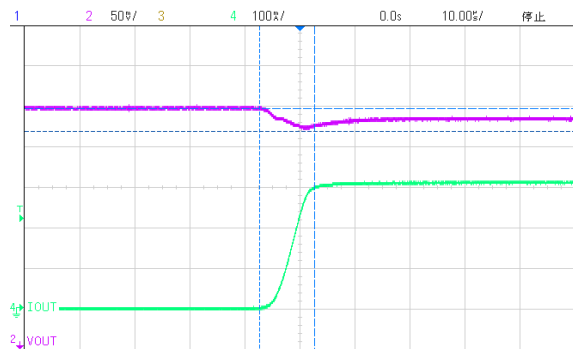
($V_{OUT} = 1.8V$, $V_{IN} = 2.8V$, $I_{OUT} = 1mA$, $C_{IN} = \text{Ceramic } 1.0\mu F$, $C_{OUT} = \text{Ceramic } 1.0\mu F$, $T_A = -40^{\circ}C \sim 85^{\circ}C$)



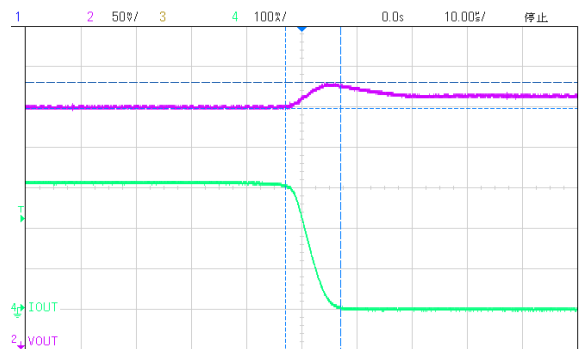
Input Transient(2.8~6.0V $t=10\mu s$ 1mA)



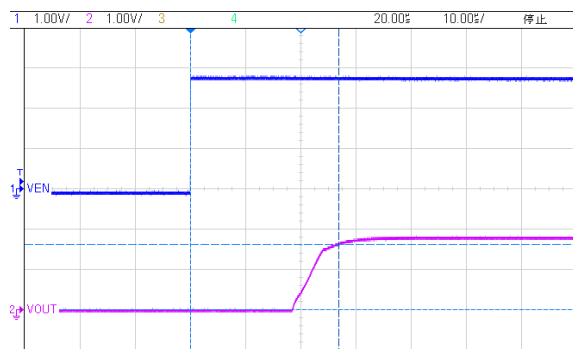
PSRR VS Output Current



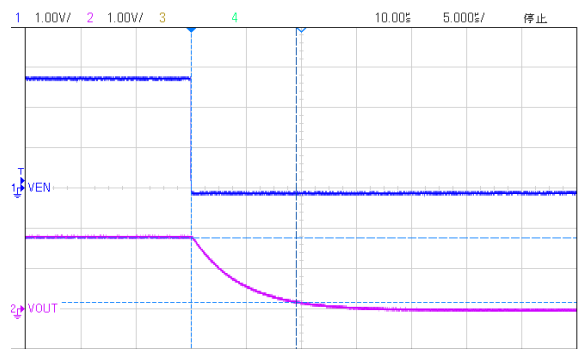
Load Transient(0mA~300mA $t=10\mu s$)



Load Transient(300mA~0mA $t=10\mu s$)



T_{ON} $I_{OUT}=1mA$

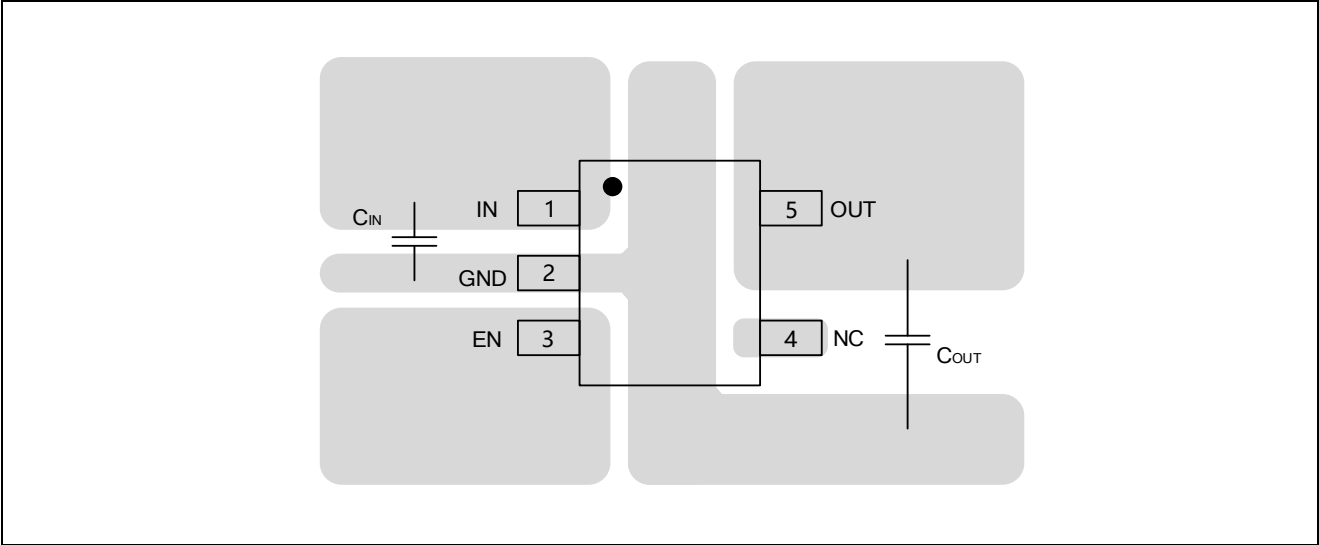


T_{OFF} $I_{OUT}=1mA$

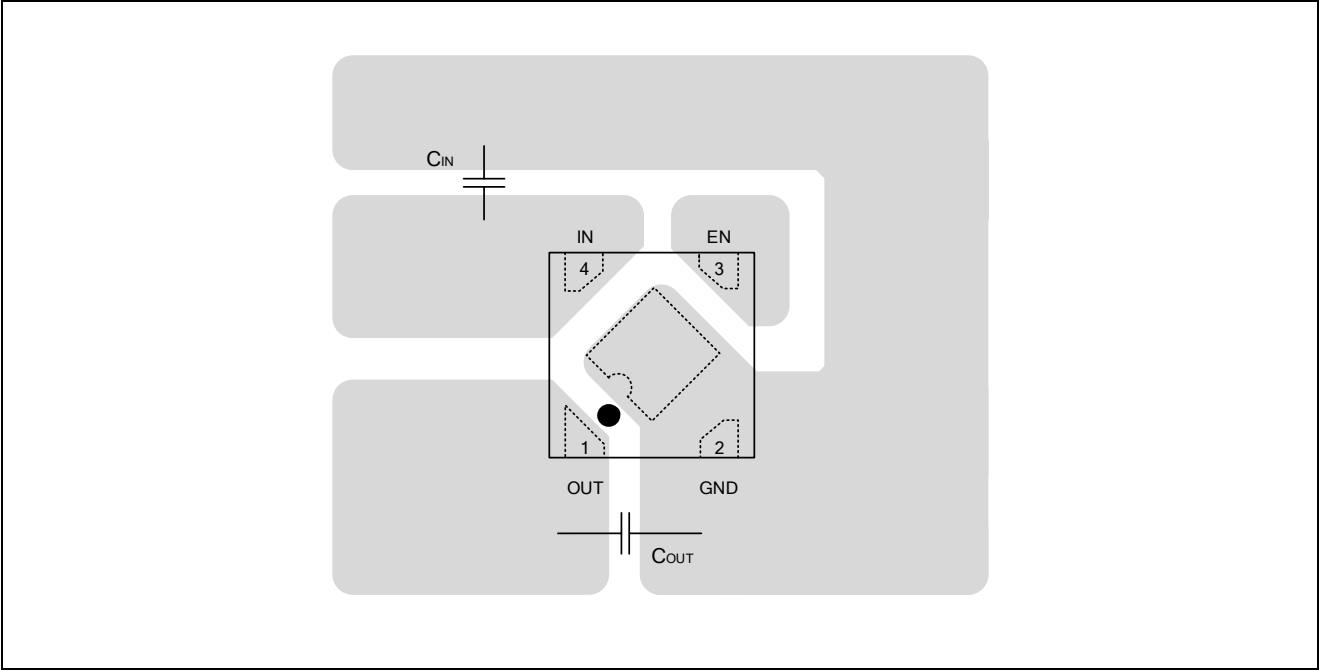
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PCB Layout Guide

SOT23-5/SC70-5



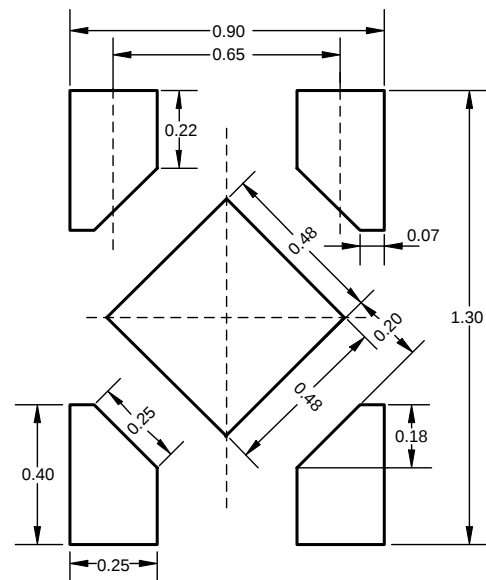
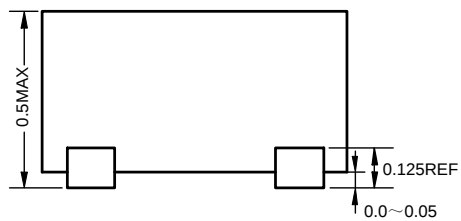
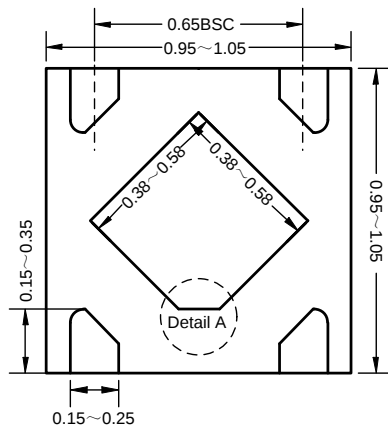
DFN4(1×1)



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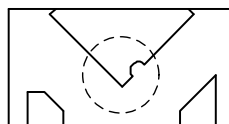
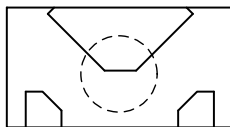
Package Dimension

DFN4(1×1)



Recommended Land Pattern

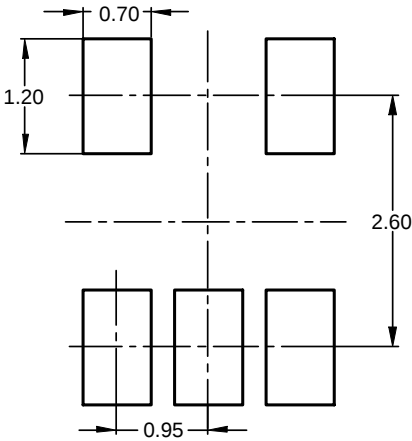
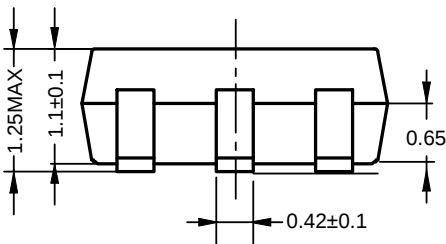
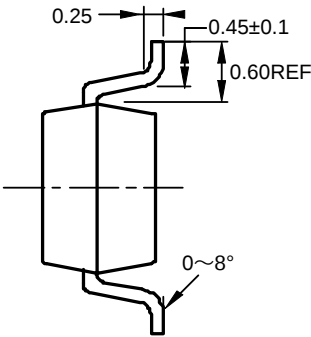
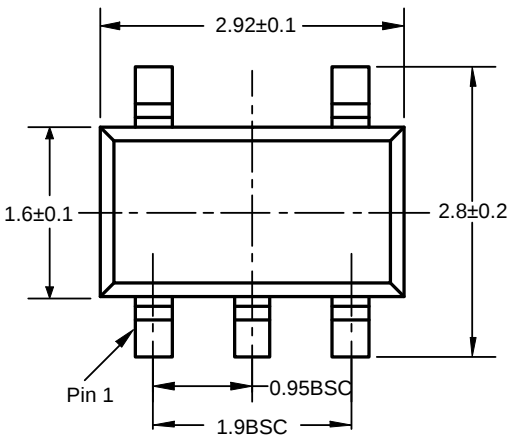
Detail A: (PIN1 shape)



Unit: mm

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SOT23-5

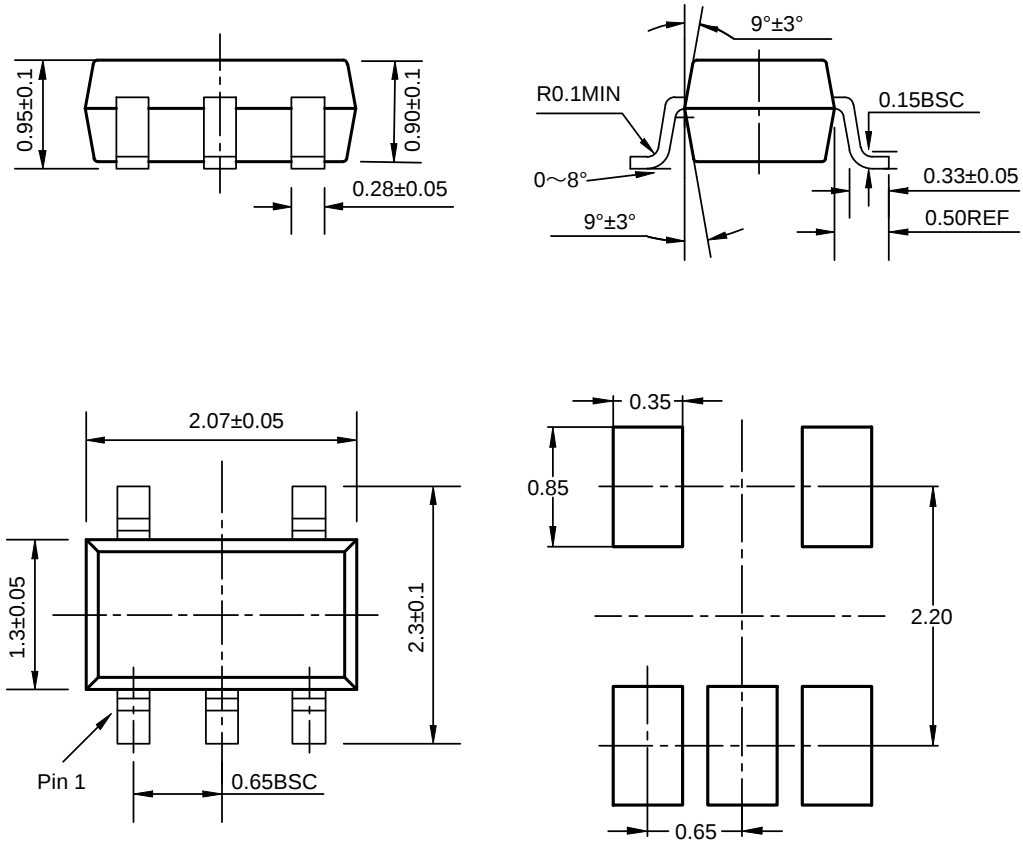


Recommended Land Pattern

Unit: mm

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SC70-5



Recommended Land Pattern

Unit: mm

Revision History and Checking Table

Version	Date	Revision Item	Modifier	Function & Spec Checking	Package & Tape Checking
2.1	2018-01-17	Updated SOT89-3 Part No.	Wuxj	Wuxj	Zhuji
2.2	2019-02-13	Change PSRR from 70 to 75	Wuxj	Wuxj	Zhuji
2.2	2020-06-16	Add characteristic chart	Wuxj	Wuxj	Zhuji
2.4	2021-01-12	Update Package size	Wuxj	Wuxj	Zhuji
2.5	2022-12-06	Update Typeset	Pengjj	Liuxm	Liuji
2.6	2023-10-26	Update package	Pengjj	Liuxm	Liuji
2.7	2025-10-6	Update device information	Shib	Liuxm	Liuji
2.8	2025-10-27	Removed SOT343 DFN1216-4	Shib	Liuxm	Liuji