30V Dual P-Channel Enhancement MOSFET

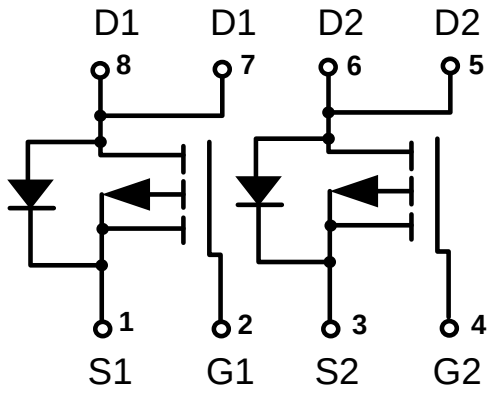
Features

- V_{DS}= -30V, I_D = -5A
  $R_{DS(on)}$ < 60mΩ @ V_{GS} = -10V
  $R_{DS(on)}$ < 90mΩ @ V_{GS} = -4.5V
- Trench Power LV MOSFET Technology

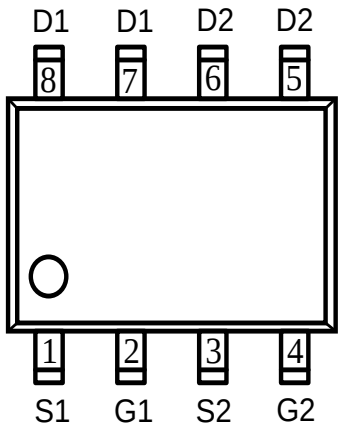
Mechanical Characteristics

- SOP-8L Package
- Marking : Making Code
- RoHS Compliant

Schematic & PIN Configuration



Device Symbol



SOP-8L(Top View)

Absolute Maximum Ratings (T_A = 25°C, unless otherwise noted)

Parameter		Symbol	Value	Unit
Drain-Source Voltage		V_{DS}	-30	V
Gate-Source Voltage		V_{GS}	±20	V
Continuous Drain Current	T_A=25°C	I_D	-5	A
Pulsed Drain Current [1]		I_{DM}	-20	A
Total Power Dissipation		P_D	2.3	W
Operating Junction and Storage Temperature Range		T_J ,T_{STG}	-55 to +150	°C

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal Resistance from Junction to Ambient [2]	$R_{\theta JA}$	54.3	°C/W

Electrical Characteristics (T_J = 25°C, unless otherwise noted)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	B_{VDSS}	V_{GS} = 0V, I_D = -250μA	-30	-	-	V
Gate-body Leakage current	I_{GSS}	V_{DS} = 0V, V_{GS} = ±20V	-	-	±100	nA
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} = -30V, V_{GS} = 0V	-	-	-1	μA
Gate-Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = -250μA	-1.0	-1.5	-2.1	V
Drain-Source On-Resistance (3)	$R_{DS(on)}$	V_{GS} = -10V, I_D = -4.1A	-	42	60	mΩ
		V_{GS} = -4.5V, I_D = -3A	-	57	90	
Dynamic Characteristics (4)						
Input Capacitance	C_{iss}	V_{DS} = -15V, V_{GS} =0V, f =1MHz	-	530	-	pF
Output Capacitance	C_{oss}		-	70	-	
Reverse Transfer Capacitance	C_{rss}		-	56	-	
Switching Characteristics (4)						
Total Gate Charge	Q_g	V_{GS} = -10V,I_D = -4.1A, V_{DS} = -15V	-	10	-	nC
Gate-Source Charge	Q_{gs}		-	2	-	
Gate-Drain Charge	Q_{gd}		-	2.8	-	
Turn-On Delay Time	$t_{d(on)}$	V_{GS} = -10V , V_{DD}= -15V, R_G = 6Ω, I_D = -4.1A,	-	6.9	-	ns
Rise Time	t_r			12		
Turn-Off Delay Time	$t_{d(off)}$		-	19	-	
Fall Time	t_f		-	7.5	-	
Source-Drain Body Diode Characteristics						
Diode Forward Voltage (3)	V_{SD}	I_S = -1.7A, V_{GS} = 0V	-	-	-1.2	V
Continuous Source Current	I_S		-	-	-5	A

Note1：Repetitive rating, pulse width limited by junction temperature $T_{J(MAX)}$=150°C

Note2：The data tested by surface mounted on a 1 inch2 FR-4 board with 2OZ copper, The value in any given application depends on the user's specific board design.

Note3：Pulse Test: Pulse width≤300μs, duty cycle≤2%.

Note4：This value is guaranteed by design hence it is not included in the production test.

Typical Characteristics

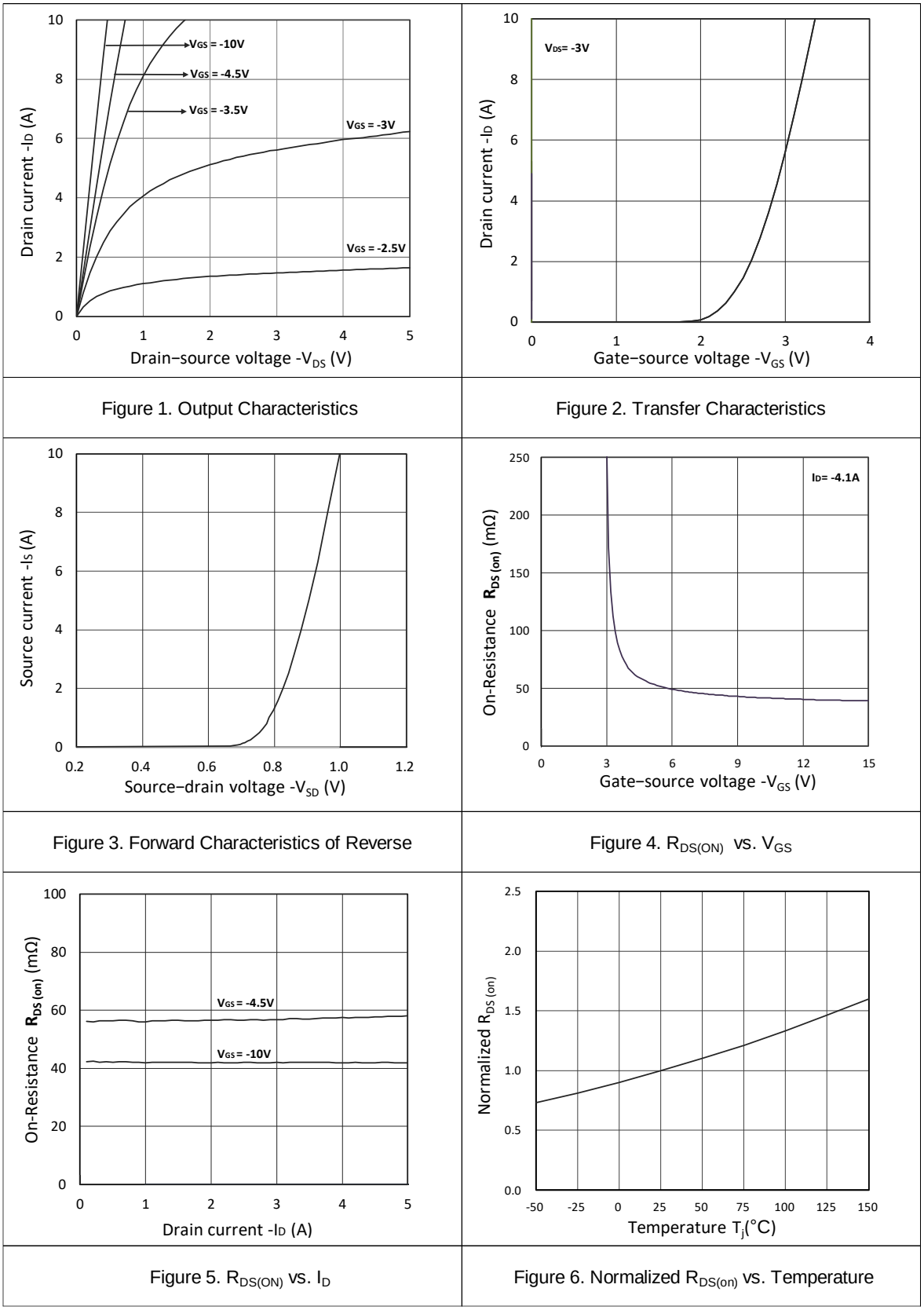


Figure 1. Output Characteristics

Figure 2. Transfer Characteristics

Figure 3. Forward Characteristics of Reverse

Figure 4. $R_{DS(ON)}$ vs. V_{GS}

Figure 5. $R_{DS(ON)}$ vs. I_D

Figure 6. Normalized $R_{DS(on)}$ vs. Temperature

Typical Characteristics(Continued)

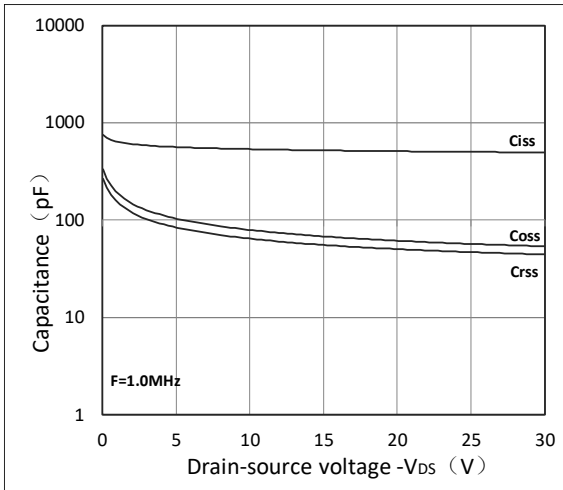


Figure 7. Capacitance Characteristics

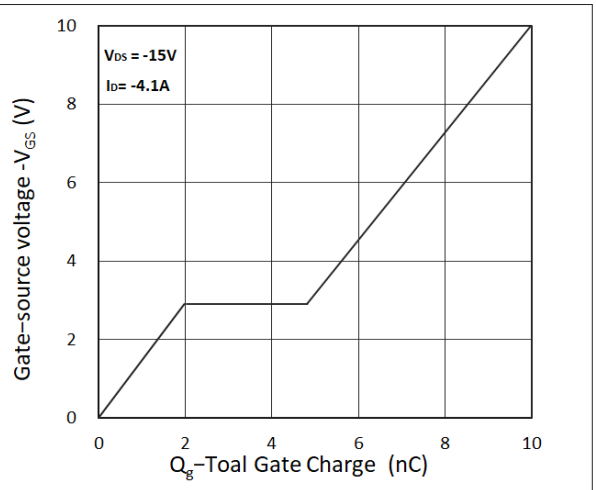
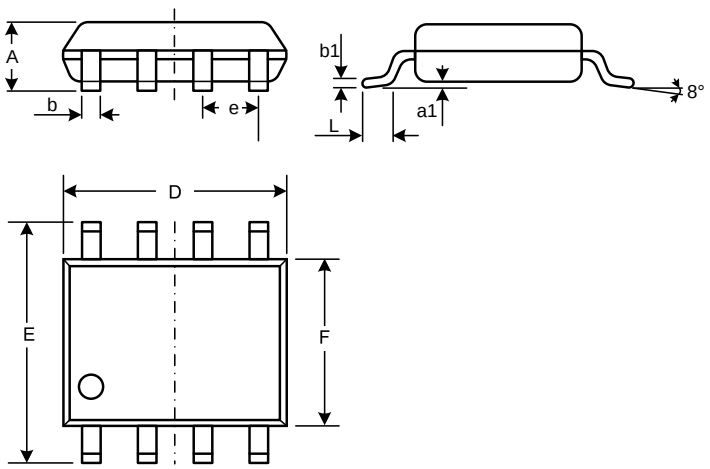


Figure 8. Gate Charge Characteristics

Package Dimension

SOP-8L

PACKAGE OUTLINE



SYMBOL	MILLIMETER	
	MIN	MAX
A	1.35	1.75
a1	0.05	0.25
b	0.31	0.51
b1	0.16	0.25
D	4.70	5.15
E	5.75	6.25
e	1.07	1.47
F	3.70	4.10
L	0.40	1.27

Ordering Information

Part	Package	Marking	Packing Information
EM03DP50A	SOP-8L	4953	4k/Reel

Revision History and Checking Table

Version	Date	Revision Item	Modifier	Function & Spec Checking	Package & Tape Checking
1.0	2018-08-07	Released Version	Qi Shu Kun	Qi Shu Kun	Liu Jia Ying