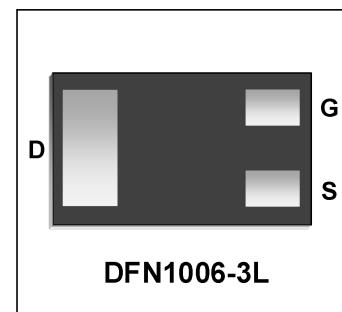
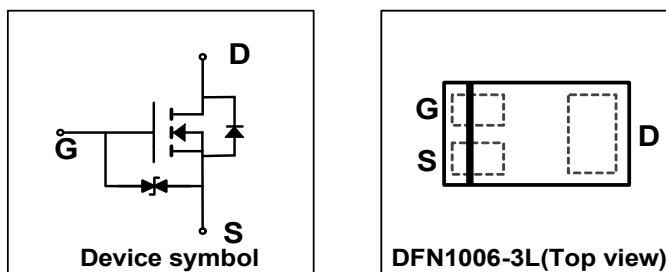



N-Channel Trench MOSFET
Features

- $V_{DS} = 30 \text{ V}$, $I_D = 0.93 \text{ A}$
 $R_{DS(on)} < 0.50 \Omega @ V_{GS} = 4.5 \text{ V}$
 $R_{DS(on)} < 0.60 \Omega @ V_{GS} = 2.5 \text{ V}$
- Very Fast Switching
- Trench MOSFET Technology
- Low Threshold Voltage
- Pb Free Device
- ESD Protected
- MSL1


Mechanical Characteristics

- DFN1006-3L Package
- Marking : Making Code
- RoHS Compliant

Schematic & PIN Configuration

Absolute Maximum Rating

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	30	V
Continuous Drain Current ¹	I_D	930	mA
Gate-Source Voltage	V_{GS}	± 12	V
Power Dissipation ¹	P_D	715	mW
Pulsed Drain Current ($t_p = 10 \mu\text{s}$)	I_{DM}	3.7	A
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 to +150	°C
Junction-to-Ambient – Steady State ¹	$R_{\theta JA}$	305	°C/W

Electrical Characteristics ($T_{amb}=25^{\circ}C$ unless otherwise noted)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0\text{ V}, I_D=250\mu\text{A}$	30	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	0.5	1.0	1.5	V
Drain Cut-off Current	I_{DSS}	$V_{DS}=30\text{ V}, V_{GS}=0\text{ V}$	-	-	1	μA
Gate leakage Current	I_{GSS}	$V_{GS}=\pm 10\text{ V}, V_{DS}=0\text{ V}$	-	-	± 10	μA
Drain-Source on-state Resistance ²	$R_{DS(on)}$	$V_{GS}=4.5\text{ V}, I_D=0.6\text{ A}$	-	0.32	0.50	Ω
		$V_{GS}=2.5\text{ V}, I_D=0.3\text{ A}$	-	0.41	0.60	
Dynamic characteristics						
Input Capacitance	C_{iss}	$V_{DS}=25\text{ V}, V_{GS}=0\text{ V}, f=1\text{ MHz}$	-	37	-	pF
Output Capacitance	C_{oss}		-	8.6	-	
Reverse Transfer Capacitance	C_{rss}		-	5.4	-	
Switching Characteristics						
Total Gate Charge	Q_g	$V_{DS}=15\text{ V}, V_{GS}=4.5\text{ V}, I_D=1.0\text{ A}$	-	0.65	-	nC
Gate-Source Charge	Q_{gs}		-	0.14	-	
Gate-Drain Charge	Q_{gd}		-	0.18	-	
Turn-on Delay Time	$t_{d(on)}$	$V_{DS}=15\text{ V}, V_{GEN}=4.5\text{ V}, R_G=6\Omega, R_L=15\Omega$	-	6.5	-	ns
Rise Time	t_r		-	9.5	-	
Turn-off Delay Time	$t_{d(off)}$		-	14	-	
Fall Time	t_f		-	5.5	-	
Drain-Source Body Diode Characteristics						
Diode Forward Voltage ²	V_{SD}	$I_S=0.6\text{ A}, V_{GS}=0\text{ V}$	-	-	1.2	V

Note:

1: Surface-mounted on FR4 board using 1 in sq pad size (Cu area = 1.127 in sq [1 oz] including traces)

2: Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$.

Typical Characteristics

Figure 1. Output Characteristics

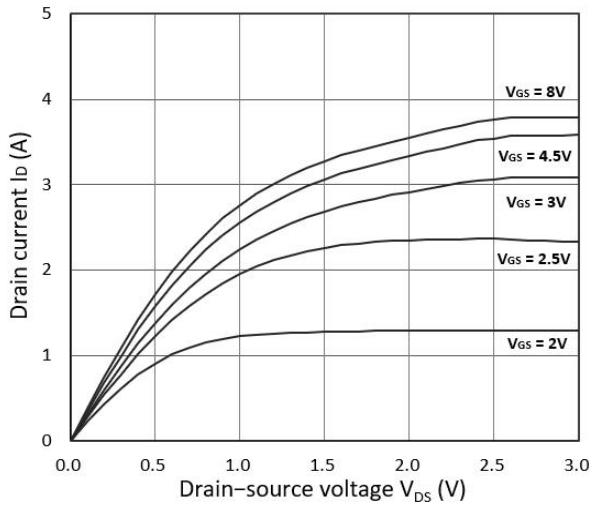
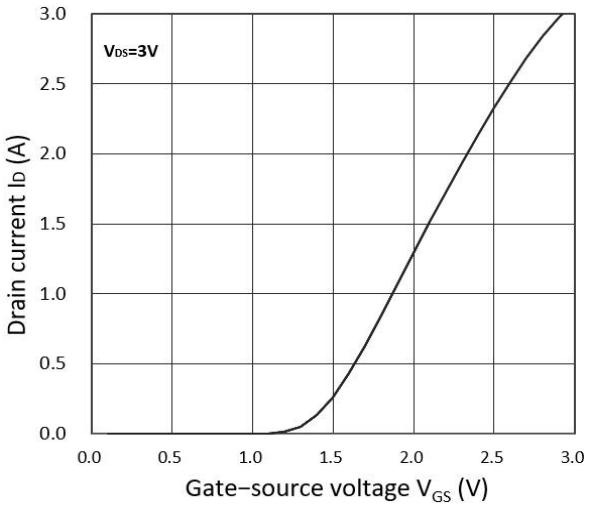
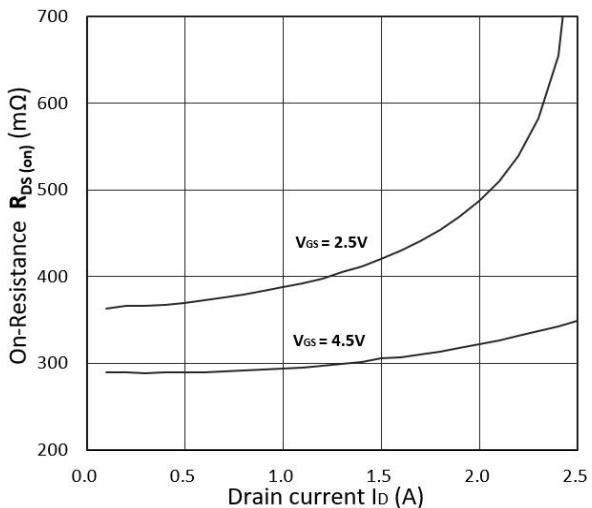
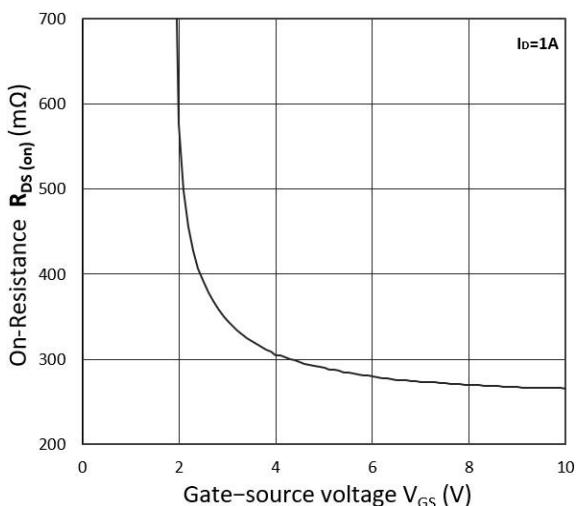
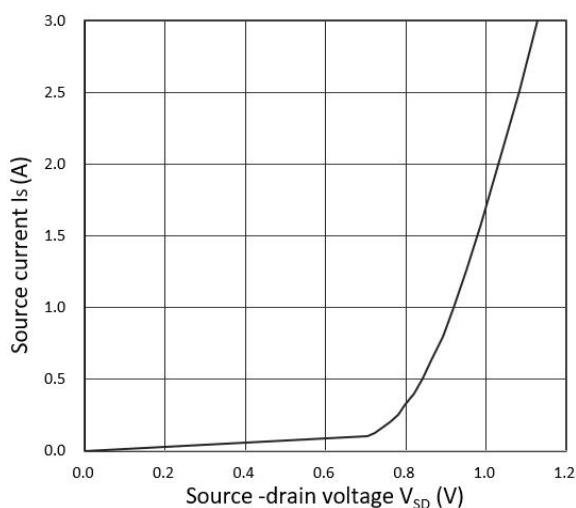
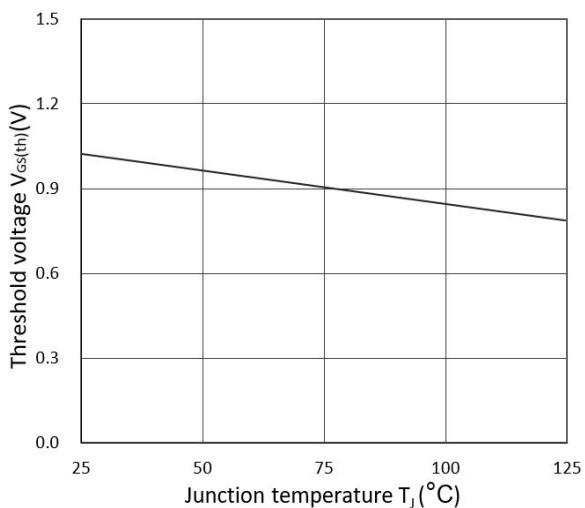
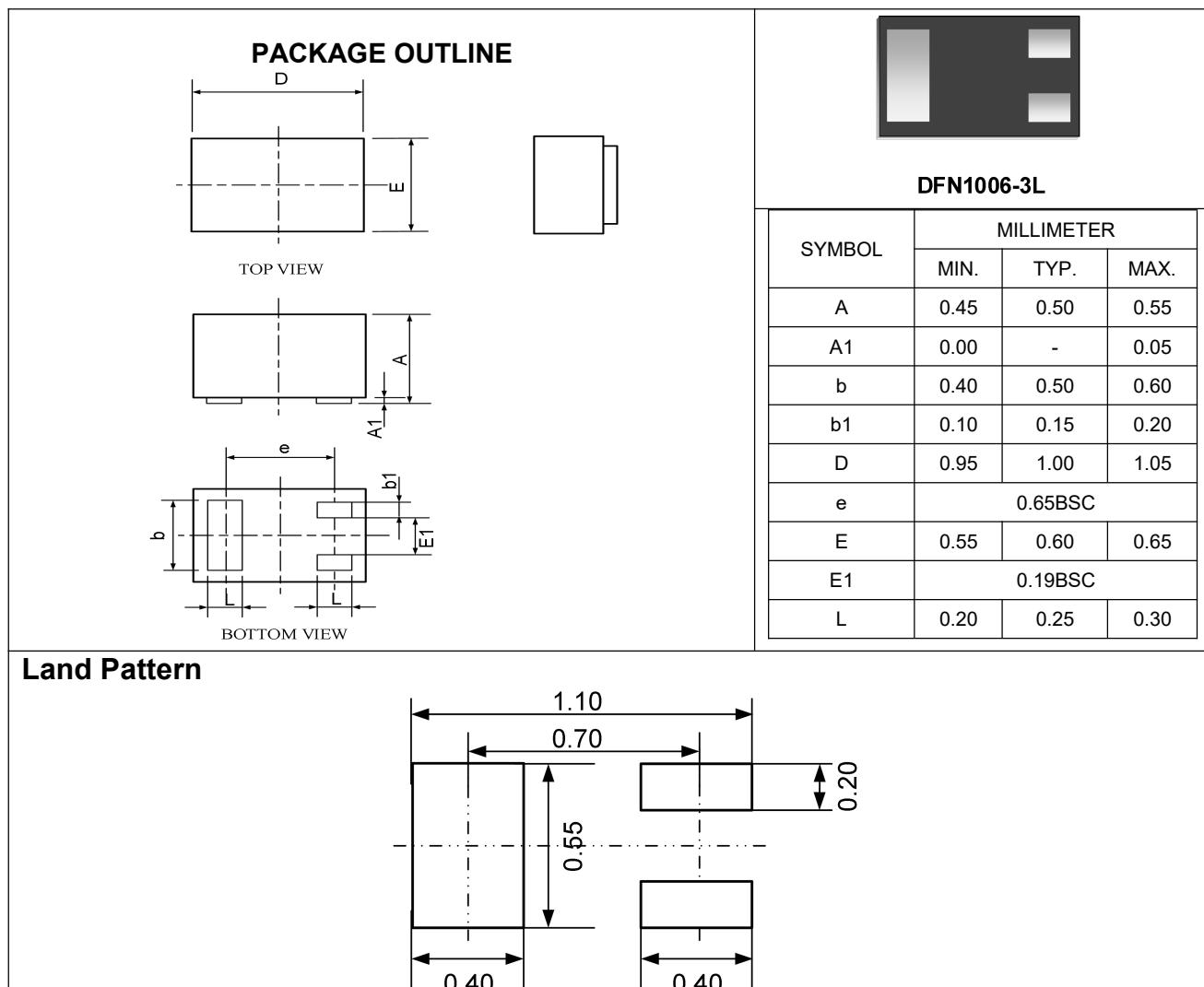


Figure 2. Transfer Characteristics

Figure 3. $R_{DS(on)}$ vs. I_D Figure 4. $R_{DS(on)}$ vs. V_{GS} Figure 5. I_S vs. V_{SD} Figure 6. $V_{GS(th)}$ vs. T_J 

Outline Drawing – DFN1006-3L



Marking Codes

Part Number	EM03N09FX
Marking Code	N5

Package Information

Qty: 10k/Reel

Revision History

No.	Version	Date	Revision Item	Request	Function and characteristic checking	Package dimension checking	Typos checking
1	1.0	2018-05-24	Released Version	Qi Shu Kun	Yin Peng	Ge Zheng Bing	Liu Jia Ying