



Features

- Small Body Outline Dimensions:
0.039"x 0.024"(1.0 mm x 0.60 mm)
- Protects one I/O or power line
- Low Clamping Voltage
- Working Voltage: 5V
- Low Leakage Current
- Response Time is Typically < 1 ns

IEC COMPATIBILITY (EN61000-4)

- IEC 61000-4-2 (ESD) ±30kV (air), ±30kV (contact)
- IEC 61000-4-4 (EFT) 40A (5/50ns)
- IEC 61000-4-5 (Lightning) 30A (8/20μs)

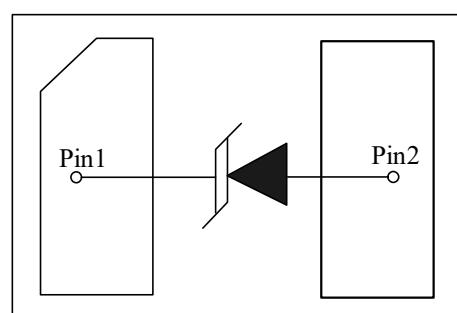
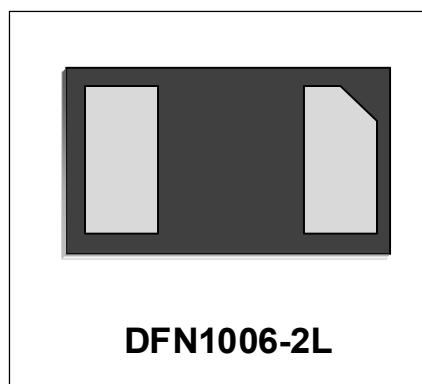
Mechanical Characteristics

- DFN1006-2L package
- Molding compound flammability rating:
UL 94V-0
- Marking: Marking Code
- Packaging: Tape and Reel per EIA 481
- RoHS Compliant

Applications

- Laptop Computers
- Cellular Phones
- Digital Cameras
- Personal Digital Assistants (PDAs)

Schematic & PIN Configuration

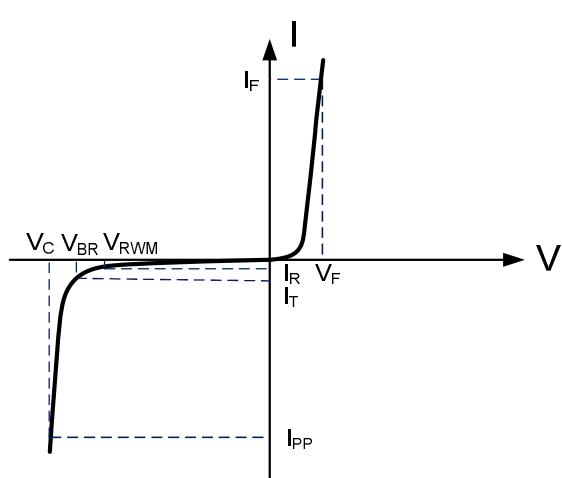


Absolute Maximum Rating

Rating	Symbol	Value	Units
Peak Pulse Power ($t_p = 8/20\mu s$)	P_{PP}	450	Watts
Operating Temperature	T_J	-55 to +125	°C
Storage Temperature	T_{STG}	-55 to +150	°C

Electrical Parameters (T=25°C)

Symbol	Parameter
I_{PP}	Reverse Peak Pulse Current
V_C	Clamping Voltage @ I_{PP}
V_{RWM}	Reverse Stand-Off Voltage
I_R	Reverse Leakage Current @ V_{RWM}
V_{BR}	Breakdown Voltage @ I_T
I_T	Test Current
I_F	Forward Current
V_F	Forward Voltage @ I_F



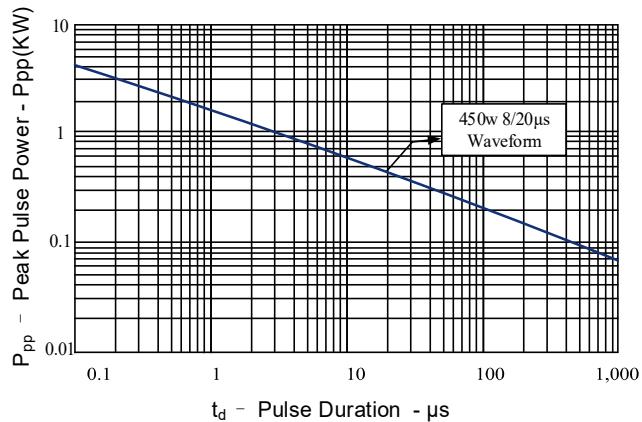
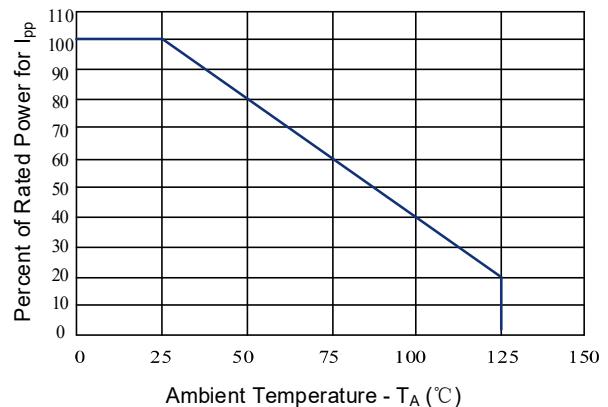
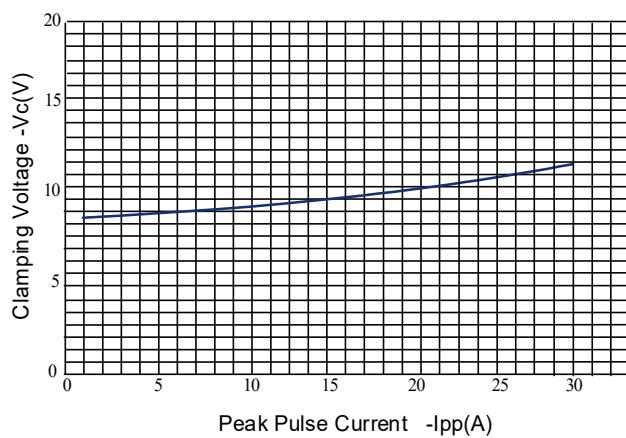
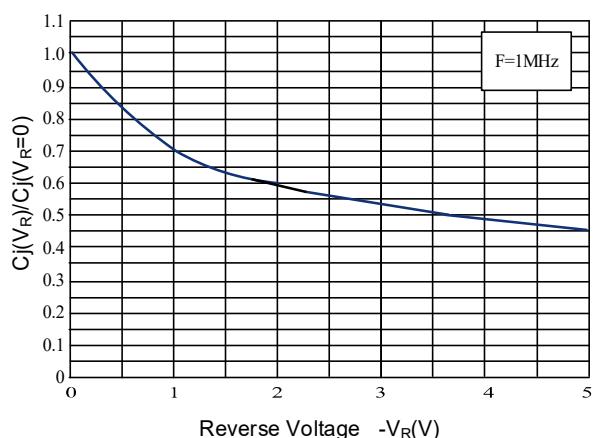
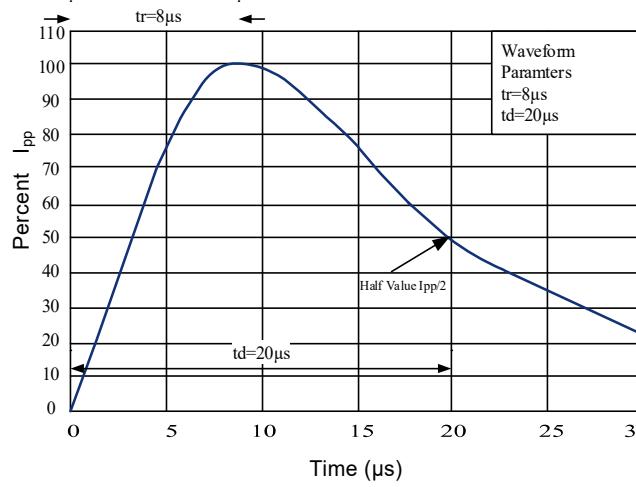
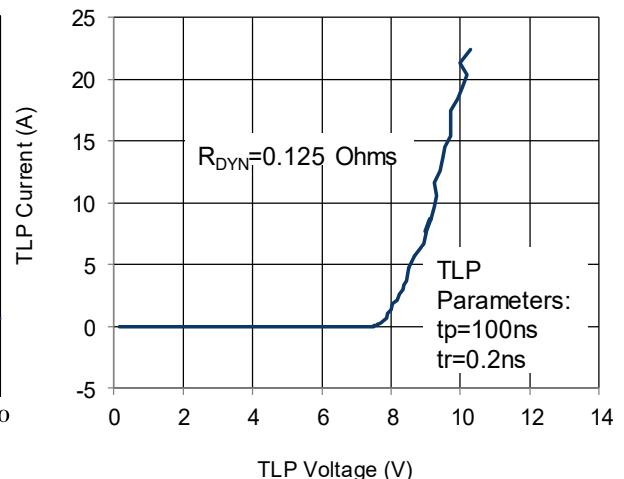
Electrical Characteristics

Parameter	Symbol	Conditions	Minimum	Typical	Maximum	Units
Reverse Stand-Off Voltage	V_{RWM}				5	V
Reverse Breakdown Voltage	V_{BR}	$I_T=1\text{mA}$	5.6		9	V
Reverse Leakage Current	I_R	$V_{RWM}=5\text{V}, T=25^\circ\text{C}$			500	nA
Forward Voltage	VF	$IF=10\text{mA}$	0.6		1.0	V
Clamping Voltage	V_C	$I_{PP}=30\text{A}, t_p=8/20\mu\text{s}$		12	15	V
Dynamic Resistance ^{1,2}	R_{DYN}	$TLP=0.2/100\text{ns}$		0.125		Ω
ESD Clamping Voltage ¹	V_C	$IPP = 4\text{A}, t_p = 0.2/100\text{ns} \text{ (TLP)}$		8.3		V
ESD Clamping Voltage ¹	V_C	$IPP = 16\text{A}, t_p = 0.2/100\text{ns} \text{ (TLP)}$		9.8		V
Junction Capacitance	C_j	$VR = 0\text{V}, f = 1\text{MHz}$		230	260	pF

Notes : 1、 TLP Setting : $t_p=100\text{ns}$, $t_r=0.2\text{ns}$, I_{TLP} and V_{TLP} sample window: $t_1=70\text{ns}$ to $t_2=90\text{ns}$.

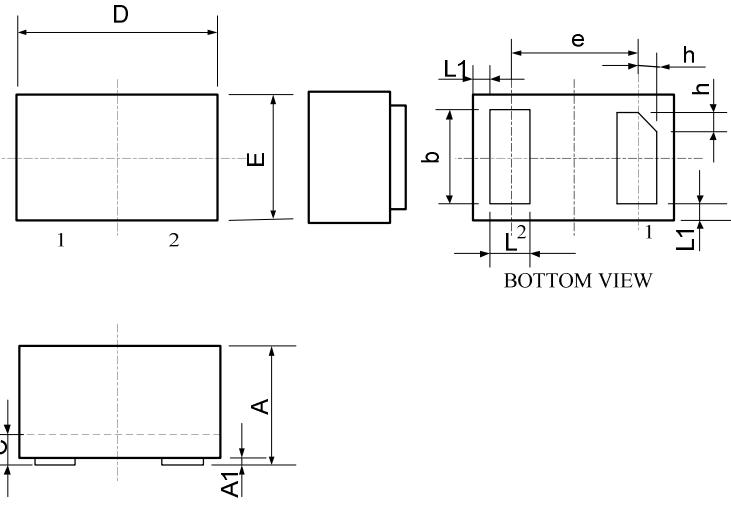
2、 Dynamic resistance calculated from $I_{PP}=4\text{A}$ to $I_{PP}=16\text{A}$ using “Best Fit”.

Typical Characteristics

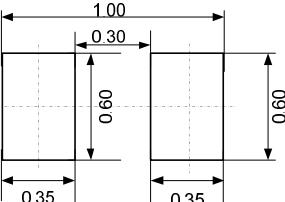
Figure 1: Peak Pulse Power vs. Pulse Time**Figure 2: Power Derating Curve****Figure 3: Clamping Voltage vs. Peak Pulse Current****Figure 4: Normalized Junction Capacitance vs. Reverse Voltage****Figure 5: 8/20μs Pulse Waveform****Figure 6: TLP I-V Curve**

Outline Drawing –DFN1006-2L

PACKAGE OUTLINE		MILIMETER		
SYMBOL		MIN	NOM	MAX
A	0.45	0.50	0.55	
A1	0.00	0.02	0.05	
b	0.45	0.50	0.55	
C	0.12	0.15	0.18	
D	0.95	1.00	1.05	
e	0.65BSC			
E	0.55	0.60	0.65	
L	0.20	0.25	0.30	
L1	0.05REF			
h	0.07	0.12	0.17	



Land Pattern



Marking Codes

Part Number	Marking Code
ES05DF	1 S5 2

Package Information

Qty: 10k/Reel

Revision History

No.	Version	Date	Revision Item	Request	Function and characteristic checking	Package dimension checking	Typos checking
1	1.0	2018-02-07	Released Version	Qi Shu Kun	Qi Shu Kun	Liu Jia Ying	Liu Jia Ying