36V Transient Voltage Suppressor

Features

- 5240 Watts Peak Power (tp = 8/20μs)
- Excellent Clamping Voltage
- Working Voltage: 36V
- Low Leakage Current
- Solid-state silicon technology

IEC COMPATIBILITY (EN61000-4)

- IEC 61000-4-2 (ESD) ±30kV (air), ±30kV (contact)
- IEC 61000-4-4 (EFT) 40A (5/50ns)
- IEC 61000-4-5 (Lightning) 131A (8/20μs)

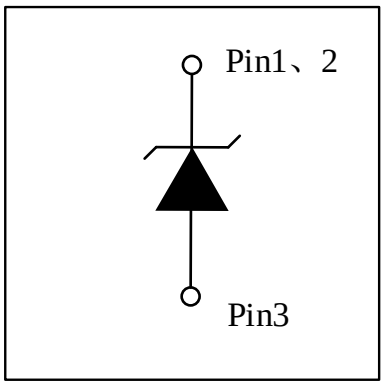
Mechanical Characteristics

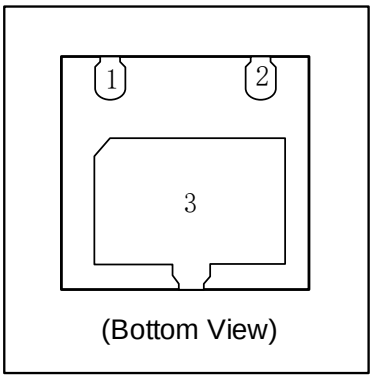
- DFN2020-3L package
- Marking : Marking Code
- Packaging : Tape and Reel per EIA 481
- RoHS Compliant & HF
- Device meets MSL1 requirement

Applications

- Power lines
- Industrial Electronics
- Microcontroller Input Protections
- Personal Digital Assistants (PDAs)

Schematic & PIN Configuration





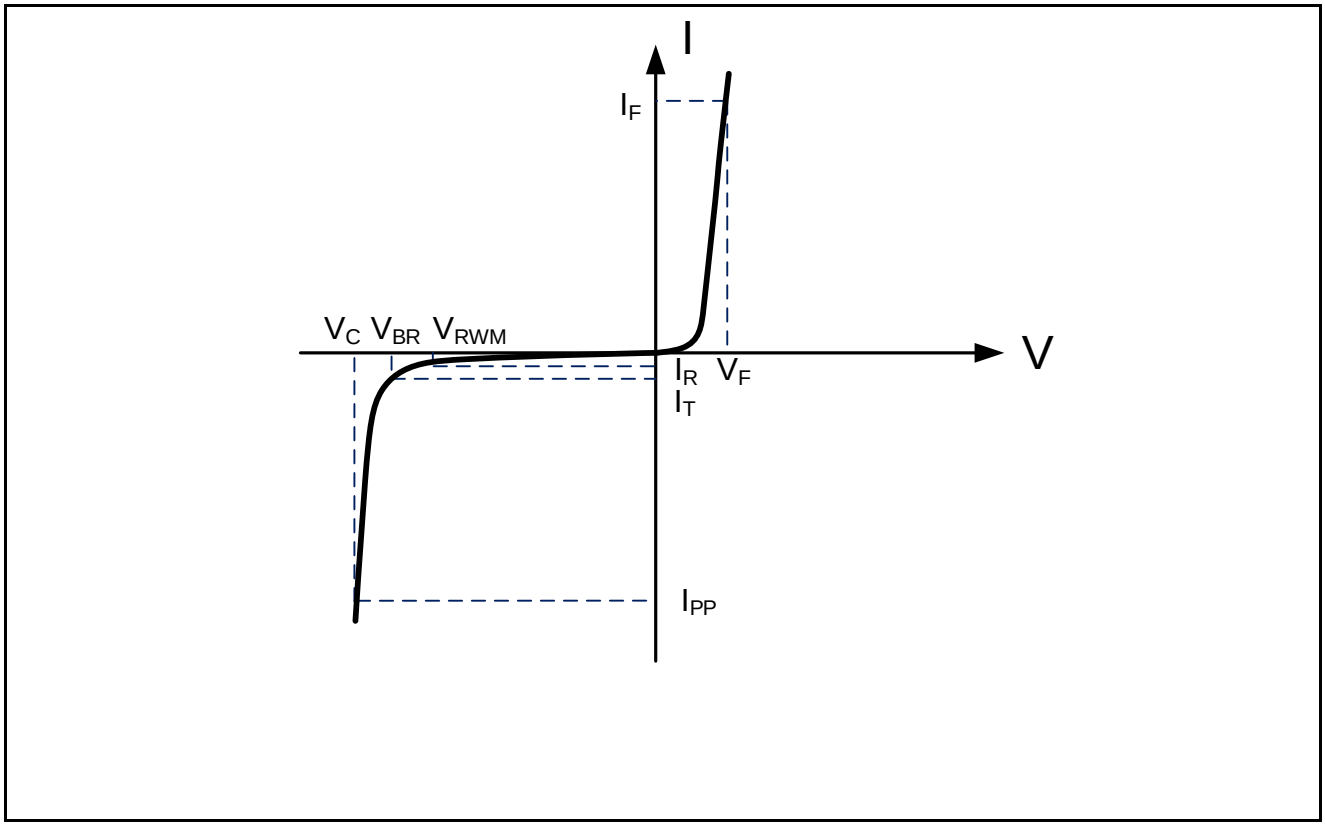
(Bottom View)

Absolute Maximum Rating

Parameter	Symbol	Value	Unit
Peak Pulse Power (t_p = 8/20μs)	P_{PP}	5240	Watts
Peak Pulse Current (t_p = 8/20μs)	I_{PP}	131	A
Operating Temperature	T_J	-55 to + 125	°C
Storage Temperature	T_{STG}	-55 to +150	°C

Electrical Parameters (T=25℃)

Symbol	Parameter
I_{PP}	Reverse Peak Pulse Current
V_C	Clamping Voltage @ I_{PP}
V_{RWM}	Reverse Stand-Off Voltage
I_R	Reverse Leakage Current @ V_{RWM}
V_{BR}	Breakdown Voltage @ I_T
I_T	Test Current
I_F	Forward Current
V_F	Forward Voltage @ IF



Electrical Characteristics

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Reverse Stand-Off Voltage	V_{RWM}				36	V
Reverse Breakdown Voltage	V_{BR}	I_T=1mA	38			V
Reverse Leakage Current	I_R	V_{RWM}=36V			500	nA
Forward Voltage	V_F	I_F=10mA	0.6		1.2	V
Clamping Voltage(1)	V_C	V_S =300V, I_{PP}=131A, t_p=8/20μs		37.8	40	V
ESD Clamping Voltage (2)	V_C	I_{PP} = 4A t_p = 0.2/100ns		42.6		V
ESD Clamping Voltage (2)	V_C	I_{PP} = 16A t_p = 0.2/100ns		43.5		V
Dynamic Resistance (2) (3)	R_{DYN}	TLP=0.2/100ns		0.08		Ω
Junction Capacitance	C_j	V_R = 0V, f = 1MHz		435	550	pF

Note1. Measured from pin 1& pin 2 to pin 3；

Note2. TLP Setting:t_p=100ns, t_r=0.2ns, I_{TLP} and V_{TLP} sample window:t_1=70ns to t_2=90ns.

Note3. Dynamic resistance calculated from I_{PP}=4A to I_{PP}=16A using “Best Fit”.

Typical Characteristics

Figure 1: Peak Pulse Power vs. Pulse Time

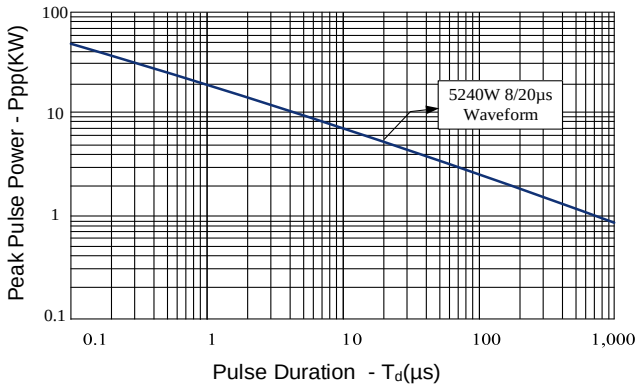


Figure 2: Power Derating Curve

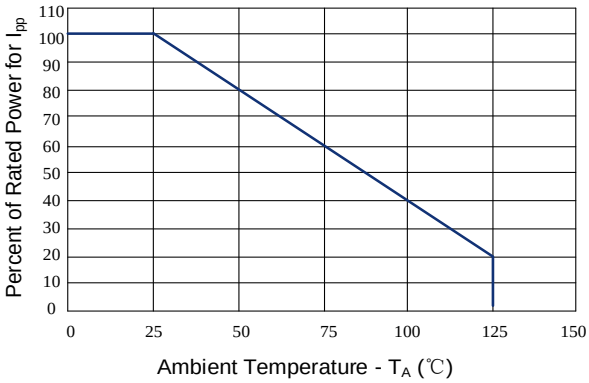


Figure 3: Clamping Voltage vs. Peak Pulse Current

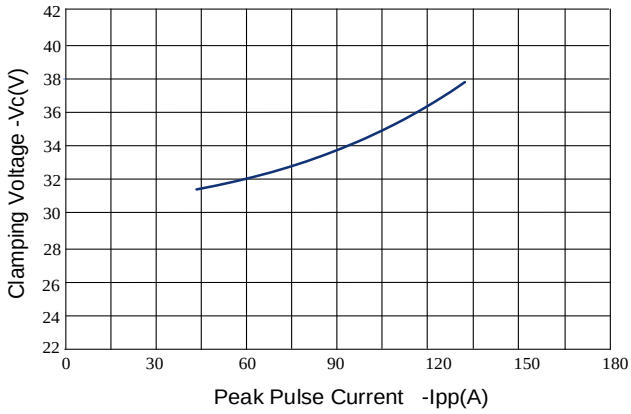


Figure 4: Normalized Junction Capacitance vs. Reverse Voltage

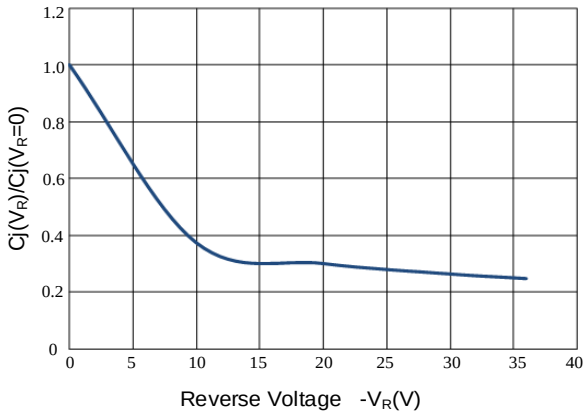


Figure 5: 8/20μs Pulse Waveform

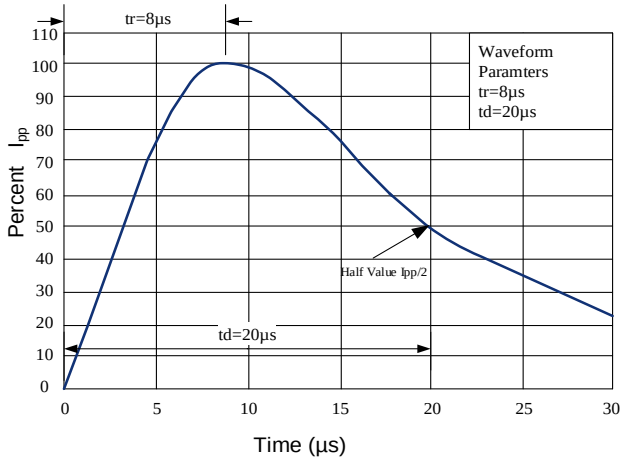
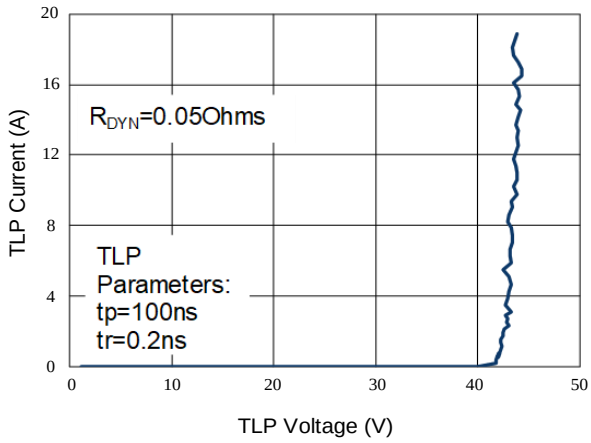
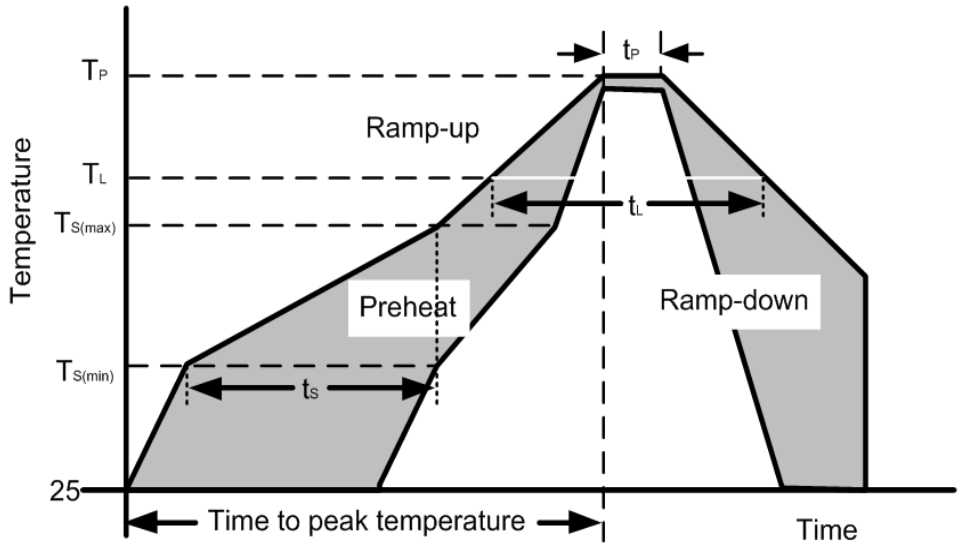


Figure 6: TLP I-V Curve



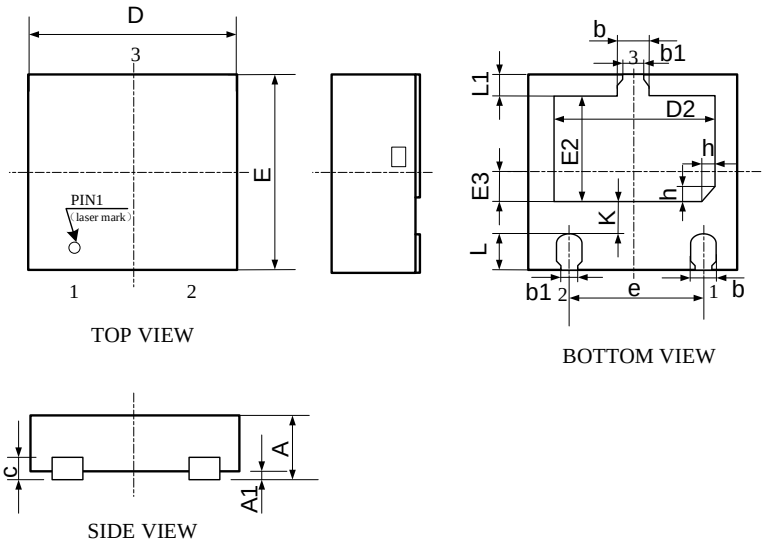
Soldering Parameters

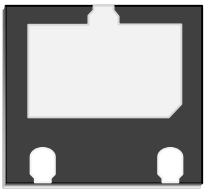
Reflow Condition		Pb – Free assembly
Pre Heat	Temperature Min ($T_{S(min)}$)	150°C
	Temperature Max ($T_{S(max)}$)	200°C
	Time (min to max) (t_S)	60 – 190 secs
Average ramp up rate (Liquidus Temp) (T_L) to peak		5°C/second max
$T_{S(max)}$ to T_L——Ramp-up Rate		5°C/second max
Reflow	Temperature (T_L) (Liquidus)	217°C
	Temperature (t_L)	60 – 150 seconds
Peak Temperature (T_P)		260+0/-5 °C
Time within actual peak Temperature (t_P)		20 – 40 seconds
Ramp-down Rate		5°C/second max
Time 25°C to peak Temperature (T_P)		8 minutes Max.
Do not exceed		280°C



Package Dimension

PACKAGE OUTLINE

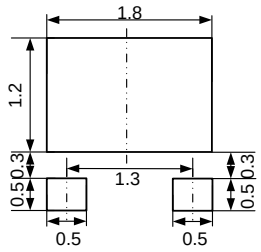




DFN2020-3L

SYMBOL	MILLIMETERS		
	MIN	NOM	MAX
A	0.45	0.55	0.60
A1	0.00	0.02	0.05
b	0.25	0.30	0.35
b1		0.20REF	
c		0.152REF	
D	1.90	2.00	2.10
D2	1.40	1.50	1.60
e		1.30BSC	
E	1.90	2.00	2.10
E2	0.95	1.05	1.15
E3	0.20	0.30	0.40
L	0.35	0.40	0.45
L1	0.20	0.25	0.30
h		0.20REF	
K	0.20	0.30	0.40

MOUNTING PAD

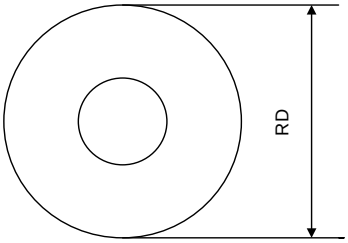


Notes:

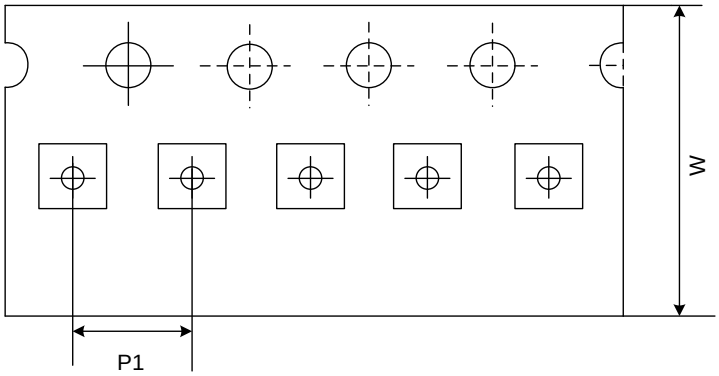
Controlling Dimension: Millimeter.

Tape And Reel Information

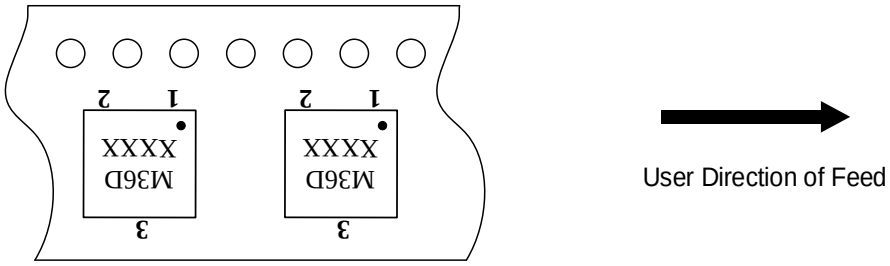
Reel Dimensions



Tape Dimensions

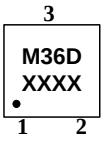


Quadrant Assignments For PIN1 Orientation In Tape



RD	Reel Dimensions	7 inch
W	Overall width of the carrier tape	8 mm
P1	Pitch between successive cavity centers	4mm

Ordering Information

Part	Package	Marking	Packing Information
ES36P4N3X	DFN2020-3L	 M36D=Specific Device Code XXXX=Lot Code	3k/Reel

Revision History and Checking Table

No.	Version	Date	Revision Item	Request	Function & Spec Checking	Package Checking	Tape Checking
1	1.0	2024-08-12	Released Version	Qi Shu Kun	Qi Shu Kun	Liu Jia Ying	Liu Jia Ying