

# **Built in Hall Sensor Single-phase**

# **Full-wave Motor Driver**

#### **General Description**

The ET3720 is a single-phase full-wave motor driver. It is built in an ultra small WL-CSP4 package. With built in HALL sensor, it is possible to drive the motor by simply applying the power supply. It is suitable for DC brush-less coin type motor for mobile phones.

#### Features

- Built in hall element
- Automatic switching to the PWM drive from starting full torque
- Thermal shout down (TSD)
- Ultra small package: WLCSP4

#### **Device Information**

Part No.	Package	Size	MSL	
ET3720	WLCSP4	0.75mm × 0.89mm	Level 1	

#### Application

• Vibration Motor driver for mobile phone

#### **Pin Assignments**



#### **Pin Function**

Pin Name	Pin No.	Function
VCC	1A	Power supply
GND	2A	GND
OUT1	1B	Motor output 1
OUT2	2B	Motor output 2

## **Block Diagram**



Rev 1.1

# Functional Description I/O truth table



#### Starting full on time

By applying the power supply voltage VCC from the motor stopped, IC output is duty100% PWM in 180ms. After 180ms, IC output switches to PWM duty 83% (no load current condition) automatically.



#### **Outputs Magnetic switch-point**

Since Outputs switch-point has hysteresis, please make sure the magnetic density at IC position is more than  $\pm$ 7.5mT in application.

#### Thermal Shutdown Circuit(TSD)

ET3720 has a built-in thermal shutdown circuit that prevents heat damage to the IC. Normal operation should always be within the IC's power dissipation rating. If the rating is exceeded for a continued period, the junction temperature (Tj) will rise which will activate the TSD circuit that will turn OFF all output pins. When the Tj falls below the TSD threshold, the circuits are automatically restored to normal operation.

#### UVLO

UVLO is active when  $V_{CC}$  is under  $V_{UVLO}$ , motor output is Hi-Z state. This protection is released when  $V_{CC}$  is more than  $V_{UVREL}$ .

#### Equivalent circuit



#### HALL position (Reference data)



## **Application Safety Measure Recommendation**

#### 1. Reverse connection protection diode

Reverse connection of power results in IC destruction as shown in figure below. When reverse connection is possible, reverse connection protection diode must be added between power supply and VCC.





#### 2. Protection against VCC voltage rise by back electromotive force

Back electromotive force (Back EMF) generates regenerative current to power supply. However, when reverse connection protection diode is connected, VCC voltage rises because the diode prevents current flow to power supply.



When the absolute maximum rated voltage may be exceeded due to voltage rise by back electromotive force, place a (A) Capacitor or (B) Zener diode between VCC and GND.

If necessary, add both of them(C) or Capacitor and resistor (D) can also be used to have better ESD surge protection.



#### 3. Problem of GND-line PWM switching

Do not perform PWM switching of GND line because GND terminal potential cannot be kept to the minimum voltage of system. However, if it becomes the use of necessity, make ensure that there is no problem with characteristics.

#### Thermal derating curve

Thermal derating curve indicates power that can be consumed by IC with reference to ambient temperature. Power that can be consumed by IC begins to attenuate at certain ambient temperature. This gradient is determined by thermal resistance  $\theta$ JA.

Thermal resistance θJA depends on chip size, power consumption, package ambient temperature, packaging condition, wind velocity, etc., even when the same package is used. Thermal derating curve indicates a reference value measured at a specified condition.

Shows a thermal derating curve.



#### **Absolute Maximum Ratings**

Symbol	Parameter	Ratings	Unit
Vcc	Supply Voltage	6	V
Pd	Power Dissipation	0.51(1)	W
TJ	Junction Temperature	-40 to +150	°C
T <sub>STG</sub>	Storage Temperature	-55 to +125	°C
V <sub>OMAX</sub>	Output Voltage	6	V
I <sub>OMAX</sub>	Output Current	300 (2)	mA
T <sub>JMAX</sub>	Junction Temperature	150	°C

Notes:

1. Reduce by 4.08mW/°C over 25°C. (On 70.0mm×70.0mm×1.6mm glass epoxy board)

2. This value is not to exceed Pd.

*Caution:* Operating the IC over the absolute maximum ratings may damage the IC. The damage can either be a short circuit between pins or an open circuit between pins and the internal circuitry. Therefore, it is important to consider circuit protection measures, such as adding a fuse, in case the IC is operated over the absolute maximum ratings.

# **Recommended Operating Condition**

Symbol	Parameter	Ratings	Unit
Vcc	Operating Supply Voltage Range	2.2 to 4.5	V
T <sub>A</sub>	Operating Temperature	-25 to +85	°C

#### **Electrical Characteristics**

(Unless otherwise specified  $T_A=25^{\circ}C$ ,  $V_{CC}=3V$ )

Symbol	Parameter	Conditions Min		Тур	Мах	Unit
Icc	Circuit Current	Output Open		1.5	3.0	mA
	Starting Full on Time	This time is changed			280	ms
t <sub>FULL</sub>		output to PWM mode	110	180		
FULL		from Full on mode later				
		V <sub>CC</sub> = 3V starting				
V	Output Voltage	lo = 100mA			0.45	v
Vout	(Upper and Lower Total)	10 – 100MA			0.45	v
Manag	Under Voltage			2.1		V
V <sub>UVLO</sub>	Lockout Voltage			۷.۱		v
V	Under Voltage			2.2		v
VUVREL	Release Voltage			2.2		
f <sub>PWM</sub>	PWM frequency		60	100	150	KHz
D		With Load	60	75	0.1	%
D <sub>PWM</sub>	PWM duty	$(L=250uH, R=25\Omega)$	69	75	91	%
D	Magnetic Switch-point for			25	7.0	
B <sub>FWD</sub>	Forward Rotation			3.5	7.0	mT
Р	Magnetic Switch-point for		7.0	2.5		mT
B <sub>REV</sub>	Reverse Rotation		-7.0	-3.5		mT
B <sub>HYS</sub>	Magnetic hysteresis			7.0	15	mT
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## Package





# **Revision History and Checking Table**

Version	Date	Revision Item	Modifier	Function & Spec Checking	Package & Tape Checking
0.0	2020-03-26	Preliminary Version	Wanggp	Wanggp	Zhujl
0.1	2021-04-12	Height Update	Wanggp	Wanggp	Zhujl
0.2	2021-05-20	B-fwd, B-rev update	Wanggp	Wanggp	Zhujl
0.3	2021-06-23	Package size update	Wanggp	Wanggp	Zhujl
0.4	2021-07-23	Ball height update	Wanggp	Wanggp	Zhujl
1.0	2021-12-09	Final Version	Wanggp	Wanggp	Zhujl
1.1	2022-12-20	Update Form	Lvyj	Wanggp	Shi Bo