

## Single Inverter with Schmitt Trigger

### General Description

The ET74LVC1G14 is an inverter in three tiny footprint packages. The device performs much as LCX multi-gate products in speed and drive.

### Features

- 24mA Sink and Source Output Capability
- Over-Voltage Tolerant Inputs and Outputs
- Chip Complexity: FETs = 20
- Designed for 1.65V to 5.5V VCC Operation
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant.
- Tiny SC70-5, SOT23-5 and DFN6(1.0×1.5) Packages
- MSL1 (DFN6) , MSL3(SC70-5,SOT23-5)

### Device Information

Part No.	Package	Size
ET74LVC1G14	SC70-5	1.3mm×2.1mm
ET74LVC1G14T	SOT23-5	1.6mm×2.9mm
ET74LVC1G14Y	DFN6	1.0mm×1.5mm

### Pin Configuration

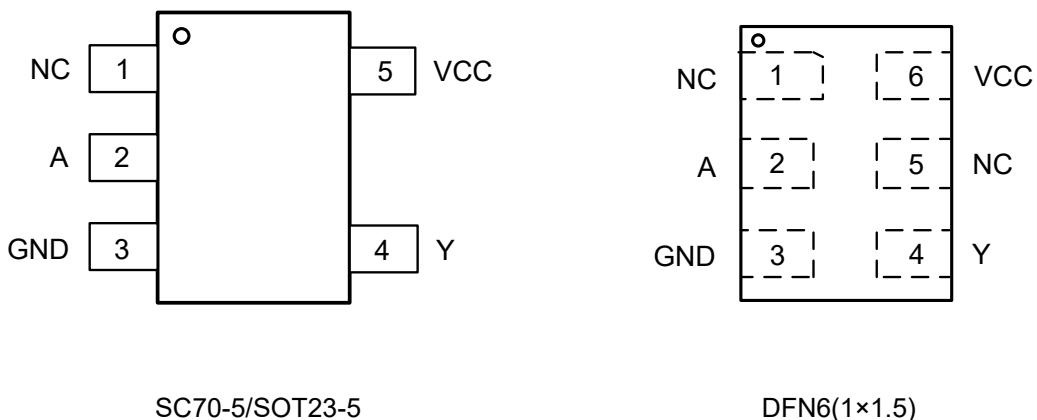


Figure1. Top View

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## Pin Function

(SC70-5/ SOT23-5)

Pin No.	Pin Name	Function
1	NC	No connect
2	A	Input
3	GND	Ground
4	Y	Output
5	VCC	Supply Voltage

## DFN6

Pin No.	Pin Name	Function
1	NC	No connect
2	A	Input
3	GND	Ground
4	Y	Output
5	NC	No connect
6	VCC	Supply Voltage

## Block Diagram

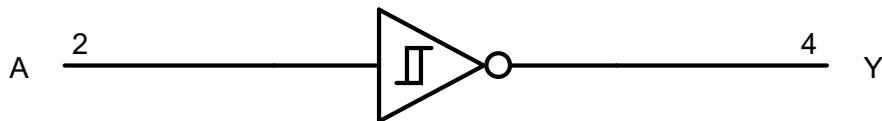


Figure2. Logic Symbol

## Functional Description

### Function Table

Input A	Output Y
L	H
H	L

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## Absolute Maximum Ratings

Symbol	Parameter		Value	Unit
V <sub>CC</sub>	DC Supply Voltage		-0.5 to 7.0	V
V <sub>I</sub>	DC Input Voltage <sup>(1)</sup>		-0.5 ≤ V <sub>I</sub> ≤ +7.0	V
V <sub>O</sub>	DC Output Voltage Output in Higher or Low State		-0.5 to V <sub>CC</sub> + 0.5	V
I <sub>IK</sub>	DC Input Diode Current V <sub>I</sub> < GND		-50	mA
I <sub>OK</sub>	DC Output Diode Current V <sub>O</sub> < GND, V <sub>O</sub> > V <sub>CC</sub>		±50	mA
I <sub>O</sub>	DC Output Sink Current		±50	mA
I <sub>CC</sub>	DC Supply Current per Supply Pin		±100	mA
I <sub>GND</sub>	DC Ground Current per Supply Pin		±100	mA
T <sub>STG</sub>	Storage Temperature Range		-65 to 150	°C
T <sub>L</sub>	Lead Temperature, Soldering 10 Seconds		260	°C
T <sub>J</sub>	Max Junction Temperature		150	°C
V <sub>ESD</sub>	ESD Classification	Human Body Model <sup>(2)</sup>	±4000	V
		Charged Device Model <sup>(3)</sup>	±1000	
I <sub>LU</sub>	Max Latchup Current Above V <sub>CC</sub> and GND at 125°C <sup>(4)</sup>		±100	mA

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

**Note1.** IO absolute maximum rating must be observed.

**Note2.** Tested to EIA/JESD22-A114-A.

**Note3.** Tested to JESD22-C101-A.

**Note4.** Tested to EIA/JESD78.

## Thermal Characteristics

Symbol	Package	Ratings	Value	Unit
R <sub>θJA</sub>	SC70-5	Thermal Characteristics, Thermal Resistance, Junction-to-Air	300	°C/W
	SOT23-5		250	
	DFN6(1×1.5)		440	
P <sub>D</sub>	SC70-5	Power Dissipation in Still Air at 85°C	215	mW
	SOT23-5		260	
	DFN6(1×1.5)		150	

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## Recommended Operating Conditions

Symbol	Parameter	Min	Max	Unit
$V_{CC}$	DC Supply Voltage Operating	1.65	5.5	V
	Date Retention	1.5	5.5	
$V_{IN}$	DC Input Voltage	0	5.5	V
$V_{OUT}$	DC Output Voltage (High or Low State)	0	5.5	V
$T_A$	Operating Temperature Range	-55	125	°C
$t_r, t_f$	Input Rise and Fall Time	$V_{CC} = 2.5 \text{ V} \pm 0.2 \text{ V}$	0	20
		$V_{CC} = 3.0 \text{ V} \pm 0.3 \text{ V}$	0	10
		$V_{CC} = 5.0 \text{ V} \pm 0.5 \text{ V}$	0	5

## Electrical Characteristics

### DC Electrical Characteristics

Symbol	Parameter	Condition	$V_{CC}(\text{V})$	$T_A = 25 \text{ }^{\circ}\text{C}$			$-40 \text{ }^{\circ}\text{C} \leq T_A \leq 125 \text{ }^{\circ}\text{C}$		Unit
				Min	Typ	Max	Min	Max	
$V_{IH}$	High-Level Input Voltage		1.65 to 1.95 2.3 to 5.5	0.75 $V_{CC}$ 0.7 $V_{CC}$			0.75 $V_{CC}$ 0.7 $V_{CC}$		V
$V_{IL}$	Low-Level Input Voltage		1.65 to 1.95 2.3 to 5.5				0.25 $V_{CC}$ 0.3 $V_{CC}$		V
$V_{OH}$	High-Level Output Voltage $V_{IN} = V_{IL}$	$I_{OH} = -100\mu\text{A}$	1.65 to 5.5	$V_{CC} - 0.1$	$V_{CC}$		$V_{CC} - 0.1$		V
		$I_{OH} = -3\text{mA}$	1.65	1.29	1.52		1.29		
		$I_{OH} = -8\text{mA}$	2.3	1.9	2.1		1.9		
		$I_{OH} = -12\text{mA}$	2.7	2.2	2.4		2.2		
		$I_{OH} = -16\text{mA}$	3.0	2.4	2.7		2.4		
		$I_{OH} = -24\text{mA}$	3.0	2.3	2.5		2.3		
		$I_{OH} = -32\text{mA}$	4.5	3.8	4.0		3.8		
$V_{OL}$	Low-Level Output Voltage $V_{IN} = V_{IH}$	$I_{OL} = 100\mu\text{A}$	1.65 to 5.5		0.0	0.1		0.1	V
		$I_{OL} = 3\text{mA}$	1.65		0.08	0.24		0.24	
		$I_{OL} = 8\text{mA}$	2.3		0.20	0.3		0.3	
		$I_{OL} = 12\text{mA}$	2.7		0.22	0.4		0.4	
		$I_{OL} = 16\text{mA}$	3.0		0.28	0.4		0.4	
		$I_{OL} = 24\text{mA}$	3.0		0.38	0.55		0.55	
		$I_{OL} = 32\text{mA}$	4.5		0.42	0.55		0.55	
$I_{IN}$	Input Leakage Current	$V_{IN} = 5.5 \text{ V}$ or GND	0 to 5.5		$\pm 0.1$			$\pm 1.0$	uA

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I <sub>OFF</sub>	Power Off Leakage Current	V <sub>IN</sub> = 5.5 V or V <sub>OUT</sub> = 5.5 V	0			1		10	uA
I <sub>CC</sub>	Quiescent Supply Current	V <sub>IN</sub> = 5.5 V or GND	5.5					10	uA

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

## AC Electrical Characteristics

t<sub>r</sub> = t<sub>f</sub> = 2.5ns; C<sub>L</sub> = 50pF; R<sub>L</sub> = 500 Ω

Symbol	Parameter	Condition	V <sub>CC</sub> (V)	T <sub>A</sub> = 25 °C			-40°C ≤ T <sub>A</sub> ≤ 125°C		Unit
				Min	Typ	Max	Min	Max	
t <sub>PLH</sub> t <sub>PHL</sub>	Propagation Delay (Figure 3 and 4)	R <sub>L</sub> = 1MΩ C <sub>L</sub> = 15pF	1.65	2.0	5.3	11.4	2.0	12.0	ns
			1.8	2.0	4.4	9.5	2.0	10.0	
		R <sub>L</sub> = 1MΩ C <sub>L</sub> = 15pF	2.5±0.2	0.2	3.5	6.5	0.8	7.0	
		R <sub>L</sub> = 1MΩ C <sub>L</sub> = 15pF	3.3±0.3	0.8	2.1	4.5	0.5	4.7	
		R <sub>L</sub> = 500Ω C <sub>L</sub> = 50pF		1.2	2.9	5.5	1.5	5.2	
		R <sub>L</sub> = 1MΩ C <sub>L</sub> = 15pF	5.0±0.5	0.5	1.8	3.9	0.5	4.1	
		R <sub>L</sub> = 500Ω C <sub>L</sub> = 50pF		0.8	2.4	4.3	0.8	4.5	

## Capacitance Characteristics

Symbol	Parameter	Condition	Typ	Unit
C <sub>IN</sub>	Input Capacitance	V <sub>CC</sub> = 5.5 V, V <sub>I</sub> = 0 V or V <sub>CC</sub>	>2.5	pF
C <sub>PD</sub>	Power Dissipation Capacitance (6)	10 MHz, V <sub>CC</sub> = 3.3 V, V <sub>I</sub> = 0 V or V <sub>CC</sub>	9	pF
		10 MHz, V <sub>CC</sub> = 5.5 V, V <sub>I</sub> = 0 V or V <sub>CC</sub>	11	

6. C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation: I<sub>CC(OPR)</sub>=C<sub>PD</sub>×V<sub>CC</sub>×fin+I<sub>CC</sub>×C<sub>PD</sub> is used to determine the no-load dynamic power consumption; P<sub>D</sub>=C<sub>PD</sub>×V<sub>CC</sub><sup>2</sup> ×fin+I<sub>CC</sub>×V<sub>CC</sub>×Fig.

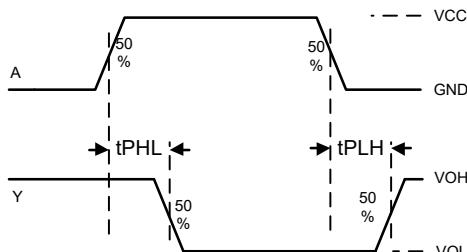


Figure 3. Switching Waveform

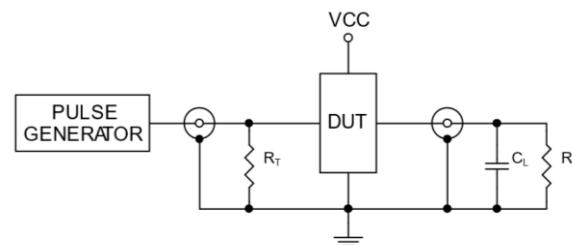
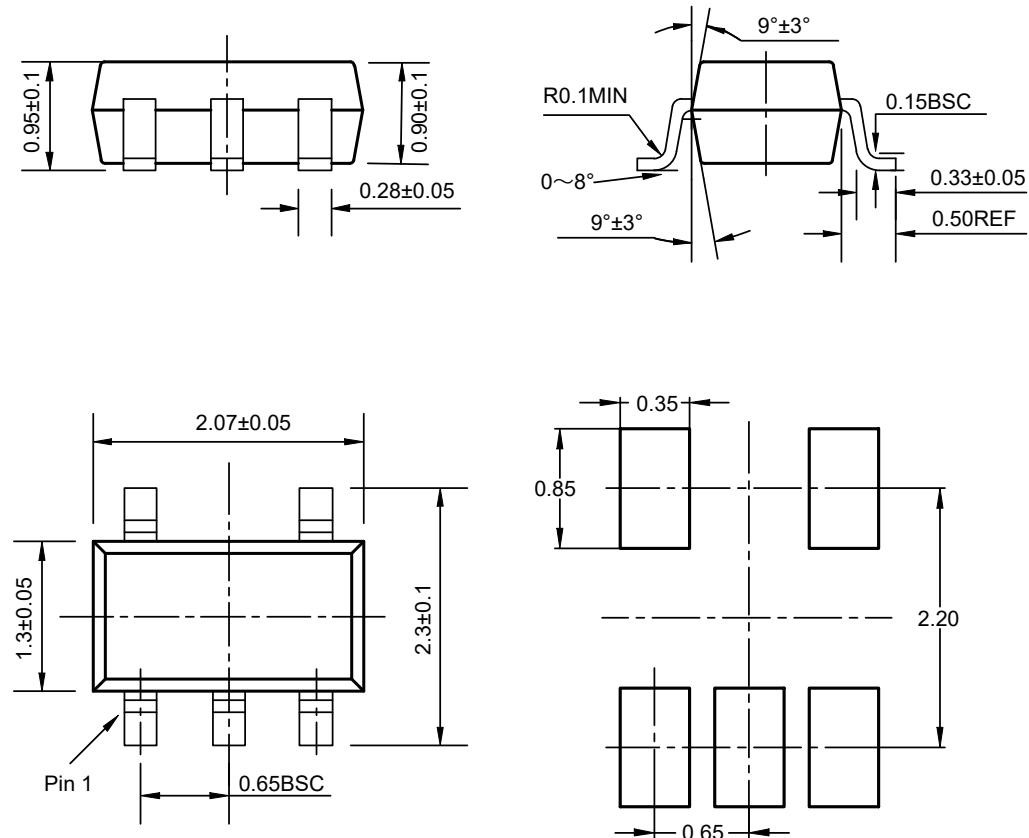


Figure 4. Test Circuit

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## Package Dimension

SC70-5

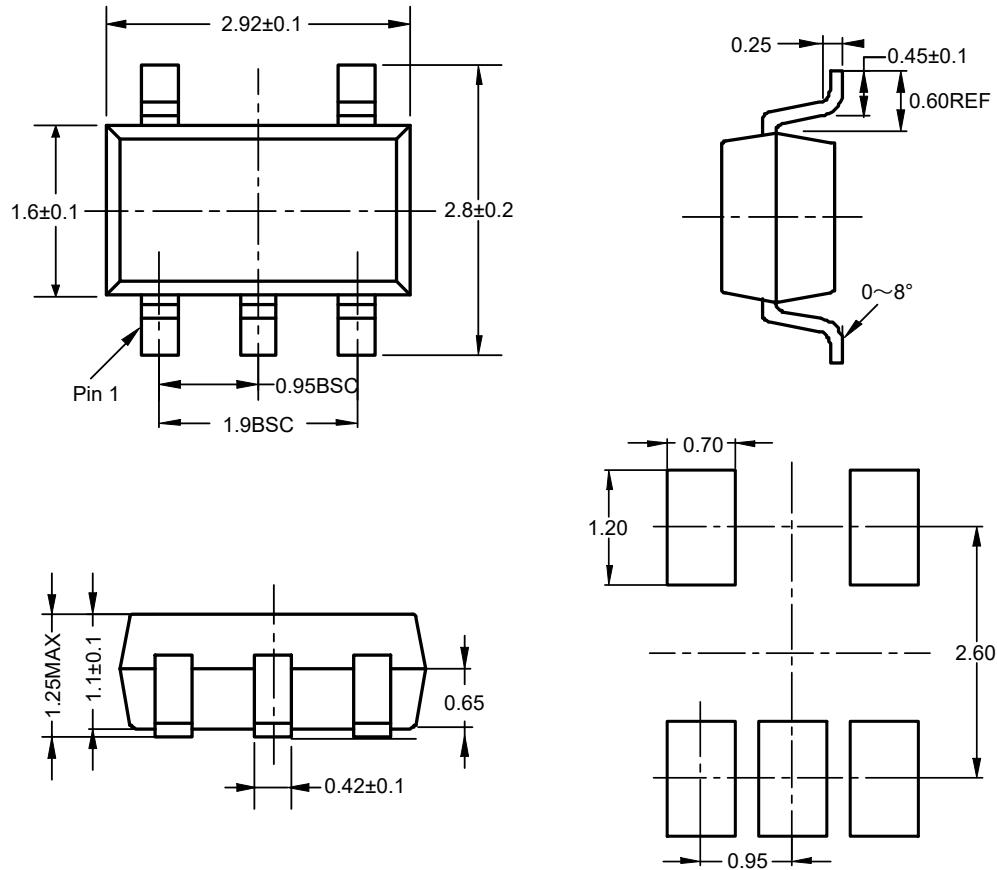


**Recommended Land Pattern**

Unit: mm

# ET74LVC1G14

SOT23-5

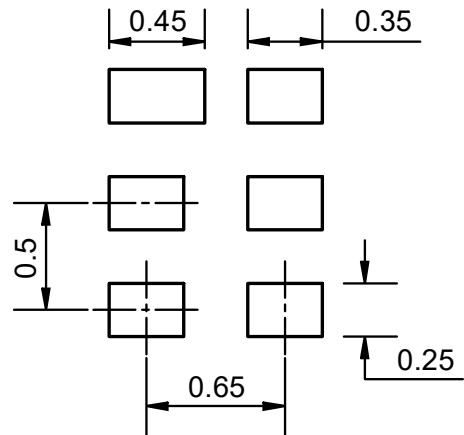
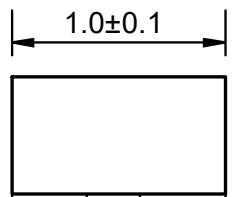
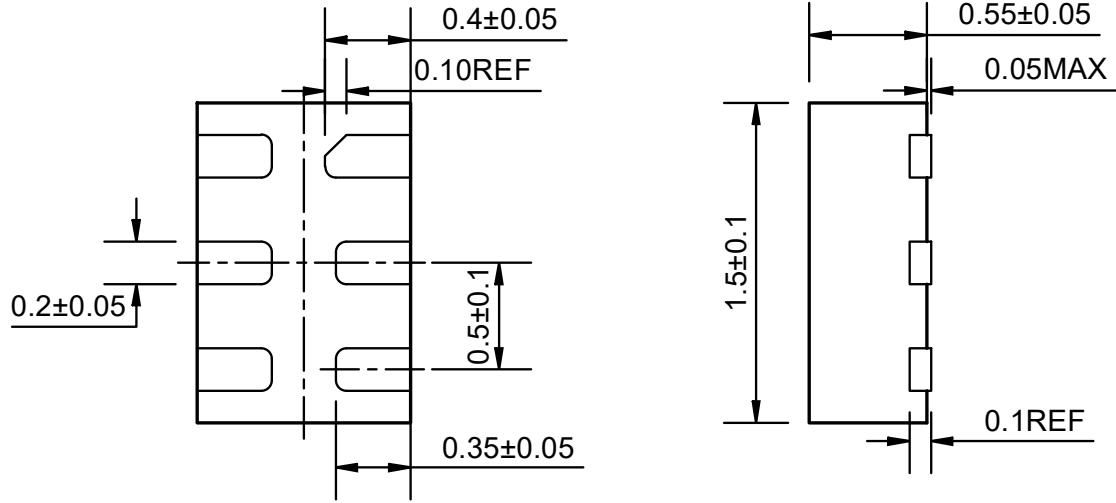


**Recommended Land Pattern**

Unit: mm

# ET74LVC1G14

DFN6 (1.0×1.5)



**Recommended Land Pattern**

Unit: mm

# ET74LVC1G14

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## Revision History and Checking Table

Version	Date	Revision Item	Modifier	Function & Spec Checking	Package & Tape Checking
1.0	2017-07-19	Original Version	Ma Yong jian	Ma Yong jian	Liu Jia Ying
1.1	2019-07-18	Update AC Table and Device Information	Ma Yong jian	Ma Yong jian	Liu Jia Ying
1.2	2021-06-25	Update Cpd in AC Table and Lin/Loff/Lcc in Dc Table	Ma Yong jian	Ma Yong jian	Liu Jia Ying
1.3	2022-01-18	Add SC70-5 Recommended Pad Layout	Shibo	Shibo	Zhujl
1.4	2022-06-10	ESD Update	Shibo	Shibo	Zhujl
1.5	2022-10-14	Update format and Thermal Characteristics	Wuhan	Shibo	Zhujl
1.6	2023-11-29	Update Typeset /ESD	Shibo	Shibo	Shibo