



# ET74LVC07A - Hex Buffer and Driver With Open-Drain Outputs

## General Description

The ET74LVC07A device is a hex buffer and driver that is designed for 1.65V to 5.5V V<sub>cc</sub> operation.

## Features

- Wide Operating Voltage Range: 1.65V to 5.5V
- Inputs and Open-drain Outputs Accept Voltages Up to 5.5V
- Max t<sub>pd</sub> of 2.6ns at 5V
- 24mA Balanced Output Sink Capability
- I<sub>off</sub> Supports Live Insertion, Partial-power-down Mode, and Back-drive Protection
- Latch-up Performance Exceeds 200mA per JESD78, Class II
- ESD Protection Exceeds JESD22
  - 4000V Human-Body Model (A114-A)
  - 2000V Charged-Device Model (C101)

## Applications

- Mobile Device
- AV Receiver
- Audio Dock: Portable
- Blu-ray Player and Home Theater
- MP3 Player or Recorder
- Personal Digital Assistant (PDA)
- Power: Telecom/Server AC/DC Supply: Single
- Controller: Analog and Digital
- Solid State Drive (SSD): Client and Enterprise
- TV: LCD, Digital, and High-definition (HDTV)
- Tablet: Enterprise
- Video Analytics: Server
- Wireless Headset, Keyboard and Mouse

## Device Information

Part No.	Package	MSL
ET74LVC07AM	SOP14(8.65mm*6mm)	3
ET74LVC07AV	TSSOP14(4.96mm*6.4mm)	3

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## Pin Configuration

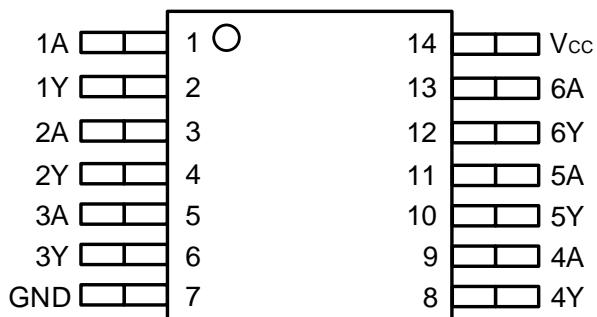


Figure1. Top View

## Pin Function

Pin		I/O	Description
Name	No.		
1A	1	Input	Channel 1, Input A
1Y	2	Input	Channel 1, Output Y
2A	3	Output	Channel 2, Input A
2Y	4	Input	Channel 2, Output Y
3A	5	Input	Channel 3, Input A
3Y	6	Output	Channel 3, Output Y
GND	7	—	Ground
4Y	8	Output	Channel 4, Output Y
4A	9	Input	Channel 4, Input A
5Y	10	Input	Channel 5, Output Y
5A	11	Output	Channel 5, Input A
6Y	12	Input	Channel 6, Output Y
6A	13	Input	Channel 6, Input A
V <sub>cc</sub>	14	—	Positive Supply

## Block Diagram

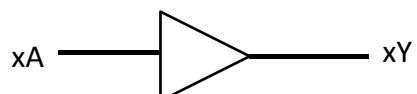


Figure2. Logic Symbol

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## Functional Description

### Function Table

Input	Output
xA	xY
L	L
H	Z

## Absolute Maximum Ratings

Over operating free-air temperature range (unless otherwise noted)

Symbol	Parameter	Conditions	Rating	Unit
V <sub>CC</sub>	Supply Voltage		-0.5~+6.5	V
I <sub>IK</sub>	Input Clamping Current	V <sub>I</sub> < 0V	-50	mA
V <sub>I</sub>	Input Voltage <sup>(1)</sup>		-0.5~+6.5	V
I <sub>OK</sub>	Output Clamping Current	V <sub>O</sub> < 0V	-50	mA
V <sub>O</sub>	Output Voltage	Active Mode <sup>(1)</sup>	-0.5~V <sub>CC</sub> +0.5	V
		Power-down Mode V <sub>CC</sub> =0V <sup>(1)</sup>	-0.5~+6.5	V
I <sub>O</sub>	Output Current	V <sub>O</sub> = 0V to V <sub>CC</sub>	±50	mA
I <sub>CC</sub>	Supply Current		+100	mA
I <sub>GND</sub>	Ground Current		-100	mA
T <sub>J</sub>	Operating Junction Range		-40 to +150	°C
T <sub>STG</sub>	Storage Temperature		-65 to +150	°C
V <sub>ESD</sub>	Human Body Mode <sup>(2)</sup>		±4000	V
	Charged Device Mode <sup>(3)</sup>		±2000	V
I <sub>LU</sub>	Latch-up Current <sup>(4)</sup>		±200	mA

**Note1:** I<sub>O</sub> absolute maximum rating must be observed.

**Note2:** HBM tested per EIA/JESD22-A114-A;

**Note3:** CDM tested per EIA/JESD22-C101;

**Note4:** Latch-up Current Maximum Rating tested per EIA/JESD78E;

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## Recommended Operating Conditions

Symbol	Parameter	Conditions	Min	Max	Unit
$V_{CC}$	Supply Voltage	Operating	1.65	5.5	V
$V_{IH}$	High-level input voltage	$V_{CC} = 1.65V \text{ to } 1.95V$	$0.65 \times V_{CC}$		V
		$V_{CC} = 2.3V \text{ to } 2.7V$	1.7		V
		$V_{CC} = 2.7V \text{ to } 3.6V$	2		V
		$V_{CC} = 4.5V \text{ to } 5.5V$	$0.7 \times V_{CC}$		V
$V_{IL}$	Low-level input voltage	$V_{CC} = 1.65V \text{ to } 1.95V$		$0.35 \times V_{CC}$	V
		$V_{CC} = 2.3V \text{ to } 2.7V$		0.7	V
		$V_{CC} = 2.7V \text{ to } 3.6V$		0.8	V
		$V_{CC} = 4.5V \text{ to } 5.5V$		$0.3 \times V_{CC}$	V
$V_I$	Input Voltage		0	5.5	V
$V_O$	Output Voltage		0	5.5	V
$I_{OL}$	Low-level Output Current	$V_{CC} = 1.65V$		4	mA
		$V_{CC} = 2.3V$		12	mA
		$V_{CC} = 2.7V$		12	mA
		$V_{CC} = 3V$		24	mA
		$V_{CC} = 4.5V$		24	mA
$T_A$	Ambient Temperature	Operating in Free Air	-40	125	°C

## Electrical Characteristics

### DC Electrical Characteristics

Over operating free-air temperature range; typical values measured at  $T_A = 25^\circ\text{C}$  (unless otherwise noted)

Symbol	Parameter	Conditions	$V_{CC}$	$-40^\circ\text{C} \leq T_A \leq 125^\circ\text{C}$			Unit
				Min	Typ	Max	
$V_{OL}$	Low-level Output Voltage	$V_I = V_{IH} \text{ or } V_{IL}$	$I_{OL} = 100\mu\text{A}$	1.65V to 5.5V		0.2	V
			$I_{OL} = 4\text{mA}$	1.65V		0.45	
			$I_{OL} = 8\text{mA}$	2.3V		0.7	
			$I_{OL} = 12\text{mA}$	2.7V		0.4	
			$I_{OL} = 24\text{mA}$	3V		0.55	
$I_I$	Input Leakage Current	$V_I = V_{CC} \text{ or } 0$	3.6V			$\pm 5$	$\mu\text{A}$
$I_{OFF}$	Leakage Current	$V_I \text{ or } V_O = 5.5V$	0V			$\pm 10$	$\mu\text{A}$
$I_{CC}$	Supply Current	$V_I = V_{CC} \text{ or } 0$	$I_O = 0$	3.6V		10	$\mu\text{A}$

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$\Delta I_{CC}$		One input at $V_{CC} - 0.6V$ , Other inputs at $V_{CC}$ or GND	2.7V to 3.6V			500	$\mu A$
$C_I$	Input Capacitance	$V_I = V_{CC}$ or 0	3.3V		5		pF

## Switching Characteristics

Over operating free-air temperature range; typical values measured at  $T_A = 25^\circ C$  (unless otherwise noted)

Symbol	Parameter	From	To	$V_{CC}$	$-40^\circ C \leq T_A \leq 85^\circ C$		$-40^\circ C \leq T_A \leq 125^\circ C$		Unit
					Min	Max	Min	Max	
$t_{pd}$	Propagation Delay	A	Y	1.8V $\pm 0.15V$	1	10.5	1	10.5	ns
				2.5V $\pm 0.2V$	1	6	1	6	
				2.7V	1	6.5	1	6.5	
				3.3V $\pm 0.3V$	1	5	1	5	
				5V $\pm 0.5V$	1	3.8	1	3.8	

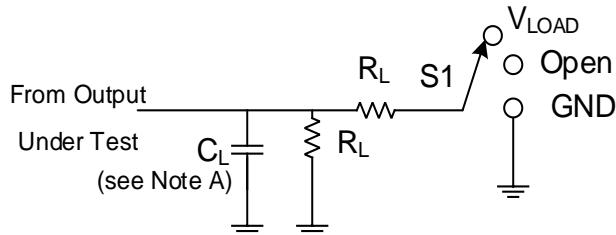
## Operating Characteristics

Over operating free-air temperature range; typical values measured at  $T_A = 25^\circ C$  (unless otherwise noted)

Symbol	Parameter	Conditions	$V_{CC}$	Min	Typ	Max	Unit
$C_{PD}$	Power Dissipation Capacitance per Buffer and Driver	$f = 10MHz$ , No Load	1.8V		5		pF
			2.5V		5.5		pF
			3.3V		6		pF
			5.5V		7.5		pF

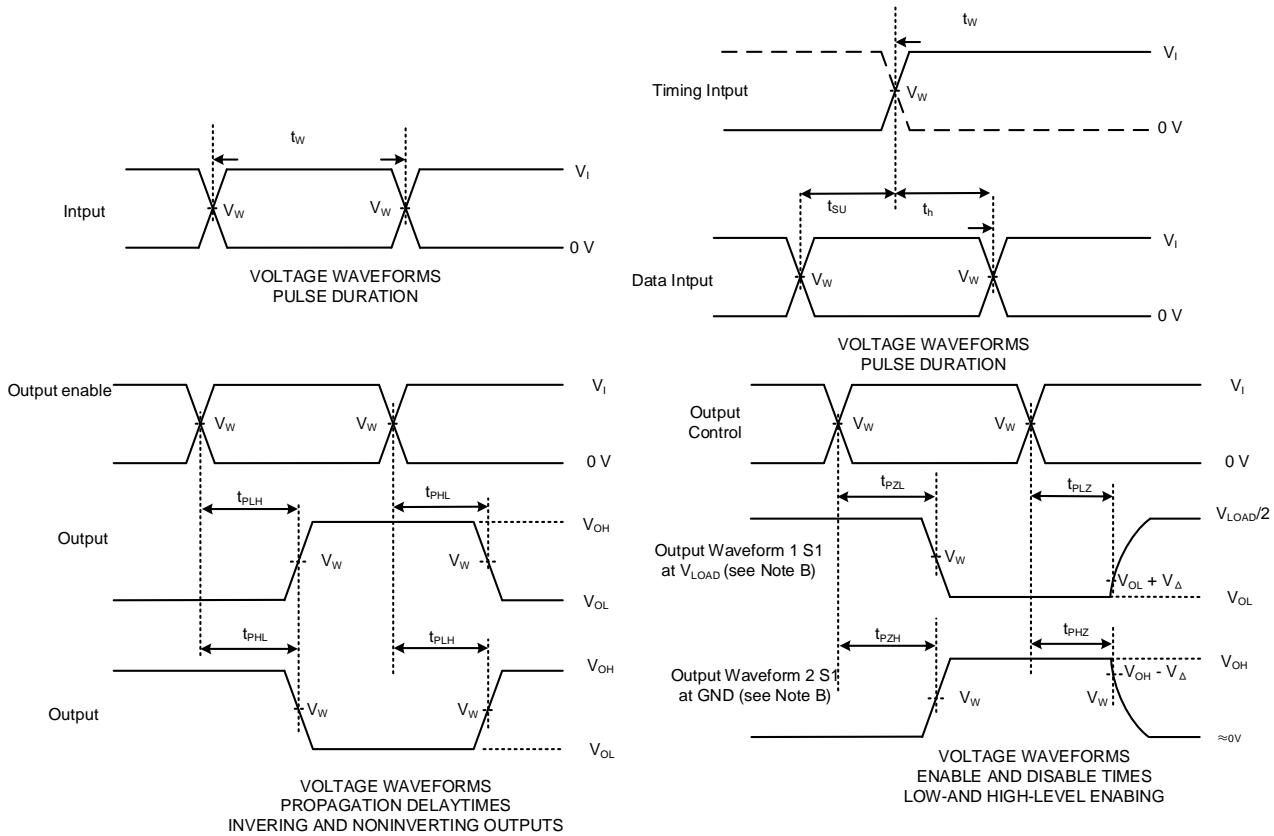
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## Parameter measurement information



TEST	S1
$t_{PLH}/t_{PHL}$	OPEN
$t_{PLZ}/t_{PZL}$	$V_{LOAD}$
$t_{PHZ}/t_{PZH}$	GND

Figure.3 Test circuit for measuring switching times



### Notes:

- A.  $C_L$  includes probe and jig capacitance.
- B. Waveform 1 is for an output with internal conditions such that the output is low, except when disabled by the output control.
- C. Waveform 2 is for an output with internal conditions such that the output is high, except when disabled by the output control.
- D. All input pulses are supplied by generators having the following characteristics:  
PRR  $\leq$  10MHz,  $Z_O = 50\Omega$
- E. The outputs are measured one at a time, with one transition per measurement.
- F.  $t_{PLZ}$  and  $t_{PHZ}$  are the same as  $t_{dis}$ .

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G.  $t_{PZL}$  and  $t_{PZH}$  are the same as  $t_{en}$ .

H.  $t_{PLH}$  and  $t_{PHL}$  are the same as  $t_{pd}$ .

I. All parameters and waveforms are not applicable to all devices.

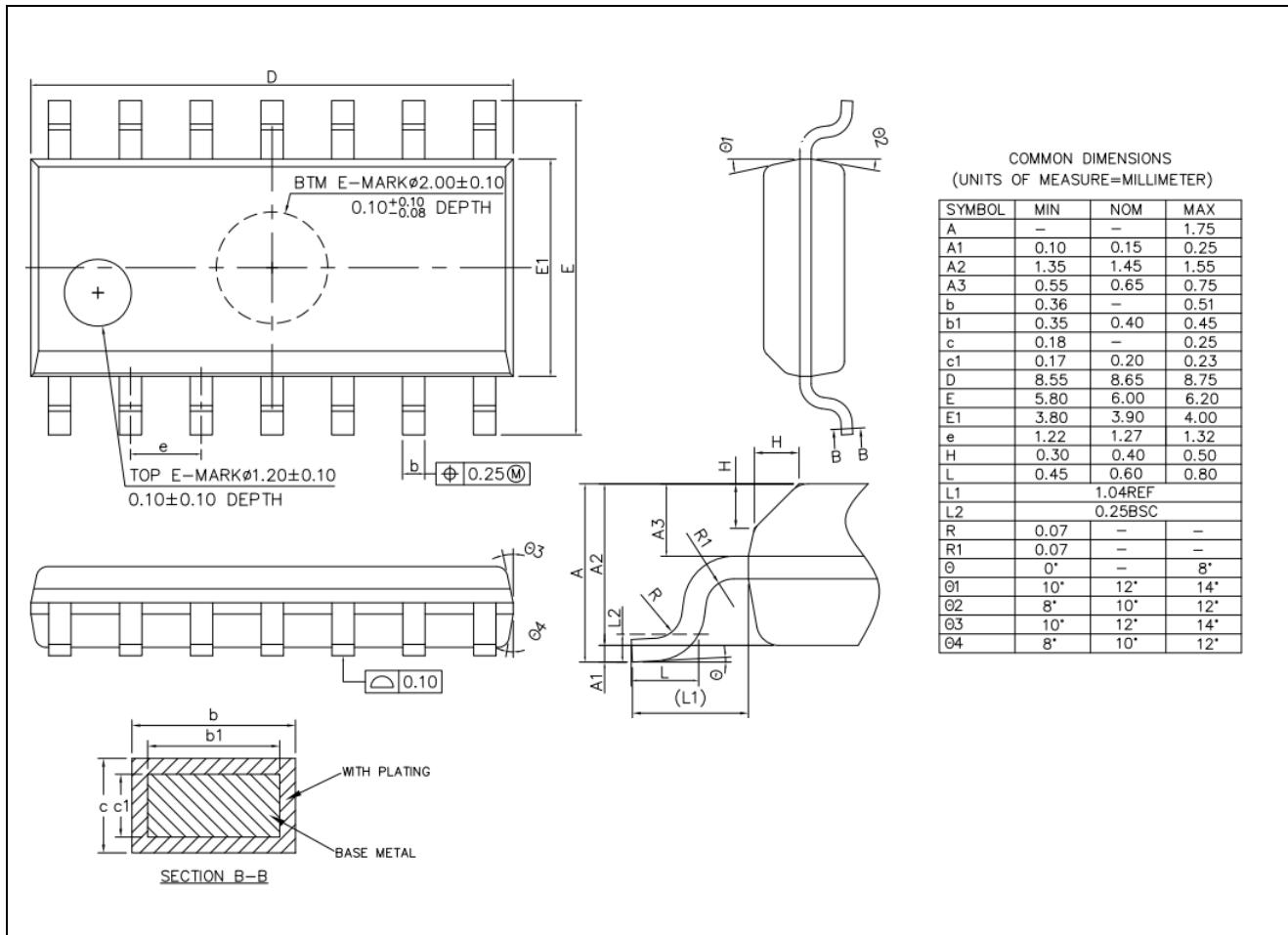
Figure.4 Input to output propagation delay times

V <sub>cc</sub>	Input		V <sub>M</sub>	V <sub>LOAD</sub>	C <sub>L</sub>	R <sub>L</sub>	V <sub>Δ</sub>
	V <sub>I</sub>	t <sub>r/t<sub>f</sub></sub>					
1.8V ± 0.15V	V <sub>cc</sub>	≤ 2ns	V <sub>cc</sub> /2	2 × V <sub>cc</sub>	30pF	1kΩ	0.15V
2.5V ± 0.2V	V <sub>cc</sub>	≤ 2ns	V <sub>cc</sub> /2	2 × V <sub>cc</sub>	30pF	500Ω	0.15V
2.7V	3V	≤ 2.5ns	1.5V	6V	50pF	500Ω	0.3V
3.3V ± 0.3V	3V	≤ 2.5ns	1.5V	6V	50pF	500Ω	0.3V
5.5V ± 0.5V	V <sub>cc</sub>	≤ 2.5ns	V <sub>cc</sub> /2	2 × V <sub>cc</sub>	50pF	500Ω	0.3V

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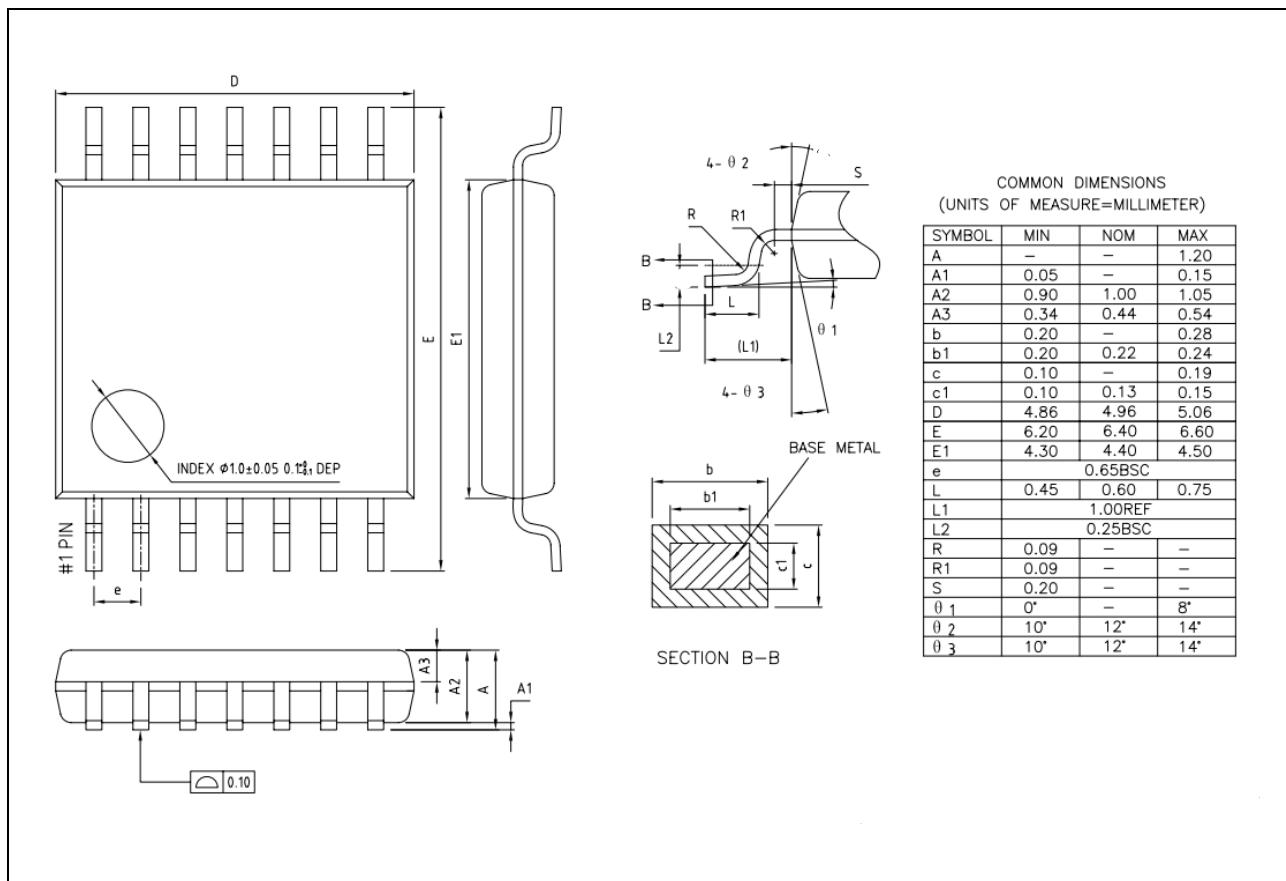
## Package Dimension

SOP14



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TSSOP14



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## Revision History and Checking Table

Version	Date	Revision Item	Modifier	Function & Spec Checking	Package & Tape Checking
0.0	2025-02-07	Preliminary Version	Gehao	Yangxiao Xu	Liujiaying
0.1	2025-04-21	Update Format	Wanganran	Yangxiao Xu	Liujiaying