Low R_{ON}, High Voltage, Rail-to-Rail Negative Signal Passing,

SPST Analog Switch

General Description

The ET32121 is a high voltage, wide range positive and negative signal passing, single-pole/single-throw (SPST) analog switch that is designed to operate from a single 2.3V to 5.5V power supply. Targeted applications include battery powered audio equipment that benefit from the ET32121 ultra low $95m\Omega$ (max@25°C) on-resistance for switch.

The ET32121 is a committed single-pole/single-throw (SPST) switch which is low R_{ON} switch. This configuration can be used as a single signals switch and power switch.

The ET32121 can pass -3V to 18V wide range positive and negative signals with very low distortion.

The ET32121 is available in Green FCDFN-6 1.2mm×1.5mm packages. It operates over an operating temperature range of -40°C to +85°C.

Features

- Wide Voltage Operation: 2.3V to 5.5V
- Ultra Low On-Resistance: 95mΩ (max@25°C)
- -3V to +18V Rail-to-Rail Low Distortion Positive and Negative Signal Passing
- Fast Switching Times
- Low Input Leakage Current
- Rail-to-Rail Input and Output Operation
- 1.2V Logic Compatible Control Pin
- -40°C to +85°C Operating Temperature Range
- Package Information:

Part No.	Package	MSL	
ET32121	FCDFN-6 (1.2mm×1.5mm)	Level 1	

Application

- HiFi Audio Switch
- Portable Instrumentation
- Battery-Operated Equipment

Pin Configuration



Pin Function

Pin No.	Name	I/O	Description
1	NO	I/O	NO Terminal. This pin can be an input or an output of switch.
2	NC	-	Null Pin.
3	COM	I/O	COM Terminal. This pin can be an input or an output of switch.
4	GND	/	Ground Pin.
5	EN	I	Enable Control. When EN=LOW, both COM and NO will be disconnected, the ET32121 will be in shutdown state. When EN=HIGH, the ET32121 will be in working state, COM and NO will be connected.
6	VCC	I	Power Supply Pin.

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Parameters			Max	Unit
Vcc		VCC to GND	0	6	V
Ven		EN to GND	0	6	V
Vsw	COM, NO to GND			20	V
Isw	Continuous Current from COM to NO			±1500	
IPEAK	Peak Current from COM to NO			±2000	
TJ	Maximum Junction Temperature			+150	°C
T _{STG}	Storage Junction Temperature			+150	°C
TLEAD	Lead Temperature (Soldering, 10s)			+260	°C
Vesd	Electrostatic	Human Body Model, JESD22-A114	±2	2.0	
	Discharge Capability Charged Device Model, JESD22-C101		±1.0		kV

Recommended Operating Conditions

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. ETEK does not recommend exceeding them or designing to Absolute Maximum Ratings.

Symbol	Parameters	Min	Max	Unit
Vcc	Supply Voltage Range	2.3	5.5	V
TA	Operating Temperature Range	-40	+85	°C

Electrical Characteristics

$V_{CC} = 3.3$	V, $T_A = -40^{\circ}C$ to +85°C, ty	ypical values are at	T _A = +25°C, u	unless oth	erwise n	oted.

Symbol	Parameters	Conditions	Temp	Min	Тур	Max	Unit
Analog S	witch						
VANALOG	Analog Signal Range		+25°C	-3		18	V
Ron			+25°C			95	_
	On-Resistance	$-V_{CC} \le V_{COM} \le +V_{CC}$, Is=200mA	Full			130	mΩ
RFLAT(ON)	On-Resistance	$-V_{CC} \le V_{COM} \le +V_{CC}$			0.001	0.01	Ω
	Flatness	Is=200mA	Full		0.001	0.01	
Ісом/NO(O	COM/NO Off	V _{COM} =-3V/3V	Full		0.01	0.35	uA
FF)	Leakage Current	V _{NO} =3V/-3V	1 411		0.01	0.00	
I _{COM(ON)}	Channel On	V_{NO} =-3V/3V, V_{COM} =floating	Full		0.01	0.35	uA
INO(ON)	Leakage Current	or V_{NO} =floating, V_{COM} =-3V/3V	1 Gil		0.01	0.55	
EN Inputs	3						
VIH	Input High Voltage	Vcc=2.3V to 5.5V,	Full	0.9			V
VIL	Input Low Voltage	V_{CC} =2.3V to 5.5V,	Full			0.4	V
Rpd	Pull Down Resistor		+25°C		5		MΩ
Dynamic	Characteristics						
	Off Isolation	f=1kHz, R∟=50Ω,	+25°C				dB
		Signal=0dBm			-95		
Oiso		f=20kHz, R∟=50Ω,	+25°C		70		
		Signal=0dBm			-70		
BW	-3dB Bandwidth	Signal=0dBm,	+25°C		150		MHz
Dvv		R∟=50Ω, C∟=5pF			150		
CON	Channel On		+25°C		30		pF
CON	Capacitance		.20 0		00		р
THD+N	Total Harmonic	$V_S=2V_{RMS}$, $R_L=600\Omega$	+25°C		-110		dB
	Distortion +Noise	$V_S=2V_{RMS}, R_L=8\Omega$	125 0		-102		uD
torupr	Ota et l. les Tiers a	Switch V _{EN} =0V to1.6V,	+25°C		180	30	us
t start	Start Up Time	V_{SW} =0.6V, RL=50 Ω	+23 C		100		
Power Re	equirements						
	Power Supply Current	V _{EN} =1.0V or 1.6V	Full		95		uA
lcc	Power Supply Current		_ <u></u> _				
	in Shutdown State	V _{EN} =0V	Full		1		uA
Thermal	Protection						
	Thermal Shutdown ⁽¹⁾		-		150		°C
T _{SHDN}	ritorinal offactorin						

Note1. This parameter is guaranteed by design and characterization.

Application Circuit 1



Application Information

Speaker+Receiver is always used in portable devices, and high voltage class D speaker driver (smart audio PA) is used to drive speaker in order to provide high audio volume. But the high output voltage of class D speaker driver will damage the receiver driver because receiver driver is designed using low voltage technology. The ET32121 can solve this design issue by providing the safe isolation between receiver driver and high voltage class D speaker driver. The ET32121 provides low R_{ON} channel to pass the positive and negative signals from capless receiver and smart audio PA. The circuit is shown in Figure 2.

Application Circuit 2



In order to improve audio performance of portable devices, external speaker power amplifier is always selected to replace the internal integrated speaker power amplifier. Because the audio signal quality of audio line-out or headset driver is better than the integrated speaker power amplifier, the audio signal of line-out or headset driver is selected as the high-performance audio signal source for external speaker power amplifier. High performance ET32121 is used as the 1-to-1 HiFi signal switch in this application. The circuit is shown in Figure 3, and a stable 3.3V power supply is required in this circuit.

Package Dimension

FCDFN-6 1.2mm×1.5mm



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Tape Information



Marking Information



Revision History and Checking Table

Version	ersion Date Revision Item I		Modifier	Function & Spec	Package & Tape	
				Checking	Checking	
0.0	2024-09-14	Initial Version	Wum	Wum	Liujy	
0.1	2024-12-27	Preliminary Version	Licx	Wum	Liujy	
0.2 2025-03-24	Update Typ Value	Liov	Wum	Liuiz		
	2025-05-24	& Tape & Marking	Licx	wum	Liujy	