ET518XXYB - High PSRR Low Noise 300mA LDO

General Description

The ET518XXYB family are the 300mA LDO with auto discharge function. It uses an advanced CMOS process and a PMOSFET pass device to achieve high power supply rejection ratio (PSRR), low noise, low dropout, low ground current, fast start-up and excellent output accuracy.

The ET518XXYB family are stable with a 1.0μF ceramic output capacitor, uses a precision voltage reference and feedback loop to achieve excellent Regulation and transient response.

The ET518XXYB family offered in a small DFN4 package, which are ideal for small form factor portable equipment.

The ET518XXYB family are available in standard fixed output voltages of 0.8V(ET51808YB),1.2V (ET51812YB),1.5V(ET51815YB),1.8V(ET51818YB),2.4V(ET51824YB),2.5V(ET51825YB),2.8V(ET51828YB), 2.85V (ET518285YB), 2.9V (ET51829YB), 3.0V (ET51830YB), 3.3V (ET51833YB) and etc.

Features

- Wide Input Voltage Range from 1.7V to 5.5V
- Up to 300mA Load Current
- Standard Fixed Output Voltage :0.8V,1.2V, 1.5V, 1.8V, 2.4V, 2.5V, 2.8V, 2.85V, 2.9V, 3.0V and 3.3V etc.
- Very Low I_Q is 45μA Typical
- Low Dropout is Typical 270mV@1.8V at 300mA Load
- Very High PSRR: 70dB at 1KHz
- Very Low Noise is 35uVrms
- Excellent Load / Line Transient Response
- With Auto Discharging Function
- Package Information:

Part No.	Package	MSL
ET518XXYB	DFN4 (1×1)	Level 1

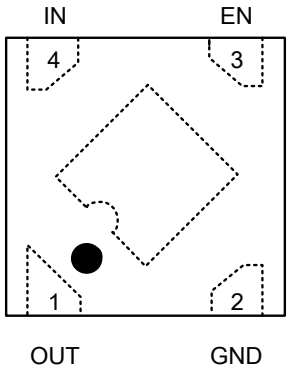
Applications

- Smart Phones and Cellular Phones
- Digital Still Cameras
- Portable Instrument

Mark Specification

Part No.	Marking	V_{OUT}	Auto Discharge Function
ET51808YB	XX	0.8V	Y
ET51812YB	XA	1.2V	Y
ET51815YB	XB	1.5V	Y
ET51818YB	XC	1.8V	Y
ET51824YB	XW	2.4V	Y
ET51825YB	XF	2.5V	Y
ET51828YB	XD	2.8V	Y
ET518285YB	XH	2.85V	Y
ET51829YB	XS	2.9V	Y
ET51830YB	XG	3.0V	Y
ET51833YB	XE	3.3V	Y

Pin Configuration

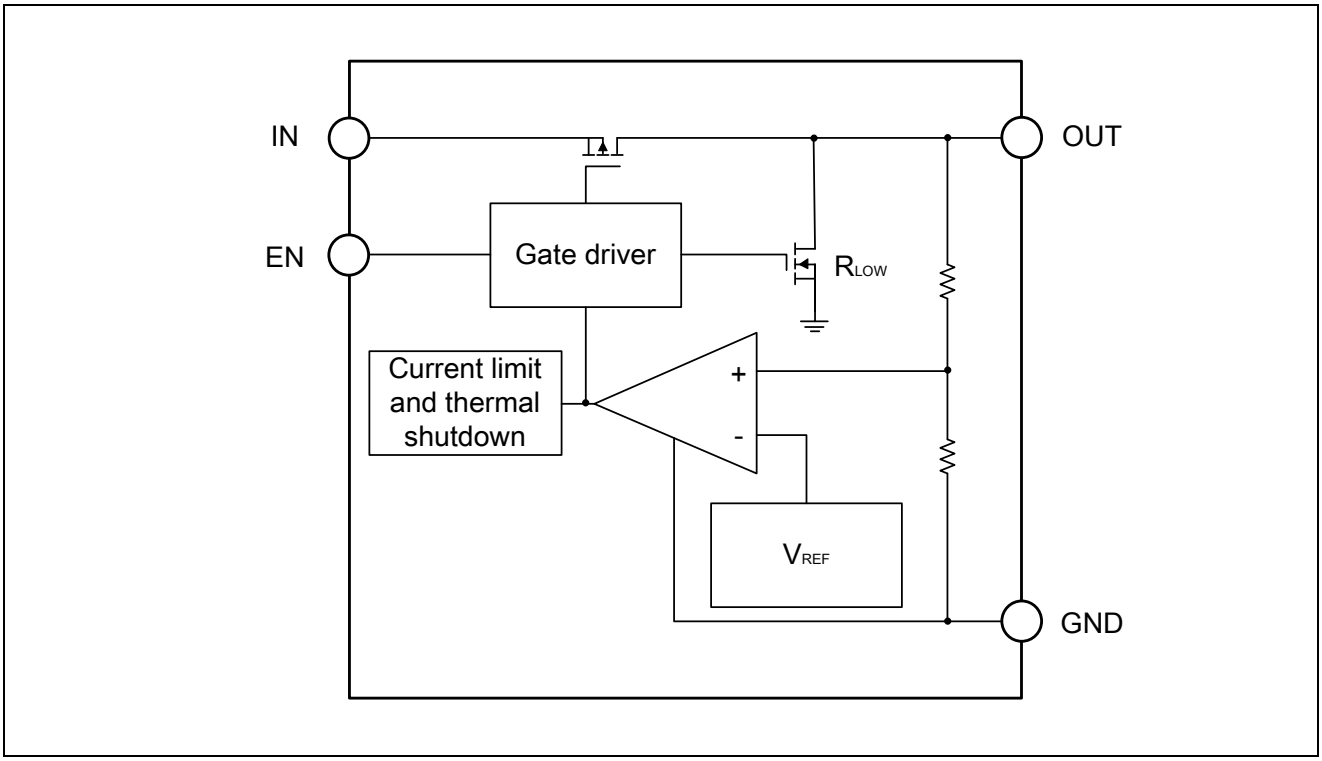


Top View

Pin Function

Pin No.	Pin Name	Pin Function
1	OUT	Output pin. A low-ESR capacitor should be connected to this pin to GND.
2	GND	Ground pin.
3	EN	Enable control input pin, active high. Do not leave EN floating
4	IN	Supply input pin. Must be closely decoupled to GND with a ceramic capacitor
-	Thermal Pad	Thermal pad for DFN4(1×1) package, connect to GND or leave floating. Do not connect to any potential other than GND

Block Diagram



Functional Description

Input Capacitor

A 1μF ceramic capacitor is recommended to connect between VIN and GND pins to decouple input power supply glitch and noise. The amount of the capacitance may be increased without limit. This input capacitor must be located as close as possible to the device to assure input stability and less noise. For PCB layout, a wide copper trace is required for both VIN and GND.

Output Capacitor

An output capacitor is required for the stability of the LDO. The recommended output capacitance is from 0.68μF to 4.7μF, Equivalent Series Resistance (ESR) is from 5mΩ to 100mΩ, and temperature characteristics is X7R or X5R. Higher capacitance values help to improve load/line transient response. The output capacitance may be increased to keep low undershoot/overshoot. Place output capacitor as close as possible to OUT and GND pins.

ON/OFF Input Operation

The ET518XXYB is turned on by setting the EN pin high, and is turned off by pulling it low. If this feature is not used, the EN pin should be tied to IN pin to keep the regulator output on at all time.

Ultra Fast Start-up

After enabled, the ET518XXYB is able to provide full power in as little as tens of microseconds, typically 80μs. This feature will help load circuitry move in and out of standby mode in real time, eventually extend battery life for mobile phones and other portable devices.

Current Limit Protection

When output current at the OUT pin is higher than current limit threshold or the OUT pin, the current limit protection will be triggered and clamp the output current to approximately 500mA to prevent over-current and to protect the regulator from damage due to overheating.

Thermal shutdown Protection

Thermal protection disables the output when the junction temperature rises to approximately +155°C, allowing the device to cool down. When the junction temperature reduces to approximately +130°C the output circuitry is enabled again. Depending on power dissipation, thermal resistance, and ambient temperature, the thermal protection circuit may cycle on and off. This cycling limits the heat dissipation of the regulator, protecting it from damage due to overheating.

Absolute Maximum Ratings

Symbol	Parameters (Items)	Value	Unit
V_{IN}	IN Voltage	-0.3 to 6.0	V
V_{EN}	Input Voltage (EN Pin)	-0.3 to V_{IN}+0.3	V
V_{OUT}	Output Voltage	-0.3 to V_{IN}+0.3	V
I_{MAX}	Maximum Load Current	300	mA
P_D	Maximum Power Consumption	400	mW
V_{ESD}	Human Body Model (JESD22-A114)	±4000	V
	Charged Device Model (JESD22-C101)	±1500	
$R_{\theta JA}$	Junction-to-ambient Thermal Resistance	250	°C/W
$R_{\psi jc}$(1)	Junction-to-case(bottom) Thermal Resistance	139	°C/W
T_J	Operating Junction Temperature	-40 to 150	°C
T_{STG}	Storage Temperature	-65 to 150	°C
T_{SL}	Lead Temperature (Soldering, 10 sec)	300	°C

***Note1*:** Test at T_A=25°C with the component mounted on 5*5mm, FR4, 2layer, Top and Bottom layer 1oz.

Recommended Operating Conditions

Symbol	Parameters	Rating	Unit
V_{IN}	Input Voltage	1.7 to 5.5	V
I_{OUT}	Output Current	0 to 300	mA
T_A	Operating Ambient Temperature	-40 to 85	ºC
C_{IN}	Effective Input Ceramic Capacitor Value	0.47 to 4.7	μF
C_{OUT}	Effective Output Ceramic Capacitor Value	0.68 to 4.7	μF
ESR	Input and Output Capacitor Equivalent Series Resistance (ESR)	5 to 100	mΩ

Electrical Characteristics

(V_{IN}=V_{OUT}+1V, V_{EN} =1.2V, I_{OUT}=1mA, C_{IN}=1μF, C_{OUT}=1μF, T_A=25°C, unless otherwise stated)

Symbol	Parameters	Conditions	Min	Typ	Max	Unit
V_{IN}	Input Voltage Range (2)		1.7		5.5	V
V_{DROP}	Dropout Voltage (3)	V_{OUT}=1.2V, I_{OUT}=300mA		460	700	mV
		V_{OUT}=1.8V, I_{OUT}=300mA		270	420	mV
		V_{OUT}=2.8V, I_{OUT}=300mA		230	345	mV
		V_{OUT}=3.0V, I_{OUT}=300mA		220	340	mV
		V_{OUT}=3.3V, I_{OUT}=300mA		210	330	mV
I_{Q_ON}	Input Quiescent Current	Active mode: V_{EN}=V_{IN}		45	80	μA
$I_{Q\ OFF}$	Input Shutdown Current	V_{EN}=0V		0.01	1	μA
V_{OUT}	Regulated Output Voltage	I_{OUT}=1mA, -40°C≤T_A≤85°C	-2		2	%
Reg_{LINE}	Output Line Regulation	V_{IN}=V_{OUT}+1V to 5.5V, I_{OUT}=10mA		0.03	0.2	%/V
Reg_{LOAD}	Output Load Regulation	I_{OUT} from 0mA to 300mA		20	40	mV
t_S	Soft-start Time	From enable to power on, V_{OUT}=1.8V		50		μs
I_{LIMIT}	Current Limit		300	650		mA
I_{SHORT}	Short Current Limit	V_{OUT}=0V		70		mA
PSRR	Power Supply Rejection Ratio(4)	f=1kHz, C_{OUT}=1μF, I_{OUT}=20mA		70		dB
		f=10kHz, C_{OUT}=1μF, I_{OUT}=20mA		65		
e_N	Output Noise(4)	10Hz to 100kHz, C_{OUT}=1μF		35		$μV_{RMS}$
V_{IL}	EN Low Threshold	V_{EN} Falling			0.4	V
V_{IH}	EN High Threshold	V_{EN} Rising	0.9			V
I_{EN}	EN Pin Input Current	V_{EN}=0V		0	0.1	μA
R_{PD}	EN pull-down Resistance		0.8	1	1.3	MΩ
R_{LOW}	Output Resistance of Auto Discharge at Off State	EN=0V, V_{IN}=4V, I_{OUT}=10mA		145		Ω
T_{TSD}	Over-temperature Shutdown Threshold(4)	T_J rising		155		°C
T_{HYS}	Over-temperature Shutdown Hysteresis(4)	T_J falling from shutdown		20		°C

Note2: Here V_{IN} means internal circuit can work normal. If V_{IN} <V_{OUT}, Output voltage follow V_N(I_{OUT}=1mA), circuit is safety. The maximum input voltage should take into account the maximum power consumption (P_D(max)). The minimum operating voltage is V_{IN}(min) = V_{OUT} +V_{DROP}(max).

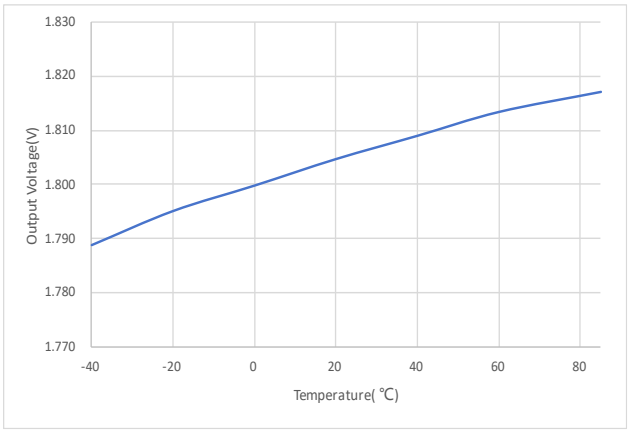
Note3. V_{DROP} FT test method: test the V_{OUT} voltage at V_{SET}+ $V_{DROPMAX}$ with 300mA output current.

Note4. Guaranteed by design and characterization. not a FT item.

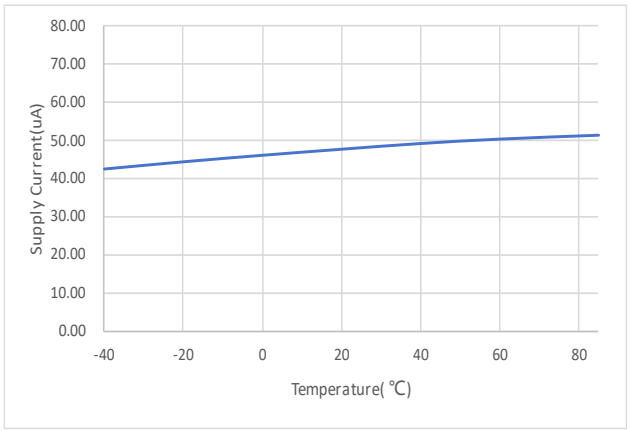
Typical Characteristics

(1) VOLTAGE VERSION 1.8V

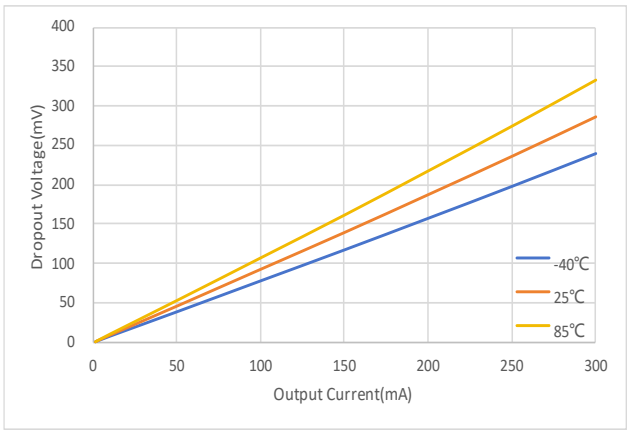
(V_{IN}=2.8V; I_{OUT}= 1mA, C_{IN}=C_{OUT} =1.0μF, unless otherwise noted. Typical values are at T_A=25°C.)



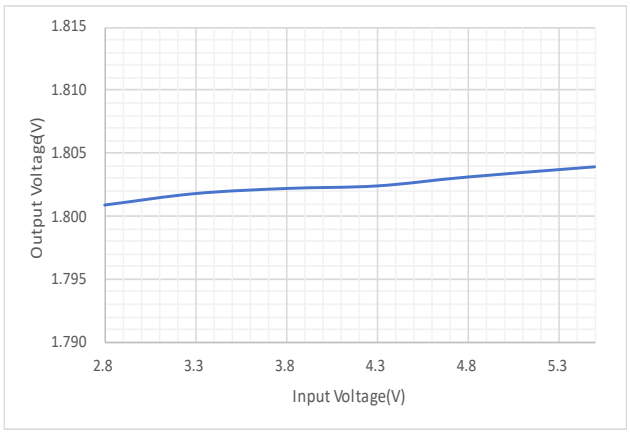
Output Voltage VS Temperature



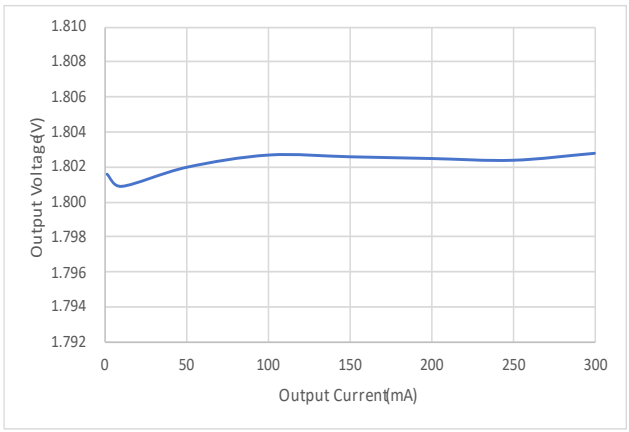
Supply Current VS Temperature



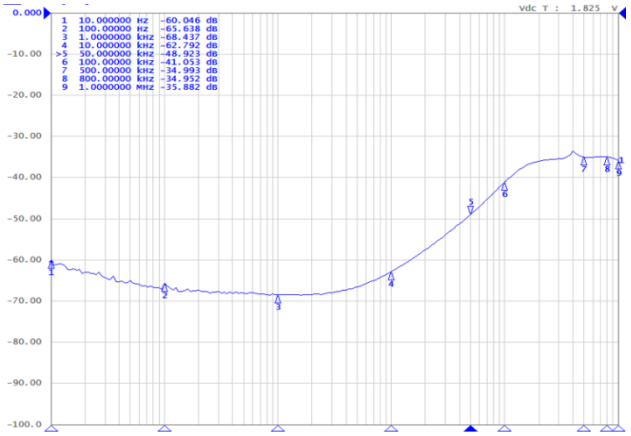
Dropout Voltage VS Output Current



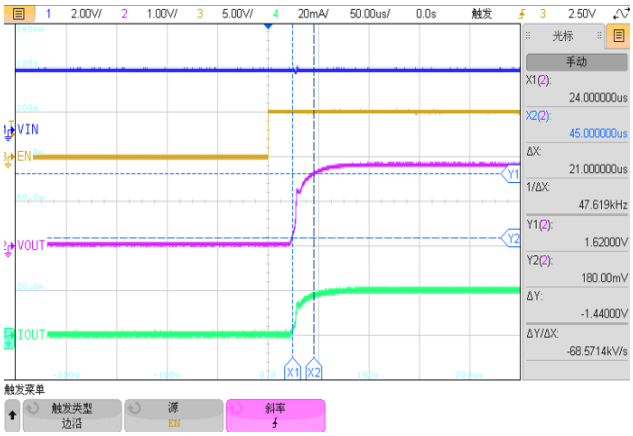
Output Voltage VS Input Voltage



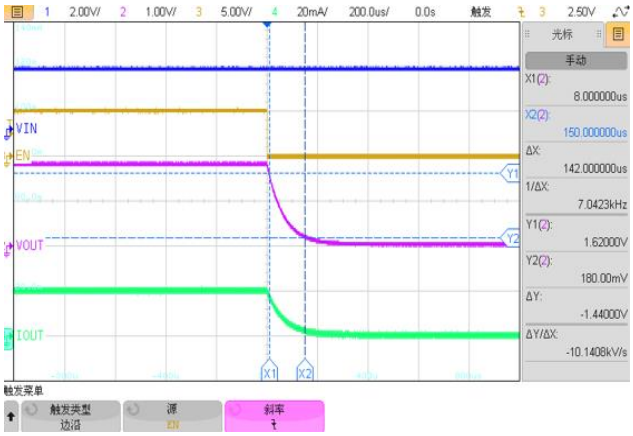
Output Voltage VS Output Current



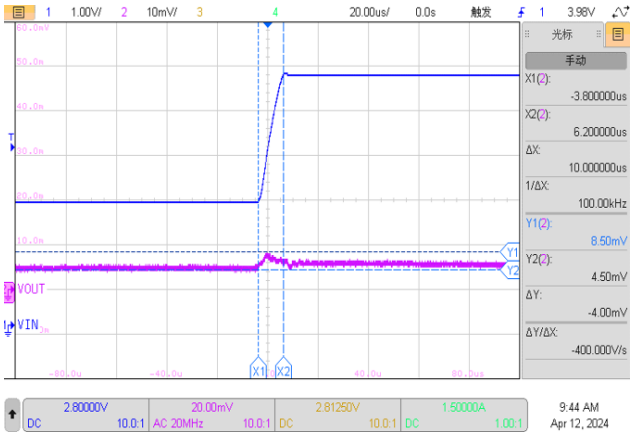
PSRR(I_{OUT}=20mA)



Turn On Speed VS EN Voltage (I_{OUT}=20mA)

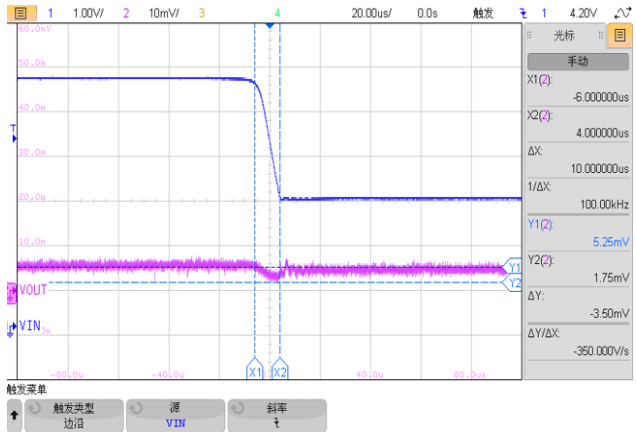


Turn Off Speed VS EN Voltage (I_{OUT}=20mA)



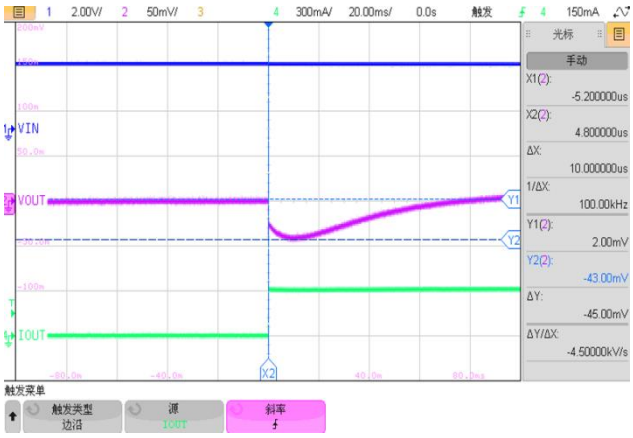
Line Transient Response

V_{IN}=2.8V~5.5V, V_{OUT}=1.8V, I_{OUT}=1mA



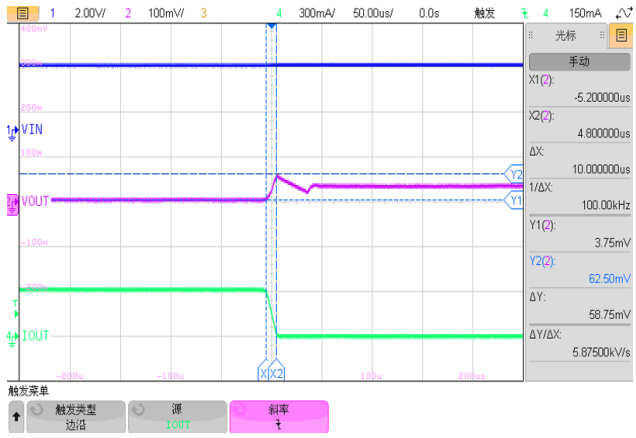
Line Transient Response

V_{IN}=5.5V~2.8V, V_{OUT}=1.8V, I_{OUT}=1mA



Load Transient Response

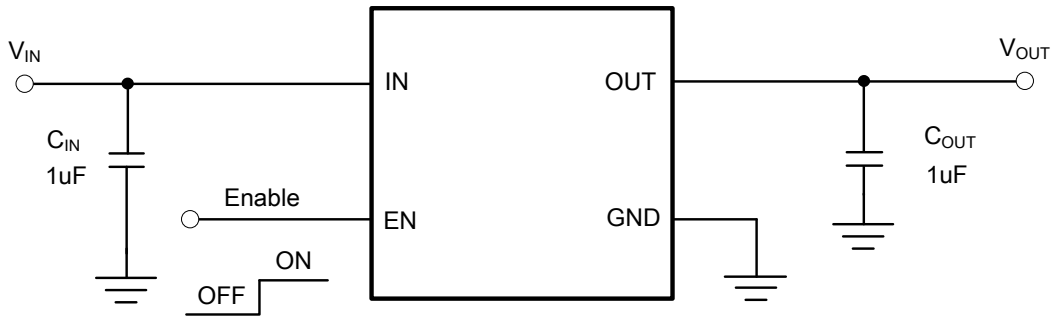
V_{IN}=2.8V, V_{OUT}=1.8V, I_{OUT}=1mA~300mA



Load Transient Response

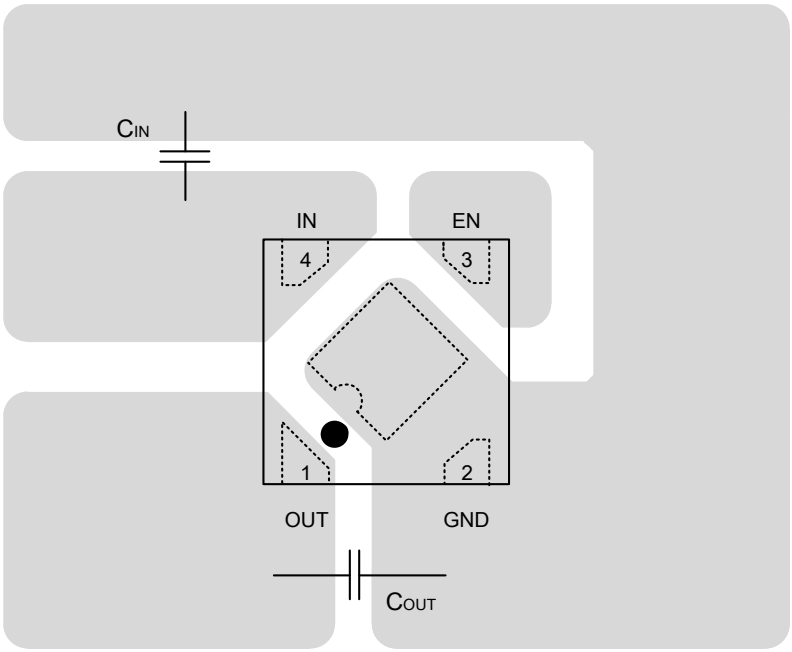
V_{IN}=2.8V, V_{OUT}=1.8V, I_{OUT}=300mA~1mA

Application Circuits



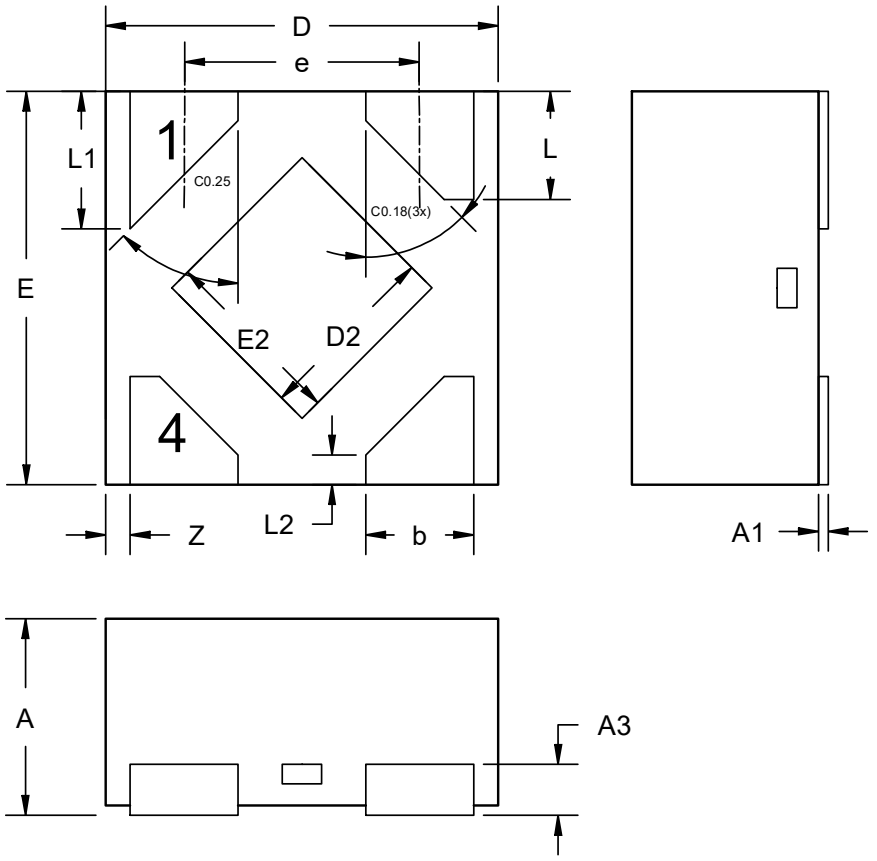
PCB Layout Guide

DFN4



Package Dimension

DFN4

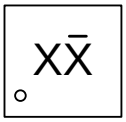


COMMON DIMENSIONS

(UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	NOM	MAX
A	0.30	0.37	0.40
A1	0.00	0.02	0.05
A3		0.127 REF	
D	0.95	1.00	1.05
D2	0.38	0.48	0.58
E	0.95	1.00	1.05
E2	0.38	0.48	0.58
b	0.20	0.25	0.30
L	0.20	0.25	0.30
L1	0.27	0.32	0.37
L2	0.02	0.07	0.12
Z		0.05 REF	
e		0.65 REF	

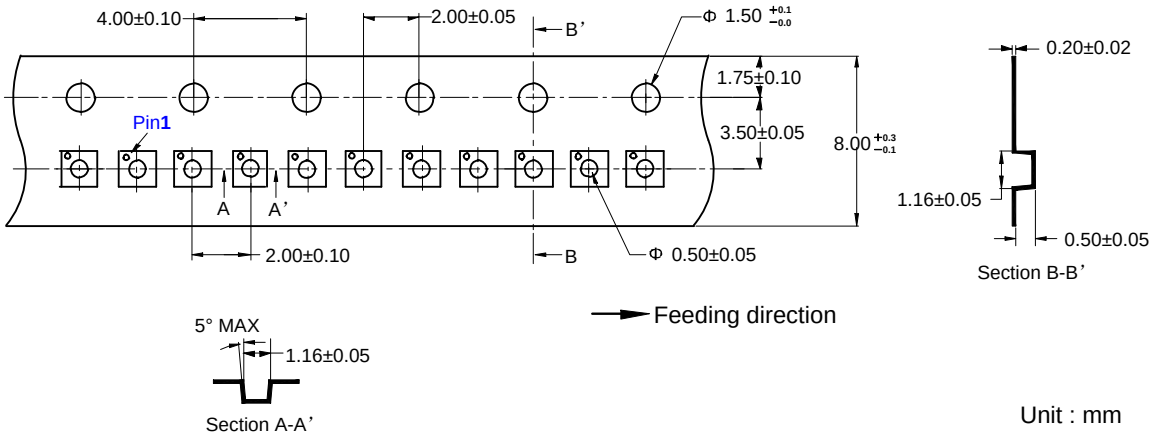
Marking



X(1) = Track Number

X(2) = V_{OUT} Version

Tape Information



Revision History and Checking Table

Version	Date	Revision Item	Modifier	Function & Spec Checking	Package & Tape Checking
0.0	2023-08-08	Original Version	Tugz	Liuxm	Liujy
1.0	2024-05-08	Official Version	Tugz	Liuxm	Liujy
1.1	2025-01-23	Update COUT range	Yangxiaoxu	Liuxm	Liujy